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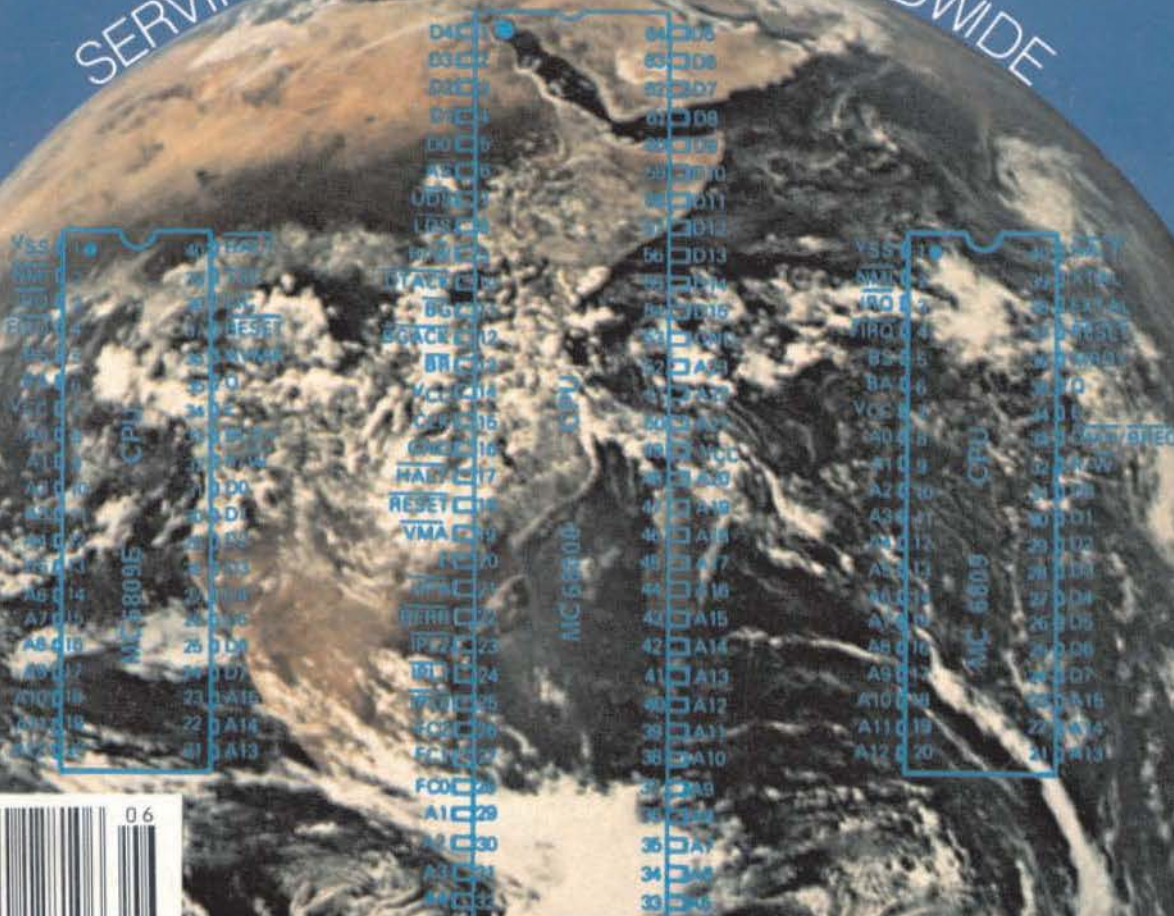
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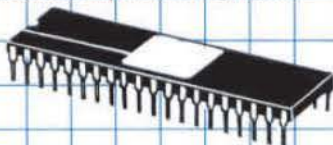
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MICROWARE'S OS-9 IS NUMBER ONE.

OS-9 NOW HAS THE LARGEST USER COMMUNITY

More users now run OS-9 on their 6809 computers than all other operating systems combined. This outstanding success story was no accident — it's due to OS-9's technical excellence backed up by outstanding Microware support. OS-9's Unix-type architecture and totally modular design gives your computer more power and versatility. OS-9 also gives you more possibilities for customization so you can tailor your system exactly to your needs. And aren't flexibility and performance the reasons you chose a 6809 computer to begin with?



OS-9 HAS BEEN CHOSEN BY OVER 50 6809 SYSTEM MANUFACTURERS

OS-9 is now offered as a standard operating system by almost every 6809 system manufacturer, and has been designed into an amazing variety of dedicated systems and products including personal and business computers, process control systems, data and telecommunications systems, and more. In all, over 50 companies and organizations have

obtained OS-9 distribution licenses including such well-known names such as General Motors, NASA, Fujitsu, Western Electric, Motorola, Sykes Datatronics, Eastman Kodak, Thomson-CSF, and Tandy Corp.

OS-9 GIVES YOU A SOFTWARE BASE TO BUILD ON

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Even when you have the best software and documentation, there can be times when you need questions answered. That's why Microware is committed to giving OS-9 users the best possible personalized service. Here are some

of the ways we deliver solid support:

- A Software Support Hotline for direct access to our technical staff
- "Pipelines", our free quarterly newsletter
- OS-9 User Seminars, the annual OS-9 community gathering
- a liberal update policy for new releases

Microware does business on a person-to-person basis. When you call you'll find yourself speaking with someone who's both knowledgeable and genuinely interested in helping.

YOU CAN COUNT ON OS-9 NOW AND IN THE FUTURE

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MICROWARE

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FOREIGN

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Articles submitted for publication should be accompanied by the authors full name, address, date and telephone number. It is preferred that articles be submitted on either 5 or 8 inch diskette in TSC Editor format or STYLO format. All diskettes will be returned.

The following TSC Text Processor commands ONLY should be used (due to our proportional processor): .sp space, .pp paragraph, .fl fill and .nf no fill. Also please do not format within the text with multiple spaces. The rest we will enter at time of editing.

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All articles submitted on diskettes should be in TSC FLEX™ format, either FLEX2 6800, or FLEX9 6809 any version.

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All letters to the editor should also comply with the above and bear a signature. Letters of 'gripes' as well as 'praise' are solicited. We attempt to publish all letters to the editor verbatim, however, we reserve the right to reject any submission for lack of 'good taste'. We reserve the right to define what constitutes 'good taste'.

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By: **Ronald W. Anderson**
As published in 68 MICRO JOURNAL™

The publishers of 68 MICRO JOURNAL are proud to announce the publication of Ron Anderson's **FLEX USER NOTES**, in book form. This popular monthly column has been a regular feature in 68 MICRO JOURNAL SINCE 1979. It has earned the respect of thousands of 68 MICRO JOURNAL readers over the years. In fact, Ron's column has been described as the 'Bible' for 68XX users, by some of the world's leading microprocessor professionals. Now all his columns are being published, in whole, as the most needed and popular 68XX book available. Over the years Ron's column has been one of the most popular in 68 MICRO JOURNAL. And of course 68 MICRO JOURNAL is the most popular 68XX magazine published.

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M.C2
PRINT.C3
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SCIPKG.C1
U.C4
PRINT.C4
SET.C5
SETBAS1.C5

File load program to offset memory — ASM PIC
Memory move program — ASM PIC
Printer dump program — uses LOGO — ASM PIC
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Modem input to disk (or other port input to disk) — ASM
Output a file to modem (or another port) — ASM
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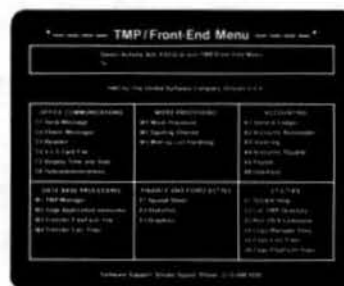


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Flex User Notes

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Reader Response

My "Printer Pal" Art Weller wrote me after seeing last month's column (sneak preview) and I will quote his interesting and objective comments directly.

"I just finished reading the latest in the 'Anderson - Farnsworth' Feud' (Just joking -- I realize it's a friendly argument) and decided to put my two cents worth in. First of all, I conclude that neither one of you can win for several reasons; (1) the ground rules haven't been established so that it can be determined when a 'win' has taken place, and (2) It's becoming clear that neither is going to be able to convince the other.

"There are compilers and there are compilers. You and I have talked about this before and agreed that people who design and develop compilers do so with some rather specific objectives in mind. Pascal as an aid to teaching structure, Cobol for business oriented applications, etc., etc. As a result, we agree, there is no one 'best' compiler that will be optimum for whatever application may come along. Further, when considering a specific compiler for some specific application, the compiler 'loses points' to the extent that the programmer must write his own function (paraphrasing an Anderson comment about Forth). As you know, I'm very much impressed with the ease and clarity of Pascal programs (when compared to, say, BASIC), but Pascal is a rather poor choice for an application that involves a lot of high speed string manipulation." (NOTE: Al Jost of Dynasoft and I would probably debate that point. RWA) "I think you see where this logic is leading and I will quit here so I can get to the point.

"The point is that I really have to sympathize with Dan because there's no way to pin Ron down. If Dan makes some really telling point in the argument, Ron can respond instantly by switching to a different compiler that happens to have been optimized to handle that particular problem.

"On the other hand, I can't help wonder if Dan realizes that what he has really done is to devise a rather specialized 'compiler' that just happens to have been tailored to his needs -- no wonder he's so happy with it. No matter that the interface with the user is crude, or that the language is not standardized, consisting mostly of library calls or that an assembler is used to interface with it -- it is performing the same functions for him that a more standardized HLL is performing for me. The statement that reveals this is 'Let us not compare primitive assembly language to HLL's when I am advocating a much more sophisticated approach.' I translate that to mean that Dan is not advocating assembler language, but prefers something more sophisticated, eg. a HLL!

"Switching sides quickly, I have to point out that when Ron is evaluating a compiler, one of the criteria is the ease (or lack thereof) of introducing assembler language operations into the flow of the program. And of course this ranges from BASIC's clumsy POKEing of decimal numbers (ugh!) to the use of actual assembler mnemonics in the program listing." (RWA NOTE: as in "C") That is, despite the sophistication of the best available HLL's, there are still times when good old tedious assembler is the way to go.

"This brings me to the comment by Dr. Franz (making sure not to miss offending everyone!). Gee, I wish he hadn't used the word 'death'. It's true that 'main frame' computer programming evolved from 'machine code' to the present HLL, and 'better' languages are still being devised. But it should be noted that machine code programming of main frame computers was at a time when computing 'power' was on the order of (or even less than) your KIM-1 and the evolution of more sophisticated programming techniques paralleled hardware complexity vs cost. That evolution is still going on and just happens to include micros, but I really doubt that we'll see the complete demise of assembler programming or machine code, for that matter. (e.g. 'MOCONTROL = \$3;' -- Isn't the \$3 value a 'machine code instruction' for the 6850 serial interface? Probably the FDC1771 disk controller would provide much better examples).

"If you don't mind my restating the point I tried to make in an earlier letter, let me tell you about an auto oil change. I usually do it myself, but sometimes weather doesn't permit so I go to a service station and on one occasion, while waiting, I happened to glance at my car on the lift just in time to see the 'mechanic' about to remove the drain plug with a pipe wrench! Of course that would ruin the plug so that the proper wrench wouldn't fit -- I actually screamed at him. Just as any mechanic, worth his salt, should have the proper tools and use them appropriately; it seems to me that, likewise, a competent programmer will also insist on a complete toolbox and carefully select the right language suitable to the application he is working on."

Gee, I think I ought to let Art write this column for a while. Thanks, Art for the objective viewpoint.

Dan Farnsworth's Reply

"I would like to compare the operation of an assembler to the operation of an automobile with a 4 speed transmission. Previous to your last column you could only think of assembler operations in low gear. However, last month you discovered Macro's and got yourself shifted into 2nd gear and showed some pretty good examples. The following will demonstrate the assembler working in high gear with Library routines."

(NOTE: I'm going to cry FOUL here. I didn't just discover Macros last month... RWA)

"I have a Floating Point BCD Math Package and from this I have extracted the Multiply routine (FPMULBCD). To solve the weight of the steel cylinder problem from last month, I would code the following: (SEE LISTING BCDWGT)

"This assembles into 347 bytes of code, about 1/3 of your code from last month and 1/6 of the code generated by PL/9."

FOUL FOUL FOUL

I was very careful to state that in order to compare oranges to oranges, I was including the four functions, not just the multiply!!!! (RWA)

"The answer is correct to .001 ounce and the execution time is 14 Ms. If you want a nice formatted output then another 300 bytes would be required and an input routine would add 140 bytes. This Floating Point Package has a range of numbers from plus or minus 10^{-64} to 10^{63} with 8 digits of accuracy on Add and Subtract, but drops to 6 digits for Mult & div. More or less accuracy can be changed by setting the PC to any even number.

"Now let us shift into overdrive. I have in my computer an Eprom which contains a 10 digit bcd fixed point mathpack. This resides at \$E800 and it is always loaded and ready to go. The coding is as follows: (SEE LISTING FPBCD).

"This assembles to 40 bytes of code and 25 bytes of storage or 65 bytes total. The accuracy is 1/2 ounce and it takes 8 ms to run. If you want a formatted output, change the JSR MOVTOT to JSR MOPPRTO (move total and print it). If an input is needed then a call to GETNUM will put the number in the NUMBER buffer. If you need more accuracy then everything could be scaled by a factor of 10 or 100. If you wanted to make 100 calculations it would take from 10 to 12 bytes of code to fetch and store the radius and height in straight line code (a loop would be much less) making 1500 bytes compared to your 7.4K in PL/9.

"Ron, I don't think anyone would argue that assembled programs will run faster and use less memory than their equivalents written in an HLL. Our discussion has been "Is the effort worth the trouble". It is quite obvious that if you have to write in an area you have never done before and probably will never do again an HLL is the way to go. If you are going to write several programs in the same area and you take the trouble to develop a library of subroutines or even better a run-time package, you will find that assembler is a "piece of cake."

"Last month I promised to include the source code for GETNUM an input routine for my BCD mathpack. (SEE GETNUM LISTING). Next month I will include PRBCD, a routine that will output a very nicely formatted number with leading zeros suppressed, commas inserted at the right places, and the decimal points lined up.

My Reply

I guess we are not very far apart at this point, Dan. Your assembler routines certainly look like my assembler routines. I was careful to make all my comparisons last time on the basis of having four functions and I/O included in the byte count, and your results are not spectacularly different than mine. I ran the calculation in the PL/9 program, and it ran (I am assuming your 8ms and 14ms are for a 2 mhz system) in 1.75 ms. You see, the binary math packages are QUITE a bit faster than the BCD. For money calculations with zero rounding errors, I agree the BCD is better. For scientific calculations where the input data may be a couple decimal places less accurate than the math package, I really don't care if I am off by one or two in the last place due to rounding errors.

Perhaps that result will once and for all kill the idea that compilers have to generate code that is slow to execute! If you write and tell me that your times are for 1 Mhz, then the PL/9 version is still more than twice as fast as your admittedly limited accuracy fixed point calculation, and four times as fast as the floating point BCD with the same accuracy as the PL/9 version.

It seems as though this debate has reached the point where there is little left to say. Your last paragraph seems to sum up what you have been saying. I can sum up my argument by saying that for the type of programming I do most of the time, a penalty of 2 in the size of the binary output code and little or no speed penalty, is a tiny price to pay for the ease of writing my programs in a higher level language. With EPROMS at \$4 or so for a 2732, one of the instruments I design and program will cost an extra \$8 in hardware, and several hundred dollars

less in my programming time. That's a pretty good trade off. It wouldn't be so good if we were producing 10000 identical units, which would then cost an extra \$80,000 in parts to save several hundred in my time. That has been my point all along.

Permit me just one more illustration. How many SS-50 computers are there out there? They continue to be sold, and I suppose now there might be some 15,000 around. Do you think any one piece of software (say a screen editor, for example, for \$200) will ever sell more than about 1000 copies? If it does, those sales will be spread over a couple of years, what we would call the "life" of the product. Over that time, the company that wrote the software has to support it, advertise it, make copies, print manuals, pay postage, and pay a portion of the company's expenses for overhead (heat light, phone, rent). If the distributor sells it for \$200, the supplier most likely wholesales it for about \$130 or so. The cost of writing the software in assembler could easily hit 30% of the total revenue. Writing it in a higher level language could easily reduce that by a very substantial amount, perhaps to 10% of the total revenue. That might easily make the difference between a profit and a loss on that item.

When we talk about 10,000 or 100,000 total sales, there is little doubt that Assembler is the way to go. Somewhere in that ballpark of 1000 however, the choice has to be to go with the HLL for any company that wants to stay in business.

Though I think the discussion has run its course, I'd be happy to publish further pieces of Dan's math package if he is willing to share them with us.

New Compiler

Frank Hoffman, the author of CRASMB, the cross assembler(s) and several other useful software products, is putting the finishing touches on his K-BASIC compiler at the time of this writing. I've known Frank for some time as a result of his sending me CRASMB for a review a couple years ago. A couple of months ago he asked me if I would write the scientific functions for his package. We agreed on a reasonable fee (this is my second software for profit venture) and I have just completed and tested the functions, and integrated them into his runtime package. I think in view of the circumstances someone else ought to do a full review after the package is released, but I do want to mention it, and indicate that it is very nearly compatible with TSC Extended BASIC at the source level. I have compiled several BASIC programs with minimal changes, and had them run correctly. The advantage, of course, of having a compiler that is compatible with an interpreter, is that the program may be developed using the interpreter, and then compiled after it is working. Suppliers of software written in BASIC, such as business software, Database systems, etc. will be able to avoid the frequent "chaining" of programs since the code is smaller and more can be held in memory at one time. Also the code will be harder for the user to "optimize" thus reducing problems for the supplier.

This was a "first test version" and of course I found a few problems in it, but they were all relatively simple to fix. I was impressed with the modularity of the runtime package, and I was able to code some of the standard scientific functions with little trouble using the stack for "scratchpad" memory for the required calculations. I think this is going to be a winner. Watch for a review of it soon.

Program Correctness Again

I've just been through a couple of long processes in getting a bug free version of a compiler or two. This discussion is not an attempt to pick on anyone in particular, and in fact the reason for it is that problems such as I am going to discuss are nearly universal among software suppliers. The problem is simply that of too many bugs in officially "released" software, and too long a time in getting them all removed.

I once received a "C" compiler with float and long capability. I found that I couldn't input a negative number via `scanf()`, the sign was simply discarded. `printf()` would output a negative number but it wouldn't be formatted as specified. The logical comparisons for long integers didn't work in general and the ones that did, didn't work in logical combinations with and or, since true returned as 1 in one case and -128 in another case. Anding these together could never result in a non zero value. I found that the basic arithmetic didn't work. Doing a long integer multiply, multiples of 65536 were lost. The real arithmetic had problems too. For certain specific numbers, the result returned was zero. The supplier had implemented successive subtraction as a method for divide for integer numbers. Dividing 32767 by 2, for example, required 16384 passes through a loop, and took half a second!

Not all of the software suppliers and writers have problems that are that obvious. I've been going around for seven months with a supplier who, all this time has been advertizing his product and selling it. I have yet to be able successfully to compile the first program I tried to compile 7 months ago. As each problem was uncovered and reported, that problem was fixed (but not related ones). First try, the initializer feature didn't work for floats. Second try, it worked for positive valued of floats but not negative. Third try it wouldn't initialize an array of type float of static storage class.... It seemed that each time the author would only go so far as to fix the specifically reported problem and go no further into checking even within the same feature to see if any more problems were present. Currently I know of only one remining problem, and it is not a very serious one. The supplier in question has been very good about supplying updates to customers and in being able to say that "all KNOWN bugs are fixed" at any point in time.

Understand that I am not talking here about suppliers who send out preliminary or pre-release versions of their software for testing, but releases that are advertized as being finished and free of known bugs. (Of course if you don't test something very well, you don't know about the bugs, do you?)

The problem doesn't seem to be one of company size. Some of the smallest suppliers have a better track record than some of the bigger ones. One of these recently released a new version of their product that had undergone three months of in house testing. Yes, there were a few bugs in the release version, but the supplier had them all corrected within a couple of weeks, and an update mailed very quickly.

I have next to me a letter to a supplier half way around the world from Ann Arbor MI. He sent me a new version of his compiler last January with REAL variable capability for the first time. Very little of that capability worked properly, past the basic four functions. I sent half a dozen simple test programs that each illustrated a problem. Two months later I received another version with the

problems I had reported all repaired. I went at a program using the capabilities of the compiler and I got about 10 lines farther than I did the first time and found another serious problem. Air mail takes two months round trip to this supplier. It is now the end of March. By the end of May, approximately, I can expect to receive another update for trial. If you were that supplier, and seriously interested in selling a product in the near future, wouldn't you try a little harder to test your product comprehensively? (Or as I have suggested, find a tester a little closer to home)

Now for a lecture to you suppliers... You MUST test every function of your product. If it is a compiler and it has long integer variables, multiplying 3 by 4 and getting 12 back is NOT a test of the long multiply!! Such multiply routines frequently show problems at the boundary between half and full length. Try $65535 * 32768$ for a result that is nearly the maximum representable number. Try positive and negative numbers in all possible combinations. Try ZERO. If you have float, double, short, integer, and long, you have to try the logical comparisons for ALL of them. You can't argue "Well the integer comparisons worked and the long are patterned after them, so I thought I didn't have to test them." You say all these tests will cost you more than you can stand to pay out to your programmers, or delay cash income longer than you can afford? Then raise the price of your product so the return will be greater when you get the product on the market. Most of your customers would rather pay an additional \$50 or \$100 for a good piece of software, and know that they are not going to have to fool around for 6 or 8 months before all the bugs are out.

If you reduce the bugs to absolute minimum before shipping product, you will have minimal expense repairing the remaining few bugs, and better yet, you'll have instilled some confidence in your customers, who will be glad to purchase your next product.

Now, may I say something in defense of the software suppliers. (I am on that side of the fence too, with my JUST text processor). A few years ago, we microcomputer users were happy to have a line editor such as TSC EDIT, and elated when something as fancy as the text processor PR came along. Those are both very good software, and this is not intended to knock them in any way. They had few bugs when they were introduced, and they served us well for a long time. Look, however, at the relative complexity of some of the software about which I have been talking here. A Pascal or "C" compiler is certainly many times more complex than a line editor. While it might be relatively easy to test something in the way of "application software" such as a text processor, no supplier can envision what users are going to do with a compiler. The vast majority of them certainly try to test the product reasonably thoroughly, but perhaps they don't go as far as a quick run through Kernighan and Ritchie to develop a check list of what features should run in their compiler (read the equivalent documentation's name here for other compilers). Then next week someone will try a peculiar construct that is legal in the language, but perhaps little used, and some little detail will cause it to fail.

Some of the software suppliers have developed a list of users who tend to test the software thoroughly, perhaps only because of the applications for which they use it, and those suppliers frequently send out "pre-release" versions for "field tests". I can truthfully say that I have never received a piece of software for such a test and not found a bug or two nearly immediately. Of

course that is the idea of the test, and the suppliers gratefully supply the testers with upgrades and updates until the final product is released. I've found just about ALL the suppliers to be VERY much interested in removing all the bugs they can, and as quickly as possible. Think for a moment about the software supplier's position. The sooner he gets all the bugs out of his product, the less revisions he will have to mail out to his customers. Mailing new disks and revised manuals is a costly operation that can make the slim profits disappear and even turn them into a loss.

If you purchase new software and find a bug, have patience with the supplier for a reasonable amount of time. Perhaps 50 other users found other bugs and he is trying very hard to remove them all before sending out updates. If you find a supplier very unresponsive, both '68' Micro Journal and I would like to hear about it. Either we can help by getting the communications established better, or perhaps we can help directly, should the problem turn out to be one of misunderstanding the instruction manual or the purpose for which the software was intended.

This note is being appended a week later... At the time I wrote (and rewrote and rewrote) the above, I was very discouraged because of a long series of "buggy compilers" that have come to me all at once. I don't mean to sound "harpy" or to repeat myself endlessly on the subject of testing and correctness, so I sound like a broken record. I do think some of the suppliers don't try hard enough to test their wares before they sell them. Software projects nearly always take longer than estimated and a small company must run out of capital eventually, and have to get some product shipped to stay alive.

If anything ever kills the SS-50 bus, it will simply be the fact that the software market is too small and the suppliers can't make it on sales of their wares.

NAM BCDWGT

```
PC      EQU 8
WARMS   EQU $CD03

PI      FCB $41,$31,$41,$59,00
RHO     FCB $41,$45,$28,00,00 DENSITY OF STEEL
HEIGHT  FCB $41,$68,$50,00,00 6.85 INCHES
RADIUS  FCB $41,$27,$50,00,00 2.75 INCHES
WEIGHT  FCB 0,0,0,0,0 PLACE FOR RESULT IN OUNCES

BEGIN   LDX #RADIUS
        LDY #TOTAL
        JSR FPMOV MOVE RADIUS TO TOTAL
        LDX #RADIUS
        BSR MULMOV SQUARE RADIUS
        LDX #HEIGHT
        BSR MULMOV MULT BY HEIGHT
        LDX #RHO
        BSR MULMOV MULT BY DENSITY
        LDX #PI
        BSR MULMOV MULT BY PI
        LDY #WEIGHT
        JSR FPMOV MOVE TOTAL TO WEIGHT
        JMP WARMS
```

LIB FPMULBCD.SRC

END BEGIN

NAM FPMCD

```
PI      FCB 0,0,0,3,$14 PI
RHO     FCB 0,0,0,4,$53 DENSITY

HEIGHT  FCB 0,0,0,6,$85 INCHES
RADIUS  FCB 0,0,0,4,$35 INCHES
WEIGHT  FCB 0,0,0,0,0 OUNCES

BEGIN   LDX #RADIUS
        JSR MOVINT MOVE RADIUS TO TOTAL
        LDX #RADIUS
        BSR MOVML SQUARE RADIUS
        LDX #HEIGHT
        BSR MOVML MULT BY HEIGHT
        LDX #RHO
        BSR MOVML MULT BY DENSITY
        LDX #PI
        BSR MOVML MULT BY PI
        LDX #WEIGHT
        JSR MOVTOT MOVE TOTAL TO WEIGHT
        JMP WARMS
```

* SUBR TO MOVE (X) TO MULBUF AND MULTIPLY
* TOTAL BY MULBUF WITH RESULTS IN TOTAL

```
MOVMUL  JSR MOVINT MOVE TO MULBUF
        JMP MULBCD MULTIPLY
```

END BEGIN

TTL GETNUM

* SUBR TO INPUT A 10 DIGIT BCD NUMBER
* C/R = TERMINATION - COMMA IS IGNORED
* ONLY TWO DIGITS ALLOWED AFTER DP
* ESC RETURN WITH V SET
* EXIT WITH X POINTING TO NUMBER, USES A & B

```
GETNUM  LDX #NUMBER POINT TO DESTINATION
        CLR DP DECIMAL POINT SW = 256
        JSR CLRBUF CLEAR NUMBER BUFFER
        LDX #NUMBER POINT TO NUMBER
        JSR INPUT INPUT CHAR (NO ECHO)
        CMPA #10 (ESC) ABORT INPUT?
        BEQ GNUMX YES, RETURN
        CMPA #0D TERMINATE ENTRY?
        BEQ GNUM4 YES, ADD DECIMAL DIGITS
        CMPA #39 NUMERIC?
        BNE GNUM3 NO, RE-ENTER
        CMPA #2C NUMERIC? /, -, OR DP
        BMI GNUM3 NO, RE-ENTER
        JSR OUTT YES, ECHO CHARACTER TO TERMINAL
        CMPA #2C <,>?
        BEQ GNUM1 IGNORE COMMA
        SUBA #30 MAKE IT BINARY
        BPL GNUM2 NOT A DP
        LOA #2 ALLOW ONLY 2 MORE DIGITS
```

```

      STA DP          SET COUNT
      BRA GNUM1       CONTINUE
GNUM2 BSR TRUCK4      ROTATE NUMBER 4 BYTES LEFT
      ORA 4,X          ADD LSB TO BINARY INPUT
      STA 4,X          SAVE IT
      DEC DP          DONE?
      BNE GNUM1       NO, CONTINUE
GNUM1 RTS
GNUM3 LDX #REENTER   MESSAGE TO RE-ENTER NUMBER
      JSR PDATAS      PRINT MESSAGE
      BRA GETNUM
GNUM4 LDA DP          HOW MANY DECIMAL ZEROES TO ENTER
      CMPA #1         ONE?
      BEQ TRUCK4      YES
      BSR TRUCK4      ENTER TWO DECIMAL ZEROES

```

• SUBR TO ROTATE LEFT

```

TRUCK4 LDB #4         NUMBER OF TIMES TO ROTATE
TRUCK  ASL 4,X
      ROL 3,X
      ROL 2,X
      ROL 1,X
      ROL 0,X
      DECB
      BNE TRUCK
      RTS

```

OS9 USER NOTES

By: Peter Dibble
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The OS-9 Seminar, OFlex, New Manuals
Some C Functions and Other Stuff

I went to the OS-9 users seminar last summer, so did almost every person I've heard of in the OS-9 community. It was interesting walking through the exhibit hall and listening to the speakers. The thing that makes me willing to go halfway across the country to take part in the seminar this summer is the fun I had last year talking with other OS-9 people. Most of us, myself included, spend our lives in a world where every other microcomputer user thinks the world ends right past PC-DOS and CPM. Last summer I fairly wallowed in the pleasure of being with hundreds of people who shared my interest in OS-9. We argued, agreed, complained, puzzled, and applauded about things that are dear to OS-9 users (and not many others).

If you need a practical reason to spend a long weekend in Des Moines, bring a question with you. If you have been itching to show the person on the Microware hotline a problem that he can't reproduce, he'll be there. Go demonstrate the problem yourself. If you want to suggest that OS-9 badly needs a WALL command you can probably find someone important and back him into a corner about it.

All the important vendors were there last year -- I assume they'll be back. If they come, you'll be able to check the Smoke Signal version of OS-9 for compatibility with other versions. Try a few things on the GIMIX III. I hope Privac comes again;

their graphics board is much more impressive in motion than in an advertisement. I imagine there'll be a bunch of new vendors there showing CoCo products.

The vendors and Microware staff notwithstanding, the best place to look for answers will be standing or sitting beside you (very likely at breakfast or some other improbable time). Last year I found the other users at the Seminar a mine of useful information. If you are a vendor, go to the Seminar even if you don't have a booth. It is a great place to test the water.

The Seminar is a businesslike affair, but it is also something of a party: Jeanne Kaplan's party. Everyone who has dealt with Microware for any length of time knows that Jeanne is a consummate organizer. Last year everything ticked along smoothly despite the fact that she must have been slowed down a little by the child she was about to have. Last year Microware hosted a banquet and a fancy brunch. The Governor of Iowa came and gave us a little talk over dinner. Ken Kaplan handed out prizes to individuals who had made particularly distinguished contributions to the OS-9 community. At the brunch more prizes were handed out. I wonder what is in store for us this year.

Microware is going to give the Users Group some software for a raffle. I don't know just how it will be organized yet, but the plan is to hand the prizes out at the Sunday morning brunch.

Last year we heard a lot about the new 68000 version of OS-9. This year we may be able to see one in action. That's not official from Microware, but there are signs that it may be ready.

I guess it sounds like I'm advertising the Seminar. I suppose I am. I wouldn't miss it for the world, and I hope I'll see you there.

OFlex

Just today I received a copy of OFlex. This program runs Flex as a process in an OS-9 Level Two system. I'm afraid it's been too long since I used Flex with any regularity for me to give the program a good workout. Still, I ran a few Flex programs and checked out the interface to OS-9.

I remembered from "The Soul of a New Machine" that Adventure was an important test used on new hardware. I have a version of Adventure which runs under Flex, so I ran through a dozen rooms or so with it and grabbed two or three treasures ... no problem. I compiled a Pascal program using the TSC Pascal compiler with no difficulties except some trouble remembering how to use Flex.

Part of the OFlex package is a program called XCOPY that runs under OFlex. XCOPY can copy from OS-9 files to Flex files and back. I tried every combination I could think of and couldn't make it fail. That brings up the one important failing I could find in OFlex; there is no FORMAT utility. I guess FORMAT is too near the hardware to run in what amounts to a virtual machine.

OFlex can read and write Flex disks. It can also format files on an OS-9 disk so the files can be treated as Flex disks by OFlex. The files are accessed through a command called ASNDISK. Using ASNDISK, files can be associated with each disk number (1 through 4). This is a useful feature for Flex. I shudder to think of the problem it would be dealing with a hard disk full of Flex files. With OFlex the hard disk can be broken up into many

smaller virtual disks giving manageable bunches of files to work with.

OFlex isn't reentrant. This is sad, but, as I remember it, many Flex programs change flags and pointers inside Flex. Because it isn't reentrant, each instance of OFlex running under OS-9 needs a full 60K, but, if the memory is available, many users can run OFlex on the same machine. This could be viewed as an easy way of getting multi-user Flex.

OFlex is licensed from TSC and Frank Hogg Labs. As far as I can tell it is regular Flex with modified I/O which feeds into OS-9. It ran the programs I tried flawlessly, but I know of several Flex programs (I've written some myself) which use memory-mapped I/O directly instead of going through Flex. They won't work under OFlex. Anyhow, if you have OS-9 and you wish you could run most of your old Flex programs, or at least read the old disks, OFlex will do what you need. If you have no particular need for OS-9 but figure OFlex might be an improved way to run Flex, you must be very brave. It is an improvement over regular Flex in several ways, but one day a program you desperately want to run won't work with this mutation of Flex. In any case try OFlex with your software before you rely on it.

New Manuals

I got a stack of new OS-9 Manuals last week. I'm not an authority on most of the OS-9 Manuals, but I've practically memorized the System Programmer's Manual. The new manual is a big improvement over the old one. There is a section on memory management for Level Two and a section on pipes with a few assembly language examples. The Level Two Service Requests are in with the other requests, not isolated in an appendix. Speaking of Service Requests, the manual goes into a good deal more detail than it used to on some of them. The explanation of Chain takes more than two pages, Exit takes about a page and a quarter, as does Intercept.

The new manual contains lots of useful snippets of code demonstrating tricky points. I was particularly pleased to see five chunks of about ten lines each that cover the most obscure parts of an interrupt driven device driver. I believe those chunks of code were taken straight out of the ACIA device driver.

Microware has been producing steadily better manuals for the last two years. The new Systems manual is their best so far. If it had been available last January, I might never have seen a need for this column.

C Functions

I have been working on a program to model a problem in distributed systems for a course I am taking. I needed some functions to manipulate floating point numbers as a separate mantissa and exponent. I spent most of an evening fussing around with assembler before I gave up and wrote the functions mostly in C. It was such a frustrating experience that I decided to include them in this column. I wrote frexp and modf to duplicate functions that are part of the UNIX math library.

Frexp returns the mantissa of val as a double less than one, and stores the exponent in the integer pointed to by eptr. The exponent is for a power of two; that is, the number was (val=x*2**exp).

Modf separates a double into an integer part and a fractional part. The integer part is stored

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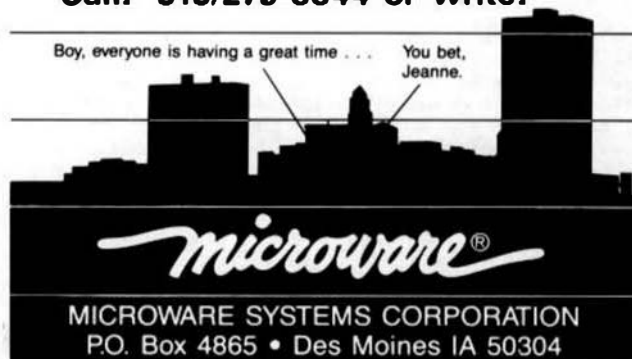
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Boy, everyone is having a great time . . . You bet, Jeanne.



at the address in ptr (as a double), and the fractional part is returned (also as a double).

I wrote most of the code for these functions in C because I couldn't do it in assembler. I certainly tried, but Microware C uses lots of internal subroutines and a special static storage location called flacc (floating point accumulator) to do floating point calculations. I had lots of trouble finding the floating point number and returning the number to the caller. As you can see from the programs, my solution was to use C to do everything in modf, and to find val and return a value in frexp.

The Butterfly

It looks like the Computer Science Department here at the University of Rochester is going to get a computer called a Butterfly. It is named after the network used to connect its processors together. The Butterfly that will be coming here has 128 68000 microprocessors. Each 68000 has at least 512K of memory and, potentially, its own buss. They are all able to read and write one another's memory. I hear that this computer will have the fastest instruction rate in the world. Of course, instruction rates are an almost meaningless measure, but won't that be a marvelous computer to develop parallel algorithms on? It's coming with a UNIX-like operating system, but I can't help but wonder whether it could run OS-9.

Dynaspell

Last summer at the OS-9 Users Seminar I met Dale Puckett at dinner -- before we were both elected as Users Group officers. I had been a loyal user of Dynaspell, a program written by Dale Puckett, but I wasn't entirely happy with it. In fact I had written a very mixed short review of it in this column. During dinner I made Dale sit through a careful explanation of my criticism of his program, and a long discussion of what I thought a spelling checker should do.

Dale was very patient with me. He even encouraged me to go into more depth about my ideas for the perfect spelling checker. I told him that I would write a new, more complete review of Dynaspell if he would send me a version that deserved fresh consideration. Some months later I got a package from Dale including something pretty close to my dream spelling checker. We went through some iterations working out various problems. Now I owe Dynaspell a review. I have been very slow about writing that review, so let me summarize here. I'll go into more depth another month. Dynaspell isn't perfect, but I haven't been able to find any bugs in the latest version. It is much faster than the early version I had. It is able to look near misses up in its dictionary and suggest corrections when it suspects a spelling error.

My remaining complaint about Dynaspell is that the new features don't go far enough. The "look up" feature isn't as selective as a word like. It often finds more possible spellings for a word than it can fit on the screen. On the other hand it sometimes doesn't search widely enough to find the correct spelling for me. I also wish it would give me the features of a screen oriented text editor when it finds a spelling error. Dynaspell has a mode in which spelling errors can be viewed in context, but the context it shows is a screen-full of the document up to and including the word in error. I would like to be able to move forward and backward through the document, and to change words other than the one in error.

I used my early copy of Dynaspell because I need a spelling checked badly and it was the best I had. I use it more often and more happily now. It is one of the best spelling checkers I know: mainframe programs included.

A Nice Experience

Early last summer I bought a TeleVideo 970 terminal. They were just becoming available on the market; in fact, I had a hard time finding one. It seems the boat bringing a large shipment in from overseas had sunk. I'm not certain I believe that, but it was definitely difficult to find one to buy. I finally found one, got it home, and started using it. Nice terminal. Big screen, nice keyboard. Almost too flexible.

After about a week I started finding bugs. A few commands didn't work right. I called the number in the manual and talked to an engineer. The next day I got a package via Federal Express with new firmware ROMs. That wasn't the end of the problems with the terminal. I'm one of those annoying people who reads the entire manual then tries all the strange combinations of commands just to see what they will do, and the 970 has a manual about two thirds of an inch thick. The last time I called them I told them that I needed a feature which was documented in the manual, but which the errata with the manual said was not implemented (downloadable fonts). Without a complaint they sent me a whole new logic board which supports that feature.

I don't think I would recommend the TeleVideo terminal to most OS-9 users. The terminal costs over a thousand dollars. That makes it hard to justify when a adequate terminal only costs five or six hundred dollars. For those who take terminals seriously, it is worth what it costs. It supports ANSI standard and VT52 control sequences, and includes about every feature I can imagine except full graphics (they say that's coming).

The best thing about the 970 is the excellent support TeleVideo gives. Many large vendors seem to lose interest after they sell you their product. TeleVideo has gone out of their way for me again and again.

Tricks for Level Two

I just learned about OS9P3 in the new OS-9 System Programmer's Manual. I have often wished for an easy way to add System Service Requests to OS-9. Under Level One, it isn't too hard, but under Level Two it has required either slight of hand or very strange practices. Only modules running in the system address space can add Service Requests, but OS-9 doesn't include a way to run a process in the system address space. I have run device drivers and file managers just to add Service requests, and considered renaming OS9P2 as OS9P21 and adding my own OS9P2 which will link to and call OS9P21.

Microware has included something like that last trick in Level Two. After OS9P2 is finished initializing (all it does is set up a list of Service Requests) it tries to find OS9P3. There is no OS9P3 unless the user adds it to the boot file, so it generally fails to find the module, but if it finds OS9P3 it executes it as a system module. This opens up lots of interesting possibilities.

Other interesting possibilities are suggested by the SS.SIG and SS.Raise SetStat codes. SS.SIG instructs OS-9 to send a specified signal when data is ready from a path. The easy use for this is to wait for output from several paths at once. This is especially good for things like "modem" programs

that need to wait for input from two paths simultaneously. Without this SetStat the only way to handle that problem was to poll both paths.

It isn't difficult to write a program that polls a number of paths. In fact, polling is the way most of the more primitive microcomputer operating systems work. The problem with polling is that it wastes tremendous amounts of CPU power. I seldom type faster than 2 characters per second. If a program has to poll for my input it will look for something to read thousands of times before it gets anything.

With SS.SIG it should be possible to do a couple of SetStats and wait for a signal. While an OS-9 program waits it uses essentially nothing but memory. This should make programs and other programs with similar problems much more efficient.

The other use I can think of for SS.SIG is to solve the problem that devices can't be preempted. If you have a system with more than one terminal you have probably noticed that if you send a message to another terminal, the message waits until the user at the other terminal types a carriage return. That's because there is a program (e.g. the shell) trying to read from that terminal. Until the read is finished OS-9 won't allow any process to write to it. SS.SIG gives us a way to break that deadlock by not leaving a read active.

I have included a trivial program which demonstrates the use of the SS.SIG setstat with this column. It doesn't do anything useful -- just copies lines from standard input to standard output. The exciting thing is that it works! I ran tstssig on one terminal; typed a few lines into it to make certain that it worked; left it at its prompt, and went to my other terminal. I typed
Echo HI there >/term
on the other terminal and it appeared immediately on the terminal running tstssig. I went back to the terminal running tstssig and typed a blank. The blank caused a signal to be sent to tstssig letting it proceed to the `!$ReadLn`. Once the read was "up" /term was locked. I tried to send another message to /term and found that I had to wait until I typed a carriage return on /term before the message was delivered and the echo command completed.

I wonder whether the SS.SIG trick should be used as a matter of policy when long waits for input are expected.

Editor's Note: As this issue was being prepared for final press, clarification of the Smoke Signal Broadcasting versions of OS-9, were received from SSB.

The net result of everything that has been discussed concerning this over the past few weeks, is simply this:

"Any SSB customer or user who is presently using the 'enhanced' version of OS-9 1.2 from SSB, or who contemplates using OS-9 on a SSB computer, can actually have either/or both." All that is required is that the user notify SSB and the proper EPROM for either, along with either Microware's (virgin) OS-9 1.2 or SSB enhanced OS-9 1.2 will be furnished, at no extra charge, so we are informed by Jim Allday of SSB.

The net result is that SSB users have a choice. again, we see responses from SSB suppliers that far exceeds the cooperative spirit found with other groups of micro users, and something I, for one, am proud of.

DMW

```

00001      nam  tstssig
00002      tti   Test SS16 set stat
00003
00004      *-----*
00005      *   Test SS16 SetStat Service request.   *
00006      *   This program will copy lines from standard input   *
00007      *   to standard output without tying the device   *
00008      *   used for standard input up with a read, or using   *
00009      *   excessive amounts of CPU time by polling the   *
00010      *   standard input path.   *
00011      *-----*
00012      *   tstssig has no practical use that I can think of.   *
00013      *-----*
00014      *
00015      *   IFPI      use 089dofs
00016      *   ENBDC
00017      *   Type      set  Object+Prgra
00018      *   Revs       set  ReEnt+1
00019      *   StdOut     set  1
00020      *   StdIn      set  0
00021      *   SSCode     set  4      code used to indicate input wa
00022      *   LineSize   set  100
00023      *   StackSize  set  200
00024      *   mod 0000 87C00072
00025      *   StdIn      fca  /tstssig/
00026      *   Edition    fcb  1
00027      *   Prompt     fcs  /=>/
00028      *   PromptL     equ  4-Prompt
00029      *-----*
00030      *   Static Storage
00031      *-----*
00032      *   IntMo     rmb  1      Save the signal from the trap
00033      *   Line       rmb  LineSize  Storage for a line to echo
00034      *   StackSize rmb  StackSize
00035      *   MemSize    equ  .
00036      *   Entry
00037      *-----*
00038      *   *****
00039      *   Set up signal intercept trap
00040      *-----*
00041      *   0010 30800090      leax  Trap,PCR      Address of interrupt trap code
00042      *   001C 103F09      OS9  F8cplt
00043      *   001F      Loop
00044      *   001F 3080FF2      leax  Prompt,PCR
00045      *   0023 108E0003      idy  0FPromptL
00046      *   0027 8601      ldx  0StdOut
00047      *   0029 103FBA      OS9  !$Write      Write the prompt
00048      *   002C 2536      bcs  Error
00049      *   002E      StrtRead
00050      *   002E 8600      ldx  0StdIn
00051      *   0030 C601      ldx  0SSReady
00052      *   0032 103F80      OS9  !$GetStt      any data ready?
00053      *   0035 2516      bcs  0SS16      No; wait for a signal
00054      *   0037      DoEcho
00055      *   0037 3001      leax  Line,U
00056      *   0039 108E0064      idy  0LineSize
00057      *   003D 8600      ldx  0StdIn
00058      *   003F 103F80      OS9  !$ReadLn      Read a line
00059      *   0042 2520      bcs  Error
00060      *   0044 8601      ldx  0StdOut
00061      *   0046 103F8C      OS9  !$WriteL      and echo it back out
00062      *   0049 2519      bcs  Error
00063      *   004B 20B2      bra  Loop      Go prompt for the next line
00064
00065      *-----*
00066      *   DoSS16
00067      *   004B C61A      ldx  0SS.SS16      setstat function code
00068      *   004F 0E0000      ldx  0SSCode
00069      *   0052 103F8E      OS9  !$GetStt
00070      *   0055 0E0000      ldx  0
00071      *   005B 103F8A      OS9  F8Sleep      Sleep until an interrupt comes
00072      *   005D 3600      ldx  0IntMo
00073      *   005D C184      caxb  0SSCode
00074      *   005F 27C0      beq  StrtRead
00075      *   0061 43      comx  .
00076      *   0062 2000      bra  Error      set carry
00077      *   0064      Error
00078      *   0064 C1B3      caxb  0ESet
00079      *   0066 2601      bne  Exit
00080      *   0068 5F      clrb  .
00081      *   0069      Exit
00082      *   0069 103F86      OS9  F8Exit

```

```

00079      0000000000000000
00080      0 Trivial interrupt trap
00081      *
00082      006C      Trap
00083      006C E7C4      stb InIMU,U      save the interrupt code
00084      006E 3B      rti
00085      006F 8A234F      read
00086      0072      TestLen equ *

```

```

00000 error(s)
00000 warning(s)
00072 00114 program bytes generated
00120 00301 data bytes allocated
02221 00737 bytes used for symbols

```

```

1 double
2 fresp(val,iptr)
3 double val;
4 int iptr;
5 {
6     register double exp;
7     int exp;
8
9     exp = bval;
10    /* at this point U contains the address of val */
11    base
12    ldd 7,U get C exponent
13    addb #128
14    sex
15    std ,5 save exp
16    lda #128
17    sta 7,U
18    pendata
19
20    iptr = exp;
21    return(val);
22 }

```

```

1 /* modf returns the positive fractional part of val.
2    and stores the integer part in the double pointed to
3    by ptr.
4 */
5 #define MAXLONG 134217727
6
7 double
8 modf(val,ptr)
9 double val, *ptr;
10
11 {
12     double temp;
13
14     if(val > MAXLONG)
15     {
16         *ptr = val;
17         return(0.0);
18     }
19
20     temp = flong(val); /* truncate to int by coercion to long */
21     *ptr = val - temp;
22     return(temp);
23 }

```

RUMORS & SUCH

Rumors and Such

As you might recall, I occasionally dump some fact and a little fiction here under this heading. Been pretty good so far - 90% or more! Now that is better than most of the big boys do. But then I have better sources than they do. That and a lot of reading and listening between the lines gets it done.

Since my bout with the pump doctor I have been pretty far removed from the normal stream of things around here, but I now am back in the swing, more and more each day. So as is my usual habit I looked around for something to 'rumor' about. Really not much as actual rumor except that Tandy may yet come out with a much improved color (6809) computer.

Seems that they DO NOT want to merchandise anything to compete with their Intel CPU driven computers, on a serious level. Fact is, they are surprised at what has happened to their 'little toy' color computer. I can remember telephone conversations with Tandy personnel, some years ago when they first came out with the CoCo. Our first one had a serial number less than 200. I was telling them what a great foundation the box made and they were touting their 'lousy' chess game. They didn't even consider disks for it at that time. Boy, what a difference disks and OS-9 has made to the little machine. A true 64K, multi-tasking, multi-user (both difficult due to built in hardware designs), but there still. I guess there are over 30,000 OS-9 users now on the CoCo and more coming each day, just goes to show what a fine CPU the 6809 is. A shame Motorola never felt as strong as we have about the 6800/6809. If they had given the 6800 and the 6809 as much attention and exposure as Intel did the 80 series of their devices, I guess all the IB?'s and clones would be 6809 based. The old 'mouse trap' saying still holds true. The 68000 has received better, but that is another ball game of sorts.

A New 6809 Computer from SWTPC

For the past several months a new computer from SWTPC has been advertised in 68 Micro Journal. We have received a lot of telephone calls from prospective users wanting some specs. Well we have some pretty solid info concerning the new SWTPC X-12+. Here goes:

The X-12+ system is both a FLEX™ and UnIFLEX™ system. The heart of the system is the 6809 CPU supported by three (3) additional CPUs.

First, there is a 3870 Keyboard encoder, which is actually a complete CPU and unburdens the system from keyboard chores. This unit is interfaced to the main system by a separate 6809 terminal controller. Which leaves the main CPU with little to do about terminal or keyboard activities.

Of course as stated above the main CPU is a 6809. Faster than a 68000 in many functions, except for math operations, well - here SWTPC has brought in a new player (option), the TI 320 32 bit, bipolar, 20 Mhz Floating Point processor. Talk about fast!

The system comes standard with 256K RAM and is expandable to a full 1 megabyte of RAM.

A FLEX version comes with floppies and a special version of FLEX that will allow a single user operation of multi-tasking capabilities. This is accomplished by dividing the RAM into two units. Each RAM unit will allow 64K for a task and 64K for 'super speed' disk type RAM. Under this scheme a user will run two separate tasks with very fast data accessing, provided the data is moved to RAM-disk. This is similar to the foreground/background facility of the 'DYNASHARE' FLEX program sold by S.E. MEDIA, and really makes a single user system fly!

In the FLEX mode the system is restricted to one user and two tasks, at super speed.

The (rumored) price of the 256K, FLEX single user system with floppies is in the neighborhood of \$4500.00, less the math option.

The system is also available with UnIFLEX and 20 megabyte winchester storage. UnIFLEX can be run with floppies only but the winchester enhances UnIFLEX operations to the point that any serious user will opt for the winchester version.

Under UnIFLEX the system is expandable to three (3) users merely by plugging in additional CRT terminals (2) maximum.

The UnIFLEX system with one floppy and 20 megabyte winchester will sell for approximately \$6500.

The basic system comes with a parallel port and 4 serial ports, two of which are designated for terminal connection.

I am sure that a call or letter will get you more information from SWTPC.

SSB Enters 68000 Arena

Smoke Signal Broadcasting in this issue announces their new 68000 computer systems (see Bit Bucket - SSB product announcement). The press release is fairly complete as concerns the specs of their new VAR/68K

systems.

However, there is a possibility of a price break for 68 MICRO JOURNAL readers, that I have to tell you about. It is rumored that for the month of JULY 1984, 68 MICRO JOURNAL readers will have the opportunity to purchase a VAR/68K 68000 SSB computer for something like **50% OFF THE REGULAR ADVERTISED PRICE!!!!**

Now let me tell you that is one sweet deal! That's right a total 68000 system with terminal, floppy, winchester and 256K RAM with a UNIX™ type operating system for **half price!** Come by our office after July and see our new VAR/68K 68000 - I guarantee you we are going to get one.

For space considerations I cannot repeat the news release particulars but if you want to know more, I just bet that the folks at SSB will certainly be more than happy to reply to your call or letter, concerning this or any of the other fine SSB products.

Oh, I believe that they will also make available to any Standard S50 Bus user a full set of boards, at a very reasonable price, to allow you to update your present S50 pin computer to equal the VAR/68K 68000 SSB computer. Another savings for those considering updating to the 68000/68008. Watch coming SSB ads and 68 MICRO JOURNAL announcements concerning the availability and price of the 68000 system boards.

♦♦

Thanks Smoke for making our readers such a fine offer!

DMW

** Late Note: Just received a call from SSB concerning the boards above, can't believe what they implied, but I'm going to tell you what I think they told me:

For those with a Standard S50 Bus computer the following parts are packaged up into a kit for 68 MICRO JOURNAL readers:

1. A new heavy duty motherboard.
2. A new standard heavy duty power supply. (handles every thing including winchester)
3. New SSB 68008 CPU board.
4. A new SSB 5-8 inch disk I/O controller.
5. A new SSB winchester disk controller.
6. A new 5 megabyte winchester disk drive.

That's right, everything but I/O and memory for
Get this - appx. \$2500. RIGHT \$2500.

This is, to the best I can figure about \$7,000.00 worth of boards and winchester drive. A real bargain for sure and for those wanting to upgrade - **HERE IT IS!!!**

THANKS AGAIN SMOKE!

"C" User Notes

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INTRODUCTION

I have assumed the responsibility of writing the C User Notes column from Norm Commo. His articles were interesting and one of the features in '68' Micro Journal which I always read. Some of you may remember that I wrote a series of articles for System 68 magazine, which has since ceased publication.

CALL FOR ASSISTANCE

I definitely need help from '68' Micro readers and vendors to supply me with a continuing supply of interesting C-related programs, functions, compilers, application programs, books, commentary, etc. to review and to discuss.

I also would like to publish one reasonably short C program or function each month, preferably with explanation. User input for such a concept is vital.

I need submissions on disk. I can accept standard FLEX, OS/9, UNIFLEX, COCO, CP/M, IBM PC, and a few other standard formats. I cannot accept non-standard formats. To protect '68' Micro and myself, I need a written release from the owner of the program or function.

If you have questions about, problems with, or success with a particular C-related product, send me a letter and/or disk (with appropriate documentation) to the address above, not to the '68' Micro address. I will accumulate them, comment on them, and publish the more interesting ones, but I will attempt to answer all of them. Maybe your experiences and questions are not as unique as you might think. I know that each of the C compilers available for the 6809 has its own set of strengths and weaknesses. Maybe publicity can help get the vendors to correct their problems.

C COMPILER 6809 IMPLEMENTATIONS

There are several implementations of C on the 6809. Like most other products, the completeness and usability of the implementations vary considerably. However, there are several major groups into which many of the implementations fall. The most complete compilers are called "Full-C" and implement UNIX C almost entirely. The least complete compilers are advertised as "Small-C" and implement only specific subsets of UNIX C. Many C compilers are complete in some areas, such as verbs, but are incomplete in some areas, such as floating-point variable types. Many C compilers claim to be complete implementations of UNIX C, but actually provide far fewer facilities.

The following list presents most of the major known versions of C for the 6809, loosely ranked in decreasing order of completeness of implementation:

Microware/McCosh	Full C
Introl	Full C
Telecon	Full C
Wordsworth (v2)	Small C
Dugger (v2)	Small C
Dyna-C	Small C
Everhart	Small C
Wordsworth (v1)	Small C
Dugger (v1)	Small C
Intersoft	Small C

Only Microware/McCosh, Introl, and Telecon currently claim to support floating-point, long variables, structures, unions, and initializers. None currently claim to support bit fields.

Essentially all of the non-Full C compilers were based on or derived from Ron Cain's Small C, which was written for the 8080. It has the advantage of being written in its own language, thus facilitating porting to new implementations. It has the disadvantage of having several major bugs, most of which were also ported to the new implementations.

The Telecon and Dugger compilers are apparently still marketed, but are not advertised in '68' Micro. The Wordsworth and Intersoft compilers are apparently no longer marketed. The Everhart compiler is in the public domain.

PUBLIC-DOMAIN C SOFTWARE FOR 6809

The Atlanta Computer Society Motorola Special Interest Group is attempting to establish a public-domain C library and bulletin board with upload/download capabilities. We have already converted a few programs from other public domain C libraries, but we need your help in making the library useful. I will print more information in future columns, including contents, addresses, phone number, etc.

USER COMMENTARY

Sidney Thompson has worked with the C language for several years, initially on UNIX systems, but then on micros. Several months ago, he discussed with me some ideas for an article based upon his experiences in attempting to use the Microware C compiler. I encouraged him to write the article and helped him rephrase and rewrite the notes. I have extracted some of his major points and have included them below.

I have read several glowing reviews of the Microware C compiler. This compiler, along with FLEX and UNIFLEX versions, are the product of James McCosh, and thus share many of the same features and problems.

I had been looking forward to using the compiler, since the McCosh compilers were the first 6809 C compilers to support structure initializers, as well as most of the other C language standards except bit fields.

I decided that I should take a program that was already running under the OS/9 INTRNL compiler and see if it would compile. This program consists of seven program modules plus a common "include" file that defines all the global variables and constants being used within the program.

There were some trivial problems, such as TRUE and FALSE not being defined in the "stdio.h" file, but these were easily fixed.

After the compiler had processed through the seven modules and started to link, I got an undocumented error message indicating a fatal "name clash", that all my variables had been previously defined.

I determined that the problem was the declaration of the variables in the common "include file". The compiler was unhappy with either having all the variables declared as int and char, or having them all declared as extern int and extern char.

The include file used in the module containing main() must NOT declare the variables as "extern" while all other modules MUST declare them as "extern". This means that the programmer must either use separate "include files" or must code the following alternate forms:

```
/* before include file in main() module */
#define GLOBAL

or

/* before include file all others */
#define GLOBAL extern
```

This is not documented in the Microware C compiler manual as a requirement to compile modules separately. This means that any source program that uses modules with a common declaration file and runs under UNIX, BDS, INTRNL, or any of several other C compilers must be modified before it will work with the

Microware C Compiler.

There is no facility provided to modify the standard library, since the manual says that few people would need this capability. Yet, there are several missing key functions, such as the following:

```
rand()
realloc() (malloc() is provided)
max()
min()
```

There are several, such as gettime(), that differ from the equivalent UNIX function by having the function and its variables with different names.

Like UNIX, Microware C requires the inclusion of header files for special functions, such as gettime(), setstat(), lalpha(), pflint(), etc. Yet the compiler does not always tell you if you do not include these files at the proper place, and the modules will compile and link. But they will not run.

I was forced to remove almost all of the extra OS/9 level 1 modules from my os9boot file before the compiler could be run at all. With a standard 56k system, there is barely enough memory to use the C compiler. I understand that COCO OS/9 has more memory available.

The preceding has attempted to present some constructive criticisms regarding the Microware C compiler. The following actions are recommended to help improve the usability of the product:

A) Place the OS/9 level 1 version of the compiler into three or four passes, in order to reduce its memory requirements, and thus allow compilation on a normal OS/9 level 1 system, not a stripped-down one.

B) Fix or document the problems associated with compiling C programs that use separately-compiled modules.

C) Document all error messages, explaining possible causes and fixes.

D) Add many of the missing functions to the standard libraries, and provide a means for a user to enhance, customize, or modify the libraries.

With these changes, the Microware C compiler would be an even more impressive product than it already is today. This would lead to some serious business type application program development, rather than it being useful, in general, only as a tool for small programs.

I hope Sidney's entire article will be printed soon in '68' Micro. He is working on a follow-up article, which I am sure will be just as interesting as this one is. Whether you agree with him or not, he has some useful comments.

EXAMPLE C PROGRAM

Following is this month's example C program; it paginates listings.

```

#include "stdio.h"
main(argc,argv)
int argc;
char **argv;
{
    char *input,*output,*date;
    char line[256];
    int lines,pages,i;
    lines=99;
    pages=0;
    input=stdin;
    output=stdout;
    date=0xcc0e; /* FLEX date locations */
    if(argc>1)
    {
        if((input=fopen(++argv,"r"))==NULL)
        {
            fputs("can't open input\n",stderr);
            return(1);
        }
    }
    while(fgets(line,256,input)!=NULL)
    {
        if(++lines>58)
        {
            if(lines<99)
                putc('\f',output);
            lines=0;
            printf("    Page %d    ",++pages);
            printf("Date %d/%d/%d",
                (i=(*(date))&15),
                (i=(*(date+1))&31),
                (i=(*(date+2))&127));
            i++;
            if(argc>1)
                printf("    File %s",*argv);
            putc('\n',output);
            putc('\n',output);
        }
        fputs(line,output);
    }
    if(lines!=99)
        putc('\f',output);
    return(0);
}

```

Editor's Note:

I would like to take this space to first thank Norm Comma for the many, many hours of time he has devoted to the 'C' User Notes. Norm was the first 'C' author and only full-time 'C' author we have had, until now. To say that Norm has done a good job, would be putting it too mildly. Norm has given more than many will ever realize. I want to THANK Norm, and his family who also had their time schedule intruded on by the constraints of getting a column out nearly every month. Without fine folks like Norm Comma, 68 MICRO JOURNAL would never have survived. I wish I knew stronger words, at times like these, but this is from the heart Norm, I thank you for all you have done and I thank your family for their understanding and consideration, for all those hours you gave to all of us. We are all enriched in this activity by your skill, effort and time. And it is our sincere prayer and hope that full health might be restored within your family, and soon.

THANKS Norm from all of us! GOD BLESS!

Most of you readers will recognize Dr. Bud Pess, who has authored many technical articles for various computer magazines, including System 68 and 68 MICRO JOURNAL.

I feel fortunate that Bud has agreed to take over the duties of getting out a 'C' column for all of us who strive to learn this new and exciting language. His qualifications are well accepted to carry on in Norm's place. And he was the first choice of Norm to carry on.

So, "Welcome Aboard", Bud. I and thousands of others are looking forward to your leading us on into the depths and power of 'C'. And to all you readers I say, "enjoy."

DMW

READING NON-FLEX BASED DISKS

CONTINUED FROM LAST MONTH

2012 AE	E9 0001	LDI	TMP3,S	set current trk/sec
2016 86	01	LDA	B1	AD0 means getnext trk/sec
2018 80	0F	BSR	MAPPER	set next trk/sec
201A 6A	E9 0000	DEC	TMP1,S	gotten all sectors?
201E 26	07	BNE	G81	brncb if not
2020 32	E9 0003	LEAS	LS6,S	release local storage
2024 1F	21	TFR	Y,X	copy ptr to end of buf to X
2026 35	26	PULS	Y,D	restore regs
2028 39		RTS		return

```

* Data area for GETBLK routine
*
0000      2029 LPC      SET      *
          0000      ORG      80000
          0000      LCL      SET      *
          0001      TMP1    RMB      1
          0001      TMP3    RMB      2
          0003      LS6     EQU      6-LCL    len of local storage

2029      ORG      LPC      restore PC

```

```

* Name      - MAPPER
* Function - This routine takes in one of two kinds
*            of parms. If ACC A is clear, then the
*            contents of the X register will be
*            taken as a CP/M block number, in which
*            case the corresponding track/sector
*            value will be returned in X. If ACC A
*            is non-zero, then X will be taken as
*            a track/sector value. In this case,
*            the next track/sector value will be
*            returned in X.
*
*            ACCs A and B are preserved
*
2029 34 06      2029 MAPPER EQU      *
          2028 40      PSMS    0      save regs
          202C 27 0F      TSTA    BEQ      BLOCK    take as block value

```

```

* simply find next trk/sec value from map table
*

```

```

202E 1F 10      TFR  X,D      set previous offset
2030 F6 207A    LDB  TBLOFF
2033 5C        INCB
2034 F1 0889    CFPB  MAPLEN  go to next one in table
2037 25 34      BLO  GETIT   wrap around?
                                now, then ok

2039 4C        INCA
203A 5F        CLRB
203B 20 30      BRA  GETIT   yes, incr track and
                                offset to table beginning
                                set sec and return

```

• convert block# in X to trk/sec equivalent
 • The formula for performing the conversion
 • from block # into 2 byte trk/sector value
 • is as follows:

```

    trk = (block# * 81 / TBLEN) + OFFSET
    sec = MAPTAB[ (mod(block# * 81 / TBLEN) ) ]

```

```

203D BLOCK EQU *
203D F6 0889  LDB  MAPLEN  divisor
2040 F7 207C  STB  DIVISR   save it
2043 C6 08    LDB  #8      counter for divide
2045 F7 2078  STB  CTR      save it

2048 1F 10    TFR  X,D      dividend = dividend*8
204A 58      ASLB
204B 49      ROLA
204C 58      ASLB
204D 49      ROLA
204E 58      ASLB
204F 49      ROLA

2050 DIVIDE EQU *
2050 58      ASLB          shift dividend, quotient
2051 49      ROLA
2052 01 207C  CFPB  DIVISR   is trial sub. successful?
2053 25 04    BCS  CHKCHT
2057 80 207C  SUBA  DIVISR   yes, sub and set bit on quot.
205A 5C      INCB

205B CHKCHT EQU *
205B 7A 2078  DEC  CTR
205E 26 F0    BNE  DIVIDE

2060 70 087E  TST  B0FFIG    5" disk?
2063 27 04    BEQ  SETS     brnch if so

2065 C8 02    ADOB  B0FFB    add in 8" offset
2067 20 02    BRA  SETDOWN
2069 C8 03    ADOB  B0FF5    add in 5" offset
206B SETDOWN EQU *

```

• Acc A = remainder, Acc B = quotient
 • Swap registers to get quotient, remainder pair
 • and then use mapping table to find actual
 • sector on CP/M disk

```

206B 1E 89      EMO  A,B      swap A,B

206D GETIT EQU *
206D F7 207A    STB  TBLOFF  save table offset
2070 8E 0887    LDB  MAPTAB  X -> mapping table
2073 E6 85      LDB  B,X     set sector
2075 1F 01      TFR  D,X     X has trk/sec value
2077 35 06      PULS D        restore regs
2079 39        RTS          return

• Data Area
•
207A 00        TBLOFF FCB 0    hold current table offset
207B          CTR  RMB 1
207C          DIVISR RMB 1

207D MAPTAB EQU * Mapping Table (8")
207D 01 07 00 13 FCB #01, #07, #00, #13, #19, #05, #08, #11
2085 17 03 09 0F FCB #17, #03, #09, #0F, #15, #02, #06, #0E

```

```

208D 14 1A 06 0C FCB #14, #1A, #06, #0C, #12, #18, #04, #0A
2095 10 16      FCB #10, #16
                EQU  MAPTAB
2097          MAPTAB EQU * Mapping Table (5")
2097 01 03 05 07 FCB #01, #03, #05, #07, #09
209C 02 04 06 08 FCB #02, #04, #06, #08, #0A
209A          EQU  MAPTAB

```

• Name - READSS
 • Function - This routine reads in one track/sector of
 • the disk in the drive specified by an FCB
 • pointed to by X. Reg D specifies the
 • track/sector to read in.

A BNE or BEQ should be used after calling
 this routine to check for possible read
 errors.

All registers except A are preserved.

```

20A1 READSS EQU *
20A1 ED 08 1E  STD  FCBP,X   set trk/sec
20A4 86 09      LDR  B,PCX   set function code
20A6 A7 84      STA  FCBFC,X  set code in FCB
20A8 7E D406    JMP  FCBFCAL read and return

```

• Name - MVC
 • Function - This routine moves the "from" string
 • to the "to" string as follows:

X -> "from" field
 Y -> "to" field
 D = # characters to move

Regs A,B,X,Y are preserved

```

20AB 34 36      MVC  EQU *
20AB 34 36      PSWS A,B,X,Y save regs

20AD MOVE EQU *
20AD 34 06      PSWS D        save len to move
20AF E6 80      LDB  D,X+     set a "from" char
20B1 E7 A0      STB  D,Y+     trans to "to" field
20B3 35 06      PULS D        get len
20B5 83 0001    SUBB #1      decr by 1
20B8 10B3 0000  CMPD #0      done yet?
20BC 26 EF      BNE  MOVE     brnch if not

20BE 35 36      PULS A,B,X,Y restore regs
20C0 39        RTS          and return

```

• Name - CLC
 • Function - This routine compares two strings
 • as follows:

X -> string1
 Y -> string2
 A = length of strings to compare

String1 is compared to String2, and the
 appropriate condition code is set.

Regs A,B,X,Y are preserved.

```

20C1 CLC EQU *
20C1 34 36      PSWS A,B,X,Y save regs

20C3 COMP EQU *
20C3 E6 80      LDB  D,X+     get a string1 char
20C5 E1 A0      CFPB  D,Y+     compare to string2 char
20C7 26 03      BNE  RPNOC    if not then done

20C9 4A        DECA
20CA 26 F7      BNE  COMP     all through strings?
                                brnch if not

```

20FE 81	46	DPA	0'F	file?
2100 26	71	SNE	R6	leave if not

- output to a file

```

2102 0E 089F      LDX    #TOFILE    ask for "to" filename
2105 80 C01E      JSR    PSTRING
2108 80 C018      JSR    INBUF      get response
210B 8E 0A3B      LDX    #ILFCB     "to" FCB
210E 80 80A0      JSR    GETFIL     validate filename
2111 24 08        BCC    A4         check if file exists

2113 0E 089F      LDX    #INVSPC    invalid filename entered
2116 80 C01E      JSR    PSTRING
2119 20 58        BRA    R6         return

```

```

2110 R4      EQU      *
2110 B6 0B      LDA      R0UT      set extension
2110 B0 C0C3     JSR      SETEXT

```

- Open the file

```

2120 B6 02      LDA    #XOMRIT    open for write
2122 A7 84      STA    FCBFCB:k  save in FCB
2124 BD B406    JSR    FCBFCAL    call FMS
2127 27 00      BEQ     R7       continue if file not there

```

- File already exists -> say so and return

```

2129 0E 0805      LDX    #FXIST
2130 00 C01E      JSR    PSTANG
2131 20 42         BRA    R6          return

```

2131 R7		EQU	*
2131 0E	08F0	L0X	GETTYPE ask for file type
2134 0D	0D1E	JSR	POSTING
2137 0D	0D09	JSR	IND1 set response
213A 04	5F	ANDA	055F lowercase-uppercase

2130	5F		CLRB		
2130	81	54	CMFA	RT	text?
213F	27	11	BEQ	R8	branch if so
2141	81	00			
2143	27	00	CMFA	ROR	CR? (same as text)
			BEQ	R8	branch if so

2145	B1	42	CPA	48	Binary?
2147	26	2A	BRE	R6	Bad leave if not

2149 C6	A0	LOB	BOPH+BINRY set open-bin bits
214B 86	FF	LDA	%SPHSC type-binary
214D B7	0A76	STA	FILECB+FOBSOF set compress. flag
2150 20	02	BRA	RI

2152 C6	C0	2152 R8	EGU	*	
			LDB	OPEN+TEX	set open-text bits
		2154 R1	EGU	*	
2154 BE	0A30		LDI	HFIFCB	set all o/p to file
2157 BF	CC24		STI	FOA	
215A 7F	CC22		CLR	OSWICH	o/p to the file
215D F7	0B7C		STB	OPHPLG	save flag bits
2160 7F	CC04		CLR	WIDTH	zero TTV width value
2163 20	0A		BRA	R9	return

```

2165 R2      EQU      *
      .
      . perform setup for output to printer
      .

2165 7F      CC22      CLR      DS10M0H      o/p to aux. device
2168 88      0E        BSR      PRTSET      fetch printer module
216A 20      03        BSR      PRT      and return

```

```

216C R3      EDU      *
      *
      * perform setup for output to terminal

```

216C 7C CC22 INC DSATCH
216F R9 EQU
216F 1C FE CLC set good RC

200F 34	36	200F ROUTE	EQI PSN6	# A,B,I,Y	save res
20E1 8E	0000		LXI	80	clear file output address
20E4 8F	0024		STX	FOA	in <u>FILE</u>
20E7 8E	085B		LXI	00UTFIL	prompt user for route
20BA 80	001E		JSR	PSTRNG	
20BD 80	0009		.ASR	INDX	set response
20FD 84	5F		ANDA	01CF	lowercase->uppercase
20F2 81	50		CPIA	01P	printer?
20F4 27	6F		BEQ	R2	brnch if so
20F6 81	54		CPIA	01T	terminal?
20F8 27	72		BEQ	R3	brnch if so
20FA 81	00		CPIA	00CR	CR? (same as terminal)
20FC 27	6E		BEQ	R3	if so brnch

```

2171 20 02      BRA R10      and leave
                2173 A6      EQU *
2173 1A 01      SEC          set bad RC
                2175 R10     EQU *
2175 35 36      PULS        A,B,X,Y restore regs
2177 39          RTS         and return

```

```

2104 B0 C03F    JSR RPTERR  report error
                2107 RS1    BQU *
2107 7F 0B7C    CLR        *BNFLG indicate no file open
210A 39          RTS         and return

                END         START

```

```

*
* Name - PRSET
* Function - This routine is called to load the
*            printer module if necessary,
*            and init the PIA, so that output can
*            be routed to the printer.
*
*
* No registers are preserved

```

```

                2178 PRSET EQU *
2178 7F 0C09     CLR PAU      disable pause feature
2178 B6 CDE4     LDA POUT     get 1st byte of space
217E B1 39      CMA #39      is it 'RTS'?
2180 26 29      RNE P15      if not the loaded

```

```

* Load printer routine

```

```

2182 8E 095B     LDX #PSYS    move in print rta name
2185 108E C844   LDY #SYSFCB+FCBNAME into system FCB
2189 0C 000B     LDD #11
218C 17 FF1C     LBSR MVC
218F 7F C843     CLR SYSFCB+FCBNAME check drive 0

2192 9E C840     LDX #SYSFCB point to system FCB
2195 86 01      LDA #STOREAD open for read
2197 47 B4      STA FCBCF.I
2199 BD 0406     JSR FMSCAL    call FMS
219C 27 05      BEQ P1        branch if open ok

```

```

219E B0 C03F    JSR RPTERR  report error
21A1 20 11      BRA P2      and return

```

```

                21A3 P1      EQU *

```

```

21A3 86 FF      LDA #SCFASC set for binary read
21A5 A7 9B 3B   STA FCBCF.I
21AB B0 C030     JSR LOAD     load module

```

```

                21AB P15     EQU *

```

```

21AB B0 C0C0     JSR PRINIT   go init port
21AE BE CCE4     LDY #POUT    get o/p address
21B1 BF CD10     STX OUTCH+1  stuff in FLEX
                21B4 P2      EQU *
21B4 39          RTS         return

```

```

*
* Name - RESET
* Function - This routine is called to reset the
*            FLEX output switch and close any file
*            that might be open through the FILECB
*            FCB.
*
*

```

```

                21B5 RESET EQU *
21B5 86 01      LDA #1
21B7 B7 CC22     STA OSWITCH reset output switch
21BA B7 CC09     STA PAU      enable pause
21BD 7D 0B7C     TST *BNFLG   is a file open?
21C9 27 15      BEQ RS1      return if not

```

```

* Output was going to file, close it

```

```

21C2 B6 0B7D     LDA TTYWID  restore TTY width
21C5 B7 CC0A     STA WIDTH

21C8 BE 0A3B     LDX #FILECB set FCB
21CB B6 04      LDA #NCLOSE close code
21CD A7 04      STA FCBCF.I
21CF BD 0406     JSR FMSCAL    call FMS
21D2 27 03      BEQ RS1      if ok return

```

```

0 ERROR(S) DETECTED

```

```

SYMBOL TABLE:

```

ADDB1	C036	ASREAD	0001	ASMRIT	0002	BAC	0008	BADFIL	08A4
BADIN	0A1B	BAK	0005	BAS	0003	BOMT	0000	BEIL	0007
BIM	0000	BIMRY	0020	BLK3	108F	BLK4	1081	BLK5	108D
BLK6	1DA7	BLKONT	0881	BLK1ST	198A	BLKOUT	1049	BLCKX	203D
BS	CC00	BSE	CC07	BS1Z8	0800	BS1Z8	0400	BUFFER	08BA
BUPINT	CC14	BUFSIZ	0B7F	CHKONT	205B	CLASS	CD21	CLC	20C1
BLN	CC1A	CLOCK	F700	CMD	0002	CMDFLG	CC28	COC	CC29
COLDS	CC00	COLUME	0002	COMPR	20C3	CONFIG	0B7E	COPYIT	1DB8
CPMPLX	0010	CPMBS	0010	CPMCF	000C	CPMDIR	0000	CPMEI	0003
CPMEIT	0009	CPMFEF	0000	CPMHL	0008	CPMLEN	0020	CPMNA	0001
CPMHL	0008	CPMRS2	090B	CPMSC	000F	CR	0000	CRUF	080A
CTR	207B	CURC	0C18	DAT	0007	DORV	0E00	DEL	CC01
DEPTH	CC03	DIR	0009	DIRBEG	0000	DIREND	0DE3	DIRMSG	0777
DIRTS	0005	DIVIDE	2050	DIVISR	207C	DOCHRD	CD4B	DOS	CC00
DP1	1DFF	DP2	1E0E	DRV	0B7B	DRVPRT	07FD	DS1Z1	0002
DS1Z2	0000	DTYPE	091B	EJECT	CC08	ENDCUP	07CF	ENDIR	0796
ENTADR	0000	ENV	CC2D	EDBUF	0883	EOP	001A	EOL	CC02
EOT	0004	ERR	1CA0	ERR02	1C9A	ESC	CC0A	ESOPR	CC16
EX1	1E40	EX2	1E52	EY3	1E73	EXTMIS	1E16	EXTSIZ	0090
FACP	0010	FADP	0040	FAMP	0020	FAMP	0080	FCBAS	0002
FCBASE	0409	FCBCDA	002F	FCBCP	001E	FCBCRN	0020	FCBCIR	D40B
FCBD1	0022	FCBDN	0003	FCBEDA	0013	FCBESB	0001	FCBFA	000F
FCBFC	0000	FCBFCO	0019	FCBFD0	0032	FCBFS	0015	FCBFSR	0017
FCBLN	0140	FCBLP	001C	FCBNA	0004	FCBMB	0024	FCBRI	0023
FCBRS1	0010	FCBRS2	001B	FCBSB	0040	FBCSCF	0038	FCBSOR	0035
FCBSSA	0011	FCBVER	0435	FCDDAY	001A	FCDIRM	0019	FCBYR	001B
FIA	CC26	FIEF	CC2F	FILFCB	0A3B	FIVE	1C0B	FLEX	CC00
FMS	D400	FMSCAL	D406	FMSCLS	D403	FMSERN	CC20	FMSINT	D400
FDA	CC24	FOUND	1F35	FSHAWN	0002	FSHSD	0000	FTYPE	09FD
FI1ST	0805	G01	1F77	GET1	1E95	GET2	1EA7	GET3	1EA9
GET4	1EA3	GETBLK	1FE2	GETCHR	CD15	GETDRV	1EB7	GETFIL	CD2D
GETHE1	CD42	GETIT	2060	GETD	1DE6	GT1	1E7A	GT2	1EDA
GT3	1EE3	GTBAD	1EE7	GTGOOD	1EE9	GTSPEC	1EAC	HEAD	0100
INBUF	CD1B	INCH	CD09	INDC2	CD0C	INDEC	CD4B	INDRV	0843
INFILE	0B2C	INTRO	0966	INWSPC	08B9	IOFLG	CC21	ISWICH	CC23
JT	1C56	LAD	CC1B	LCL	0000	LF	000A	LINEBU	CC00
LOAD	CC30	LPC	2029	LS1	0004	LS2	0003	LS3	000B
LS4	0003	LSS	0002	LS6	0003	LSTRM	CC11	MD	1C5E
ML	1C72	R2	1C7C	R3	1C87	NAP	CC00	NAPADR	08B7
NAPLEN	08B9	NAPPER	2029	NAPTS	2097	NAPT8	207D	NEMEND	CC2B
MEM2	1C37	MEMU	1C34	MOVE	20AD	MVC	20AB	ML	1C3A
N08	1B83	MULL	CC05	NUTCH	CD27	OFF5	0003	OFFB	0002
OPN	0080	OPMFLG	0B7C	OSWICH	CC22	OUT	000B	OUTADR	CD45
OUTCH	CD0F	OUTCH2	CD12	OUTCHC	CD39	OUTDRV	0772	OUTEXT	0824
OUTFIL	085B	OUTHE1	CD3C	OUTNAM	081B	OUTSIZ	0000	P1	21A3
P15	21AB	P2	21B4	PAU	CC09	PB1	1F94	PB10	1F72
PB11	1F76	PB12	1F7A	PB13	1F99	PB14	1F5B	PB2	1F89
PB3	1FDF	PB4	1FD9	PB5	1FCD	PB6	1F7F	PB7	1F4E
PB8	1F90	PB9	1F63	PCALF	CC24	P01	1CF1	P05	1D04
P06	1D1B	P07	1D00	P08	1D27	PDATA	20CF	PDATA1	20D1
PORTA2	200C	PDIR	1CA6	PDIRT	1D2D	POUT	CCE4	PRCHK	CC0B
PREVC	CC19	PRINIT	CCDD	PRON	1C40	PRT	000A	PRTBLK	1F40
PRSET	217B	PSTRNG	CD1E	PSYS	095B	AUTCHR	CD1B	R1	2154
R10	2175	R2	2165	R3	216C	R4	211B	R6	2173
R7	2131	R8	2152	R9	216F	ROEPR	0947	READSS	20A1
REITER	CD06	RESET	21B5	RET	1F39	ROUTE	200F	RPMDC	20CC
RPTERR	C03F	RSTRIO	CD2A	RS1	2107	SBDATA	0044	SBLINK	0040
SBRIS1	0042	SCFSC	00FF	SCFSC	0000	SCR	0006	SEDLK	000B
SEDSIZ	08B5	SETS	2049	SETDGN	2068	SETEXT	0033	SETPAU	1C92
SFA	C980	SIMORE	0023	SIRDAY	0024	SIRFSB	0010	SIRFSR	001F
SIRFS	0021	SIRLEN	002B	SIRPTH	0023	SIRPTS	0026	SIRNAM	0010
SIRTS	0003	SIRVOL	001B	SIRYR	0025	SP	0020	SP3	0E2B
SFS	C700	SRC2	1F14	SRC3	1F27	SRODM	1EEC	SS1Z5	0100
SS1Z8	0080	START	1BDA	START1	1BDD	STAT	CD4E	STKA	CC00
STSIZ	1C1B	SVEFIL	0000	SVDR	CC0E	SVDRV	CC0B	SYS	0004
SYSOON	CC4E	SYSOR1	CC00	SYSOR2	CC2A	SYSOR3	CC30	SYSOR4	CC3B
SYSFCB	CD40	TAB	CC06	TABCON	0009	TABLES	000A	TABLES	001A
TBLOFF	207A	TEX	0040	TNP1	0000	TNP3	0001	TUFLE	08B5

```

TRACOR CC1E TRFEG CC1D TTYWID 087D T1T 0001 LCA C100
UCTA CC12 UNK1 1D3E UNKOUT 1D35 URAM 0000 MARKS C003
MELCO2 0368 MELCON 012A WIDTH C004 WICURV C00C XBOR 0016
XDELOE 0004 XDELET 000C XEND 0014 XGIR 0007 XGRB 0011
XASS 000F XODIR 0006 XOREAD 0001 XOSIR 0010 XOLPOT 0003
XOMRIT 0002 XPIR 0008 XPOSH 0015 XPRB 0012 XREMA 000C
XRES1 0008 XRES2 000E XRES3 0013 XRES 0009
XRAMB 0000 XTHP 0001 XASS 000A

```

```

*****
*
*   DOS to FLEX Utility
*
*   Written by: Scott R. Fraser
*               547 Sharron Bay
*               Winnipeg, Manitoba, Canada
*               R2G 0H0
*               Ph. (204) 338-7641
*
*****

```

```

* NOTE: Program tested for single sided,
*       single density disks only

```

```

* Uses SSB's DOS69D version

```

```

* DSECT for a DOS Sequential File
* Information Block (FIB)

```

```

0000          ORG 60000
0000 FIB      EQU *
0000 FIBFM    RMB 6      file name
0006 FIBEXT   RMB 3      file extension
0009 FIBFT    RMB 1      file type
000A FIBPS    RMB 1      file status
000B FIBFSU   RMB 2      1st file sector used
000D FIBLSU   RMB 2      last file sector used
000F FIBSUC   RMB 2      file size (# sectors)
0011 FIBRSV   RMB 6      reserved
0017 FIBFES   RMB 1      extended file status
0018 FIBLEN   EQU *      length of FIB

```

```

* Define DOS directory DSECT

```

```

0000          ORG 90000
0000 DOSDIR   EQU *
0000 DOSNXT   RMB 2      link of next dir block
0002 DOSPRV   RMB 2      link of previous dir block
0004 DOSDSS   RMB 1      dir sector size (preallocated)
0005 DOSLNU   RMB 3      unused (800)
0008 DOSLEN   EQU *      length of dsect

```

```

* Define DOS DSECT for free space list info
* (starts at offset +8, trk 00 sec 01)

```

```

0000          ORG 90000
0000 DOSFSL   EQU *
0000 FSLFF    RMB 1      must be $FF
0001 FSLDC    RMB 8      don't cares ($FF)
0009 FSLDIF   RMB 1      disk interleaving factor
000A FSLDT    RMB 1      disk type
000B FSLNXT   RMB 2      link of next free block
000D FSLNST   RMB 2      trk/sec of last free block
000F FSLSC    RMB 2      count of available sectors
0011 FSLJNT   RMB 2      trk/sec of boot file
0013 FSLSPD   RMB 2      count of # sectors/disk
0015 FSLUNU   RMB 3      unused
0018 FSLLEN   EQU *      length of FSL

```

```

0100          ORG 00100

```

```

* ***** DEFINE PROMPTS USED BY ALL ROUTINES *****

```

```

*
* PDIR routine

```

```

0100 000A      EQU *
0102 4E 41 4D 45  FDB CRLF
0119 20 20 20 20  FCC / NAME TYPE BEGIN END/
0121 000A      FDB CRLF
0123 04         FCB EDT

0124 MELCOP    EQU *
0126 44 65 73 63  FDB CRLF
0140 000A 000A    FCC /Description of Selections:/
0144 20 20 20 30  FDB CRLF,CRLF
0146 000A      FCC / 0 - you're looking at it buddy!/
0168 20 20 20 31  FDB CRLF
018F 64 69 72 65  FCC / 1 - this selection will yield a DOS /
01A1 000A      FCC /directory listings./
01A3 20 20 20 20  FDB CRLF
01C0 20 6F 66 20  FCC / The user is prompted for the drive/
01DC 000A      FCC / of the DOS disk/
01DE 20 20 20 32  FDB CRLF
0205 67 69 76 65  FCC / 2 - this selection will copy a /
0218 000A      FCC /given DOS file to a/
021A 20 20 20 20  FDB CRLF
0238 20 70 72 6F  FCC / FLEX file. The user is/
0239 000A      FCC / prompted for the "from" drive/
025B 20 20 20 20  FDB CRLF
0270 20 61 6E 64  FCC / (i.e. where the DOS disk is)/
0291 000A      FCC / and the "from" file/
0293 20 20 20 20  FDB CRLF
02B3 20 6C 6F 77  FCC / name. No conversion (say/
02CA 000A      FCC / lowercase -> uppercase/
02CC 20 20 20 20  FDB CRLF
02EF 20 6E 61 6D  FCC / for example) is done on this/
0300 000A      FCC / name, so type it/

0300 000A      FDB CRLF
0302 20 20 20 20  FCC / exactly as it appears in your/
0326 20 64 69 72  FCC / directory listings./
0339 000A      FDB CRLF
033B 20 20 20 33  FCC / 3 - exit program/
034E 000A 000A    FDB CRLF,CRLF
0352 4D 6F 72 65  FCC /More to Come ...../
0369 04         FCB EDT

036A MELCO2    EQU *
036E 4E 6F 74 65  FDB CRLF,CRLF
0391 20 69 6E 73  FCC /Note: a) the user may hit [Return]/
03A5 000A      FCC / instead of typing a/
03A7 20 20 20 20  FDB CRLF
03CC 20 74 68 65  FCC / drive number. In this case/
03DF 000A      FCC / the last specified/
03E1 20 20 20 20  FDB CRLF
0404 20 2B 6F 72  FCC / drive number will be used/
0416 000A      FCC / (or the FLEX work/
0418 20 20 20 20  FDB CRLF
043F 20 64 72 69  FCC / drive in the case no previous/
043D 000A 000A    FCC / drive was given/
0454 20 20 20 20  FDB CRLF,CRLF
0478 20 72 6F 75  FCC / b) the user has the option of/
0488 000A      FCC / routing his output/
048D 20 20 20 20  FDB CRLF
04B0 20 74 68 65  FCC / from selection 1 or 2, to/
04C7 000A      FCC / the printer, terminal./
04EB 20 69 73 20  FDB CRLF
04FF 000A      FCC / or a file. If "printer"/
0501 20 20 20 20  FCC / is selected and the/
0525 20 62 65 65  FCC / FLEX printer module hasn't/
0535 000A      FCC / been loaded, it/
0537 20 20 20 20  FDB CRLF
0551 20 61 75 74  FCC / (PRINT.SYS) will/
0569 000A      FCC / automatically be loaded/
056B 20 20 20 20  FDB CRLF
058D 20 69 73 20  FCC / from drive 0. If "file"/
05A3 000A      FCC / is selected, the user/
05A5 20 20 20 20  FDB CRLF
05C7 20 22 74 6F  FCC / will be prompted for the/
05D0 000A      FCC / "to" file name. Type/
05D0 000A      FDB CRLF
05D0 20 20 20 20  FCC / this as a standard FLEX/

```

```

0690 20 66 69 6C      FCC / file specification (eg:/
0618 000A             FDB CRLF
061A 20 20 20 20      FCC /
063A 20 77 69 6C      FCC / will also be asked/
0640 000A             FDB CRLF
064F 20 20 20 20      FCC / whether the file is/
066C 20 22 74 65      FCC / "text" or "binary". If/
0683 000A             FDB CRLF
0685 20 20 20 20      FCC / type "binary" is chosen./
06A7 20 74 68 65      FCC / the FLEX space/
06B6 000A             FDB CRLF
06B8 20 20 20 20      FCC / compression flag will be/
06DA 20 73 65 74      FCC / set and no expansion/
06EF 000A             FDB CRLF
06F1 20 20 20 20      FCC / of tabs, etc. is performed./
0716 20 20 49 66      FCC / If ROUTE=P or T./
0728 000A             FDB CRLF
072A 20 20 20 20      FCC / tabs will be expanded./
074A 000A 000A         FDB CRLF,CRLF
074E 45 6E 74 65      FCC /Enter [Return] to Continue.../
076B 04               FCB BOT

```

```

076C 000A             EQU *
076E 20 20 20 20      FCC /
0787 00              OUTDRV FCB 0
0788 000A             FDB CRLF
078A 04               FCB EOT

```

```

078B 000A             EQU *
078B 20 20 20 45      FCC / End of Directory Listing. /
07A8 49 69 74 20      FCC /Hit [Return] to Continue.../
07C3 04               FCB EOT

```

```

07C4 000A             EQU *
07C4 20 20 20 45      FCC / End of Conn. Enter /
07DA 5B 52 65 74      FCC /[Return] to Continue.../
07F1 04               FCB EOT

```

```

07F2 000A             EQU *
07F2 20 20 20 45      FCC /
080E 04               FCB EOT

```

```

* BLINKOUT routine
080F 20 20 20 20      SP4 FCC / / 4 spaces
0813 04               FCB EOT

```

```

* COPYIT routine

```

```

0814 000A             EQU *
0814 20 20 20 45      FCC / Enter "from" file: /
082A 04               FCB BOT

```

```

082B 000A             EQU *
082B 20 20 20 45      FCC / Enter "from" drive: /
0842 04               FCB EOT

```

```

0843 000A             EQU *
0843 20 20 20 4F      FCC / Output to File(F), Printer(P), /
08A5 6F 72 20 54      FCC /or Terminal(Te)? /
0876 04               FCB EOT

```

```

0877 000A             EQU *
0877 20 20 20 45      FCC / Enter "to" file: /
088B 04               FCB EOT

```

```

088C 000A             EQU *
088C 46 49 4C 45      FCC /FILE DOES NOT EXIST!/
08A0 04               FCB EOT

```

```

08A1 000A             EQU *
08A1 49 4E 56 41      FCC /INVALID FILE SPECIFICATION!/
08BC 04               FCB EOT

```

```

08BD 000A             EQU *
08BD 53 4F 52 52      FCC /SORRY, FILE ALREADY EXISTS/
08D7 04               FCB EOT

```

```

08D8 000A             EQU *

```

```

08D8 20 20 20 49      FCC / Is file type Binary(B) /
08F2 6F 72 20 54      FCC /or Text(Te)? /
08FF 04               FCB EOT

```

```

* Misc
*
0900 3C 3C 3C 20      RDERR FCC /<<< READ ERROR >>>/
0913 04               FCB EOT

0914 50 52 49 4E      PSYS FCC /PRINT/
0919 00 00 00         FCB 0,0,0
091C 53 59 53         FCC /SVS/

```

```

091F 000A             EQU *
091F 000A 000A         FDB CRLF,CRLF
0923 20 20 20 46      FCC * FLEX/DOS (UTILITY (C) 1983*
093F 20 62 79 20      FCC * by Scott Fraser*
094F 000A 000A         FDB CRLF,CRLF
0953 20 20 20 20      FCC /
0968 20 20 20 20      FCC *
0986 000A             FDB CRLF
0988 20 20 20 20      FCC /
099F 20 20 20 20      FCC /
098A 000A 000A         FDB CRLF,CRLF
098E 20 20 20 45      FCC / ENTER SELECTION: /
09D2 04               FCB EOT

```

```

09D3 000A             EQU *
09D3 20 20 20 49      FCC / INVALID SELECTION. REENTER: /
09F2 04               FCB EOT

```

```

09F3 000A             EQU *
09F3 20 20 20 49      FCC /
09F2 04               FCB EOT

```

```

* Define some DOS constants

```

```

0080 SSIZ5 EQU 128      #bytes per sector (5")
0000 EOF EQU 000        delimits end of file
00FE DIREND EQU 0FE      indicates end of directory
0001 DIREND EQU 00001    trk/sec of start of DOS dir
0009 TARCEN EQU 009      universal tab char (Cntrl)

```

```

0833 000A             EQU *
0834 00              DRV RMB 1
0834 00              OPNPLG FCB 0
0080 OPN EQU 00000000    #file is open
0040 TEX EQU 001000000    #file is text
0020 BINRV EQU 000100000 #file is binary
0835 000A             EQU *
0835 20 20 20 45      TTYWID RMB 1

```

```

0080 BSIZ5 EQU SSIZ5+1  size of buffer (5")
0836 000A             EQU *
0836 20 20 20 45      BUFFER RMB BSIZ5

```

```

C100 000A             EQU *
C100 20 01          START BRA START1
C102 04               FCB 4

```

```

C103 10FE CC2B        START1 EQU *
C103 10FE CC2B        LDG MEMEND initialize system stack ptr

C107 B6 CC0C          LDA MCDRV default to work drive
C10A 87 0833          STA DRV save default
C10D 8E C119          LDH MENU
C110 BF CC16          STX ESCRR modify escape return register
C113 86 CC04          LDA WIDTH set current TTY line width
C116 87 0835          STA TTYWID and save it

```

```

C119 7F CC1A          CLR DJN
C11C 7C CC22          INC DMWCH
C11F 8E 091F          LDH #INTRO

```

```

C122 80 C01E      JSR  PSTRNG  ask for selection

      C125 PRON EQU  *
C125 80 C009      JSR  INCH     set a response
C126 80 C021      JSR  CLASS    classify it
C128 25 42        BCS  MI       bad selection
C128 81 33        CMPA  B'3     over 3?
C12F 22 3E        BHI  MI       bad selection

C131 84 0F        ANDA  #00001111 keep low 4 bits only
C133 48          ASLA          selection=selection*2

C134 8E C138      LDY  B'1     addr of jump table
C137 30 86        LEAX  A,X     addr proper selection
C139 6E 84        JMP  0,X     goto selection

C138 20 06        BT  BRA  M0   selection=0
C138 20 18        BT  BRA  M1   =1
C13F 20 20        BT  BRA  M2   =2
C141 20 29        BT  BRA  M3   =3

      C143 M0 EQU  *
C143 8E 0124      LDY  #DELCOM  do selection 0
C146 80 C01E      JSR  PSTRNG  print instructions

C149 86 C003      LDA  DEPTH    breather
C14C 87 CC1A      STA  CLN      more to come
C14F 8E 036A      LDY  #DELCO2
C152 80 C01E      JSR  PSTRNG
C155 20 20        BRA  SETPAU

      C157 M1 EQU  *
C157 80 32        BSR  PD1R     print dir
C159 8E 078B      LDY  #ENDIR
C15C 80 C01E      JSR  PSTRNG
C15F 20 16        BRA  SETPAU

      C161 M2 EQU  *
C161 17 00ED      LBSR  #00ED   copy file
C164 8E 07C4      LDY  #DELCO1
C167 80 C01E      JSR  PSTRNG
C16A 20 08        BRA  SETPAU

      C16C M3 EQU  *
C16C 7E C003      BQU  #WRMS   return to FLEX

      C16F M4 EQU  *
C16F 8E 0903      LDY  #BADIN   bad selection
C172 80 C01E      JSR  PSTRNG  print msg
C175 20 AE        BRA  PRON

      C177 SETPAU BQU  *
C177 86 C003      LDA  DEPTH    pause (if set)
C17A 87 CC1A      STA  CLN      so user can read stuff
C17D 20 90        BRA  MEN2

      C17F ERR02 EQU  *
C17F 80 C03F      JSR  #PTERR   report error first
C182 8E 0900      LDY  #RDEB0   set msg

      C185 ERR EQU  *
C185 80 C01E      JSR  PSTRNG  print msg
C188 7E C003      JMP  #WRMS   and return to FLEX

      *
      * Name - PD1R
      * Function - This routine is called to print out
      *               directory information on a DOS disk.
      *
      * No parms are required
      * All registers are used and not restored
      *
      C188 PD1R EQU  *
C188 32 E9 FFFC      LEAS  -LS1,S  alloc local storage

C18F 8E 07F2      LDY  #ORVPMT  ask for a drive
C192 80 C01E      JSR  PSTRNG
C195 17 0112      LBSR  GETDRV   set drive 0
C198 1025 0093    LBCS  PD1T     if bad then leave

```

```

C19C 17 0284      LBSR  ROUTE   where to route output
C19F 1025 008C      LBCS  PD1T   leave if bad
C1A3 80 C024      JSR  PCRLF
C1A6 86 0833      LDA  DRV      set drive
C1A9 87 C843      STA  SYSFCB+FCB0H save in FCB
C1AC 8A 30        DRA  B'0     convert to ascii
C1AE 87 0787      STA  QUITRV   save to print

C1B1 8E 076C      LDY  #DIRMSG  print msg
C1B4 17 025C      LBSR  PDATA

C1B7 8E 0100      LDY  #HEAD    print dir header
C1B9 17 0256      LBSR  PDATA

C1BD CC 0001      LDD  #DIRBEG  get beginning of directory

      C1C0 PD3 BQU  *
C1C0 17 0202      LBSR  GETBLK   get a block
C1C3 ED E9 0002    STD  TEMP,S   save next blk/sec to read

      *
      * BUFFER is full of directory data. If
      * 1st sector of directory then skip over
      * dir data and FSL stuff, otherwise just
      * skip over dir data.
      *
C1C7 8E 0836      LDY  #BUFFER  position to 1st entry
C1CA EC 02        LDD  #OSPRV,X  1st sector of dir?
C1CC 26 03        BNE  PD2      branch if not
C1CE 30 88 18      LEAX  FSLLEN,X skip over FSL info
C1D1 30 08        BQU  #        *
C1D1 30 08        LEAX  #D0SLN,X skip over dir info

      C1D3 PD1 EQU  *
C1D3 AF E9 0000    STI  #ENTADR,S save entry addr

C1D7 A6 84        LDA  FIBFN,X  valid file entry?
C1D9 27 42        BEQ  PD4      branch if not
C1DB 81 FE        CMPA  #D1PEND  end of dir ret?
C1DD 27 50        BEQ  PD4      branch if so

      *
      * Now print the file name
      *
C1DF 30 84        LEAX  FIBFN,X  field to move
C1E1 C6 09        LDB  #9       length to move

      C1E3 PD5 EQU  *
C1E3 A6 80        LDA  0,X+     set a char
C1E5 26 02        BNE  #+2+2   if -0 then skip
C1E7 86 20        LDA  #SP      *
C1E9 80 C018      JSR  PUTCHR   print the char
C1EC 5A          DEC  B          *
C1ED 27 08        BEQ  PD6      exit if all done
C1EF C1 03        CMPB  #3      just extension left?
C1F1 26 F0        BNE  PD5      branch if not
C1F3 86 2E        LDA  B'.'     print a '.' first
C1F5 80 C018      JSR  PUTCHR   *
C1F8 20 E9        BRA  PD5      and continue

      C1FA PD6 BQU  *
C1FA AE E9 0000    LDY  #ENTADR,S
C1FE 30 08        LEAX  FIBFSU,X print 1st file sector
C200 80 35        BSR  UNKOUT

C202 AE E9 0000    LDY  #ENTADR,S
C206 30 00        LEAX  FIBLSU,X print last file sector
C208 80 2D        BSR  UNKOUT

C20A 8E 080F      LDY  #SPA     print 4 spaces
C20B 17 0203      LBSR  PDATA
C210 AE E9 0000    LDY  #ENTADR,S
C214 30 0F        LEAX  FIBSUC,X print file size
C216 5F          CLRB          *
C217 80 C039      JSR  QUITREC  *
C21A 80 C024      JSR  PCRLF

      C21D PD4 EQU  *
C21D AE E9 0000    LDY  #ENTADR,S
C221 30 88 18      LEAX  FIBLEN,X goto next file entry
C224 8C 08D6      CMPB  #D173 finished?
C227 25 AA        BLD  PD1      branch if not

```

```

C229 EC E9 0002 LDD TEMP,S read in next sector
C22D 26 91 BNE PD3 read in next sector and continue

C22F PD3IT EQU *
C22F 17 02C7 LBSR RESET clear things up first

C232 32 E9 0004 LEAS LS1,S release local storage
C236 39 RTS return

```

• Data area for PDIR routine

```

0000 C237 LPC SET *
0000 ORG 00000
0000 LCL SET *
0000 ENTADR RMB 2
0002 TEMP RMB 2
0004 LS1 EQU 4-LCL len of local area

C237 ORG LPC restore PC

```

• Name - LINKOUT
• Function - This routine takes in a pointer to a 2 byte hex link field (ptr in X) and prints it as 2 one byte fields separated by a "-".

All registers are preserved

```

C237 LINKOUT BQU *
C237 34 12 PSMS A,X save registers
C239 86 20 LDA BSP print a space first
C23B 8D C018 JSR PUTCHR
C23E 8D C018 JSR PUTCHR

```

```

C241 8D C03C JSR OUTHEX
C244 30 01 LEAX 1,X
C246 86 20 LDA 0-
C248 8D C018 JSR PUTCHR
C24B 8D C03C JSR OUTHEX

```

```

C24E 35 12 PULS A,X restore registers
C250 39 RTS and return

```

• Name - COPVIT
• Function - This rtn is called to copy a DOS file to a FLEX file. The user is prompted for a "from" file (off the DOS disk) and a "to" file (in FLEX). If the "to" file is not specified, output goes to the terminal.

All registers are used and not restored

```

C251 COPVIT EQU *
C251 32 E9 FFF7 LEAS -LS3,S alloc local storage

C255 8E 082B LDI 01NDRV set "from" drive
C258 8D C01E JSR PSTRNG
C25B 8D 40 BSR GETDRV get drive#
C25D 25 43 BCS DP2 if bad then leave

C25F 8E 0814 LDI 01NFILE prompt for "from" filename
C262 8D C01E JSR PSTRNG
C265 8D C01B JSR INBUF
C268 30 E9 0000 LEAX SVEFIL,S where to put filename
C26C 8D 61 BSR GETSPEC get DOS filename
C26E 25 32 BCS DP2 if bad then leave

C270 17 009C LBSR SPMCH so do search
C273 24 08 BCC GETD if found, get "to" file

```

• File not found in DOS directory -> flash and repropt user.

```

C275 8E 08BC LDI 0BADFIL print msg
C278 8D C01E JSR PSTRNG

```

```

C27B 30 25 BRA DP2 return

```

• The DOS directory entry has been found.
• Get the beginning track/sector of the file and continue transfer until end link found

```

C27D 86 0833 LDA DRV get drive #
C280 87 C843 STA SYSFCB+FCBDM save in FCB
C283 17 019D LBSR ROUTE get route
C286 7D 0834 TST DP4FLG writing to a file?
C289 26 03 BNE DP1 yes, then no lead CRLF

C28B 8D C024 JSR PORLF print CRLF first
C28E DP1 EQU *
C28E EC 08 LDD FIBFSU.X get start of file
C290 84 3F ANDA 0100111111 keep low 6 bits of trk
C292 C4 1F ANDA 0000111111 keep low 5 bits of sec
C294 DP4 EQU *
C294 1083 C000 CMPO 010000 end of file?
C298 27 08 BEQ DP2 branch if so

C29A 17 012B LBSR GETBLK get a block of data
C29D 17 00BF LBSR PRIBLK and print it
C2A0 20 F2 BRA DP4 continue

```

```

C2A2 DP2 EQU *
C2A2 17 0254 LBSR RESET clear things up first

C2A5 32 E9 0009 LEAS LS3,S release local storage
C2A9 39 RTS return

```

• Data area for COPVIT routine

```

0000 0000 LCL SET *
0000 0000 SVEFIL SET *
0009 LS3 EQU 4-LCL len of local storage

C2AA ORG LPC restore PC

```

• Name - GETDRV
• Function - This routine gets a drive # from the terminal. The user can either enter a drive # in the range 0-3, or type null, in which case the default drive# in "DRV" will be taken.

The carry is clear if # ok, else it is set. The valid drive number is returned in ACC A, and is also saved in DRV.

All registers are preserved.

```

C2AA GETDRV EQU *
C2AA 34 34 PSMS 0,1,Y save regs

C2AC 8D C009 JSR INDI get response
C2AF 81 00 CMA BCR use default?
C2B1 26 05 BNE GET1 branch if not

C2B3 86 0833 LDA DRV get default
C2B6 20 0E BRA GET4 and save it

```

```

C2B8 GET1 EQU *
C2B8 8D C021 JSR CLASS classify it
C2BB 25 00 BCS GET2 bad drive
C2BD 81 33 CMA 0'3 in range 0-3?
C2BF 22 09 BHI GET2 bad drive

```

```

C2C1 84 0F ANDA 0100001111 keep low 4 bits
C2C3 87 0833 STA DRV save in DRV storage
C2C6 GET4 EQU *

```

```

C206 1C FE      CLC      set good return
C208 20 02      BRA      GET3      and return

      C20A GET2 EQU      *
C20A 1A 01      SBC      set bad return
      C20C GET3 EQU      *
C20C 36 34      PULS     B,I,Y      restore regs
C20E 39          RTS              and return

*
* Name - GTSPEC
* Function - This routine parses the system [NSUF
*            buffer for a DOS file name. A valid
*            filename must contain up to 6 chars,
*            then an optional "." and up to 3 char
*            file extension. The X reg points to
*            an area to place the 9 char file name
*            and any unused chars are padded with
*            spaces.
*
*            Carry is clear if file spec ok, else
*            it is set.
*
*            All regs are preserved.
*
      C20F 34 36      C20F GTSPEC EQU      *
      PSMS     A,B,I,Y      save regs

*
* First blank out the "to" field
*
      C2D1 96 00      LDA      00      pad with zeroes
      C2D3 A7 84      STA      0,X
      C2D6 31 01      LEAY     L,X      "to" field
      C2D7 CC 0008     LOD      09-1     length to move
      C2DA 17 0112     LBSR     MVC      move in spaces

      C2DB 108E C080     LDV      00AEBCF pt to system buffer
      C2E1 86 09      LDA      09      # chars to move

      C2E3 E6 A4      LDB      0,Y      user just hit return?
      C2E5 C1 08      CMPB     #CR
      C2E7 27 21      BEQ      GTBAD     yes, then bad return
      C2E9 E6 A0      EQU      *
      LDB      0,Y+      get a char

      C2EB C1 20      CMPB     #SP      space here?
      C2ED 27 FA      BEQ      GT1      skip spaces

      C2EF C1 2E      CMPB     #".      have an extension
      C2F1 26 0A      BNE      GT2      no, then continue

*
* The rest of the filename from the system
* buffer is the extension. Adjust the
* "to" ptr so that any unused chars in the
* "to" name are spaces
*
      C2F3 1F 89      TFR      A,B      copy A
      C2F5 C0 03      SUBB     #3
      C2F7 29 11      BHI      GTBAD     if neg then bad name
      C2F9 30 85      LEAX     B,X      adjust "to" ptr
      C2FB 20 EC      BRA      GT1      and continue

      C2FD GT2 EQU      *
      C2FD C1 00      CMPB     #CR      end vet?
      C2FF 27 05      BNE      GT3      done if so

      C301 E7 30      STB      0,X+      save to "to" field
      C303 4A 0003     DECA
      C304 26 E3      BNE      GT1      brnch if not

      C306 GT3 EQU      *
      C306 1C FE      CLC      good return
      C308 20 02      BRA      GTGOOD
      C30A GTBAD EQU      *
      C30A 1A 01      SEC      set bad return
      C30C 35 36      GTGOOD PULS     A,B,I,Y
      C30E 39          RTS              restore regs
                        and return

* Name - SRCM
* Function - This routine searches the directory on
*            a DOS disk for the file name pointed to
*            by the X register. *SY9FCB* is the FCB
*            used, and is assumed to contain the
*            drive where the DOS disk lies
*
*            On exist, X->DOS file entry of found
*            file. If file is not found, the carry
*            is set, otherwise it is clear
*
*            All registers are preserved
*
      C30F 34 26      C30F SRCM EQU      *
      C311 32 E9 FFFE     PSMS     A,B,Y      save regs
      LEAS     -LSS,S      alloc local storage

      C315 CC 0001      LDD      #DIRBEG start with 1st dir sector

      C318 17 00AA      C318 SRC1 EQU      *
      LBSR     STD      TMP4.S      get a block
      C318 ED E9 0000     LDV      #BUFFER save next sector to read
      C31F 108E 0B36     LDV      #BUFFER V-> dir data
      C323 EC 22      LDD      DISP4R.Y is this 1st dir sector?
      C325 26 03      BNE      SRC3      brnch if not
      C327 31 A8 18      LEAY     FSLLEN.Y skip over FSL info
      C32A 31 28      C32A SRC3 EQU      *
      LEAY     DOSLEN.Y skip over dir info
      C32C 31 28      C32C SRC2 EQU      *
      LDA      FIBFN.Y      get 1st char of file name
      C32E 27 11      BNE      SRC4      goto next entry if so
      C330 81 FE      CMPA     #DIREND end of directory?
      C332 27 1C      BNE      SRC1IT brnch if so

*
* Compare dir file with file set down to
* see if a match
*
      C334 34 20      PSMS     Y      save entry ptr

      C336 31 A4      LEAY     FIBFN.Y Y->string2
      C338 86 09      LDA      09      len to compare
      C33A 17 00C8     LBSR     CLC      compare

      C33D 35 20      PULS     Y      restore entry ptr
      C33F 27 13      BEQ      FOUND     brnch if a match

      C341 31 A8 18      C341 SRC4 EQU      *
      LEAY     FIBLEN.Y go to next entry
      C344 108E 0B86     CMPY     #BUFFER+BS1Z5 finished?
      C348 25 E2      BLO      SRC2      brnch if not

      C34A EC E9 0000     LDD      TMP4.S note next dir sector
      C34E 26 C8      BNE      SRC1      and continue search

      C350 1A 01      C350 SRC1IT EQU      *
      SEC      file not found
      C352 20 04      BRA      RET      return

      C354 1C FE      C354 FOUND EQU      *
      CLC      file found
      C356 1F 21      TFR      Y,X      X-> found entry
      C358 20 00      RET      *

      C35B 32 E9 0002     LEAS     LSS,S      release local storage
      C35C 35 26      PULS     A,B,Y      restore regs
      C35E 39          RTS              and return

*
* Data area for SRCM routine
*
      C35F 0000      C35F LPC      SET      *
      ORG      $0000
      0000      LCL      SET      *
      TMP4      RMB      2
      LSS      EQU      *-LCL      len of local storage

      C35F 0002      ORG      LPC      restore PC

```

```

* Name - PRTBLK
* Function - This routine dumps the contents of
*            BUFFER to the standard output.
*
*            All registers are preserved
*
C35F 34 36 C35F PRTBLK EQU 0
PSHS A,B,I,Y save regs

C361 8E 0B36 LDX #BUFFER point to buffer area
C364 30 04 LEAX 4,X skip over link info
C366 5F CLRB start column counter at 0

C367 8C 0B86 EQU *
C36A 27 56 CMPI #BUFFER+BSIZ end of buffer yet?
C36C 46 80 BEQ PB3 branch if so
LDA 0,X get a char

C36E 34 04 PSHS B save col ctr
C370 F6 0B34 LDB #OPNFLG get open flag bits
C373 C1 A0 CMPI #OPN+BINRY file open and binary?
C375 35 04 PULS B restore col ctr
C377 27 43 BEQ PB4 branch if so

C379 81 0A CMPI #LF line feed?
C37B 27 EA BEQ PB1 skip if so

C37D 81 0D CMPI #CR carriage return?
C37F 26 0B BNE PB2 branch if not

C381 5F CLRB clear column counter
C382 7D 0B34 TST #OPNFLG writing to a file?
C386 26 35 BNE PB4 branch if so
C387 80 C3C4 JSR #PCRLF print a CR/LF
C38A 20 0B BRA PB1

C38C B1 09 EQU *
C38E 26 1C CMPI #TABCON a tab char?
BNE PB6 branch if not

```

* Encountered a tab character. If submitting
 * to a file, the tab must be converted to the
 * proper spaces, depending on the current
 * column.

```

C390 34 04 PSHS B save current column ctr
C392 C4 07 ANDB #00000011 B = mod(curcol,B)
C394 84 0B LDB #B
C396 34 04 PSHS B
C398 A0 E0 SUBA 0,S+ A = B - mod(curcol,B)
C39A 34 02 PSHS A save this value too
C39C 1F 89 TFR A,B B = #spaces to insert

C39E 86 20 C3A0 PB5 LDA #SP
C3A0 BD C01B EQU *
C3A3 5A 04 JSR #PUTCHR output a space
C3A4 26 FA BNCB until all done
BNE PB5

C3A6 35 04 PULS B set back # spaces
C3A8 E3 E0 ADDB 0,S+ add to cur column ctr
C3AA 20 8B BRA PB1 and continue

C3AC 4B C3AC PB6 EQU *
C3AD 2C 0D TSTA snc compression byte?
BGE PB4 branch if positive

```

* The char represents a DOS space compression
 * byte, and indicates the 2's complement of
 * the actual number of spaces that should
 * appear at this point. For example, 2
 * spaces is represented as -2 (X'FE').
 * Expand this to the proper number of spaces.

```

C3AF 40 NECA convert to eye ant
C3B0 1F 09 TFR A,B transfer to acc B
C3B2 86 20 LDA #SP
C3B4 8D C3BA PB7 EQU *
C3B8 BD C01B JSR #PUTCHR output a space
C3B7 5A DECB

```

```

C3B8 26 FA BNE PB7
C3BA 20 AB BRA PB1 set another character

C3BC PB4 EQU *
C3BD BD C01B JSR #PUTCHR output a char as is
C3BF 5C 04 INCB incr col ctr
C3C0 20 A5 BRA PB1 continue
EQU 0
C3C2 35 36 C3C2 PB3 EQU 0
C3C4 39 RTS A,B,I,Y restore regs
and return

```

* Name - GETBLK
 * Function - This routine takes in a trk/sector
 * value in acc D, and reads that sector
 * into BUFFER.
 *
 * On exit, acc D contains the next
 * trk/sec to be read (or #0000 if none)
 *
 * All registers are preserved

```

C3C5 GETBLK EQU *
C3C5 34 30 PSHS I,Y save registers

C3C7 8E C3A0 LDX #SYSFCB point to an FCB
C3C9 8D 19 BSR #READSS read a sector
C3CC 1026 F0AF LBNB #ERR02 branch if error

```

* Transfer bytes just read, to BUFFER

TO BE CONTINUED

DISK SPEED MEASUREMENT

DISK SPEED MEASUREMENT

Mike Jeffrey
 787 Continental Circle, 1213
 Mt View, CA 94040

Have you ever been unable to read a file written on one drive,
 but readable on another drive? If so, the problem may be
 inaccurate drive speed. The following utility allows a user to
 make a periodic check of floppy disk drive speed. This
 utility, known as DISKSPD.CMD, works by selecting a drive and
 making ten separate measurements of the time necessary for
 the disk to complete one rotation. These measurements are stored
 in memory until the tenth measurement has been completed, and
 then a simple plot is produced on the user's terminal. A typical
 usage of DISKSPD.CMD looks like this:

```
+++DISKSPD,1
```

```

SLOW          CORRECT          FAST
-51           V               +51
*
*
*
*
*
*
*

```

If the number of the drive to be tested is not entered on the
 command line, this program will prompt the user for the required
 input. Before taking the time to enter this utility into
 your own system, please make note of the following fact. This
 utility assumes the disk controller directly in order to
 measure the time for a single rotation of the disk.
 Therefore, it may require some modifications for some systems.
 My system is set up like most GATC systems, i.e. with the Disk

Controller board in slots 5 & 6, or memory locations \$D014 through \$D018. If your system has a disk controller board at the same location using the popular 1771 or 179x series of chips, then this utility will probably run as is.

DISK SPEED CHECK UTILITY

THIS UTILITY COMMAND CHECKS THE MOTOR SPEED OF THE SPECIFIED DRIVE. A 5 INCH DISK MUST BE IN THE SPECIFIED DRIVE. NO WRITING TO THE DISK IS PERFORMED. A SOFTWARE TIMING LOOP IS USED; THEREFORE, TIMING ROUTINES MUST BE CHANGED ON NON 1 MHZ SYSTEMS.

SAMPLE USAGE:
***DISKSPD,1 (CHECKS SPEED OF DRIVE 01)

WRITTEN BY: MIKE JOHNSON
707 CONTINENTAL CIRCLE, 01213
MT VIEW, CA 94040

BASED ON A ROUTINE WRITTEN BY JOSEPH AULICINO
PUBLISHED IN '60 MICRO JOURNAL' 1 FEB 1984

LAST MODIFIED: 14 FEB 84

ND179: EQUATE - NOTE: USER MUST MATCH
THIS POINT TO HIS OWN SYSTEM. THIS IS
NOT A STANDARD FLEX MEMORY POINT

E010 CONREG EQU \$E010 179x COMMAND REGISTER

FLEX EQUATES

\$E09 RESTOR EQU \$DE09
\$CC03 TTYEOL EQU \$CC03
\$C027 NITCN EQU \$C027
\$C015 GETCHR EQU \$C015
\$C01E PSTRNG EQU \$C01E
\$C010 PUTCHR EQU \$C010
\$C024 PCRLF EQU \$C024
\$C03 WARMS EQU \$C003

C100 ORG \$C100 FLEX UTILITY SPACE

C140 20 17 DSXSPD BRA START

C102 01 VM FCB 1 VERSION NUMBER

C103 DRVMNR RND 1 DRIVE NUMBER TO TEST
C104 TIME RND 20 SPACE FOR 10 TIME VALUES
C110 TSTCNT RND 1 NUMBER OF TIMES TESTED

GET DRIVE NUMBER FROM COMMAND LINE OR
PROMPT USMR IF NOT THERE

C119 00 C027 START JSR NITCN GET DRIVE NUMBER FROM COMMAND LINE
C11C 01 CC03 CMA TTYEOL WAS IT MISSING ON COMMAND LINE?
C11F 27 0E BEC PROMPT
C121 01 00 CMA 0000 WAS IT MISSING ON COMMAND LINE?
C123 27 0A BEC PROMPT
C125 00 20 SUBA 0024 CONVERT ASCII TO BINARY
C127 01 01 CMA 01 MUST BE DRIVE NUMBER 0 OR 1
C129 102E 003A LOST ERROR NO SUCH DRIVE
C12D 20 0F BRA SELECT DRIVE OK. GO SELECT IT

C12F 0E C140 PROMPT LDI BPRMSG POINT AT PROMPT MESSAGE
C132 00 C01E JSR PSTRNG
C135 00 C019 JSR GETCHR GET RESPONSE
C138 00 30 SUBA 0030 CONVERT ASCII TO BINARY
C13A 01 01 CMA 01 MUST BE DRIVE NUMBER 0 OR 1
C13C 2E 29 BEC ERROR NO SUCH DRIVE

SELECT THE SPECIFIED DRIVE

C13E 07 C103 SELECT STA DRVMNR SELECT DRIVE
C141 0E C103 LDI DRVMNR GET READY FOR CALL TO FLEX
C144 30 10 LEAI -3,1
C146 00 DE09 JSR RESTOR
C149 20 3A BRA TIMER

C148 44 52 49 56 PRMSG FCC 'DRIVE NUMBER TO BE TESTED?', 4
C14F 45 20 4E 35
C153 40 42 45 52
C157 20 54 0F 20
C15B 42 45 20 54
C15F 45 53 54 45
C163 44 3F 20 04

C167 0E C170 ERROR LDI BPRMSG
C16A 00 C01E JSR PSTRNG PRINT IT
C16D 7E C003 JMP WARMS RETURN TO FLEX

C170 49 4E 56 41 BPRMSG FCC 'INVALID DRIVE NUMBER', 4
C174 4C 49 44 20
C178 44 52 49 56
C17C 45 20 4E 35
C180 40 42 45 52
C184 04

LOOP THROUGH TIMING ROUTINE 10 TIMES, STORING
RESULTS IN TIME TABLE FOR DISPLAY LATER.

C185 C6 0A TIMER LDI 010 DO TEN TIMING TESTS
C187 0E C104 LDI 0100 POINT AT START OF TIME TABLE

FIND FIRST INDEX PULSE

C18A 1A 10 SET L0A 0000 DISABLE INTERRUPTS FOR TIMING LOOP
C18C 06 00 LDA STA 0000 179x 'FORCE INTERRUPT' COMMAND
C18E 07 E010 STA CONREG INSURES 'TYPE 1' STATUS
C191 17 0020 LSR DELAY WAIT 32 USEC FOR 179x

WAIT LDI 010000 CLEAR TIMING REGISTER
C198 06 E018 LDA CONREG LOOK FOR INDEX PULSE
C19B 04 02 ANDA 0002 IN BIT 1 OF CONREG
C19D 27 F3 BEC WAIT LOOP UNTIL PULSE DETECTED

TIMING LOOP BEGINS TO INCREMENT AFTER THE
INDEX PULSE IS DETECTED, THROUGHOUT THE
INTERVAL BETWEEN PULSES, AND STOPS WHEN
THE INDEX PULSE IS DETECTED AGAIN.

C19F 31 21 CNTHI LEAY 1,Y INCREMENT TIMING REGISTER WHILE
C1A1 21 FC BRN CNTHI STILL IN INDEX PULSE?
C1A3 12 LDA CONREG STILL IN INDEX PULSE?
C1A6 06 E018 ANDA 0002
C1A7 04 02 BNE CNTHI YES, CONTINUE TO INCREMENT TIMER

C1AB 31 21 CNTLOW LEAY 1,Y INCREMENT TIMER UNTIL NEXT
C1AD 21 FC BRN CNTLOW INDEX PULSE DETECTED
C1AF 12 LDA CONREG STILL WAITING FOR INDEX PULSE?
C1B0 06 E018 ANDA 0002
C1B3 04 02 BNE CNTLOW YES, CONTINUE TO INCREMENT TIMER

STORE RESULT OF TEST

C1B7 10AF 01 STY 0,X++ STORE RESULT IN TIME TABLE
C1BA 3A DEC B DONE 10 TESTS YET?
C1B0 26 D7 BNE WAIT NO, DO ANOTHER TEST
C1B0 1C EF CLT ALLOW INTERRUPTS AGAIN
C1B7 20 07 BRA GRAPH 60 GRAPH RESULTS

C1C1 17 0000 DELAY LSR DELI DELAY ROUTINE
C1C4 17 0000 DELI LSR DEL
C1C7 39 DEL RTS

TAKE RESULTS OF 10 TESTS NOW STORED IN TIME
TABLE AND GRAPH RESULTS ON SCREEN. A PERFECTLY
ACCURATE DRIVE WILL YIELD A COUNT OF DEC 10000
(HEX 2710). THAT VALUE WOULD RESULT IN A POINT
BEING PLOTTED IN THE CENTER OF THE GRAPH.
A LOWER VALUE REPRESENTS A FASTER DRIVE AND
VICE VERSA. THE GRAPH CONSISTS OF 31 'BINS',
RANGING FROM APPROX 5% SLOW TO 5% FAST.
THE CENTER BIN WILL HOLD POINTS RANGING FROM
9990 TO 10010, WITH 25 EQUAL SIZED BINS
(REPRESENTING .2% INCREMENTS) ON EITHER SIDE
OF CENTER.

C1C8 0E C215 GRAPH LDI 0HEADER PRINT HEADER
C1CB 00 C01E JSR PSTRNG
C1CE C6 0A LDI 010 PLOT 10 TIME DATA POINTS
C1D0 F7 C110 STB TSTCNT
C1B3 0E C104 LDI 0100 I WILL BE POINTER INTO TIME TABLE

CHECK FOR DATA POINT OUTSIDE RANGE OF GRAPH

RESULT LDI 09490 SEE IF MORE THAN 5% FAST
C1D0 10AC 04 CMPI 0,1
C1D0 102E 00AC LOST FAST
C1E1 100E 290E LDI 010510 SEE IF MORE THAN 5% SLOW
C1E5 10AC 04 CMPI 0,X
C1E8 102D 0096 LBLT SLOW

DATA POINT NOT TOO HIGH OR TOO LOW. SO
PLOT IT.

C1EC 100E 290E LDI 010510 EDGE OF SLOWEST CELL
C1F0 31 00 EC LEAY -20,Y 20 IS SIZE OF CELL
C1F3 10AC 04 CMPI 0,1 IN THIS CELL?
C1F6 2F 07 BLE PLTPNT YES, GO PLOT POINT
C1F8 04 20 LDA 0020 NO, SO SKIP TO NEXT CELL
C1FA 00 C010 JSR PUTCHR PRINT THE SPACE
C1FD 20 F1 BRN PLOT
C1FF 06 2A LDI 010 PLTPNT
C201 00 C010 JSR PUTCHR
C204 00 C024 JSR PCRLF
C207 30 02 LEAI 2,1
C209 F6 C110 LDI 010
C20C 5A DEC B
C20D F7 C110 STB TSTCNT
C210 26 C4 BNE RESULT

C212 7E C003 JMP WARMS
C215 00 0A HEADER FCC 000,000
C217 33 4C 4F 57 FCC 'SLOW' CORRECT

C218 20 20 20 20
C21F 20 20 20 20
C223 20 20 20 20
C227 20 20 20 20
C22D 20 20 43 4F
C22F 52 52 45 43
C233 54
C234 20 20 20 20
C238 20 20 20 20
C23C 20 20 20 20
C240 20 20 20 20
C244 20 20 46 41
FDC FAST, 000, 000

```

C240 33 54 00 0A
C24C 20 25 25 20
C250 20 20 20 20
C254 20 20 20 20
C258 20 20 20 20
C25C 20 20 20 20
C260 20 20 20 20
C264 20 20 20 20
C268 20 20 20 20
C26C 20 20 20 20
C270 20 20 20 20
C274 20 20 20 20
C278 20 20 20 20
C27C 20 20 20 20
C280 25 00 0A
C284 04

FCC '52'

C28C 06 3C SLOW LBA 0'< PUTCHR PRINT II
C294 00 CD10 JSR PCRLF
C298 0D CD24 JSR PCRLF
C29C 16 FF7A LBR4 NITPNT

C2B0 C6 32 FAST LBB 050 SPACE OVER 50 SPACES
C2B4 06 20 SKIP LBA 0520
C2B8 00 CD10 JSR PUTCHR PRINT A SPACE
C2BC 5A 00 DECB 000E YET?
C2C0 26 F8 DMB SKIP NO, PRINT ANOTHER SPACE
C2C4 06 3E LBA 0'>
C2C8 0D CD10 JSR PUTCHR
C2CC 0D CD24 JSR PCRLF
C2D0 16 FF65 LBR4 NITPNT

END DSKSPD

```

4 ERROR(S) DETECTED

PC

Enclosed please find a program I call "PC" which is a printer driver that prints in multicolumn form. I know that we've seen an awful lot of printer drivers lately, but I think that this one provides something not yet offered.

I do a lot of long listings -- most often LISTS of committee reports and ASSEMBLIES of programs. I wrote this driver to conserve paper. Basically, it is used like "P", but it intercepts and stores all output until it has enough to fill 2 pages, then prints it all out in 2 column format on one page. It assumes the availability of a 132 column printer -- I use an Epson MX-80 in the condensed mode. I have found an additional benefit of this program is that it helps me to comprehend long assembly listings because you get to see more of the listing at one time. You use the program as shown;

```

+++pc, list myfile (or)
+++pc/assembly of program PC, asmb pc, pc.cmd

```

The first form will cause the paginated output with a page number and the date at the top of each page. The second form works the same, but adds a title ("assembly of program PC" in this case) to the top of each page. Note that when you use LIST, you should not tell it (LIST) to do paging, because this will interfere with the paging from PC. Note also that PC will cut off all output beyond column 64 of any listing -- you may want to keep a normal listing of programs for your final documentation so that you can read your comments.

For the most part, the accompanying listing should be self-explanatory (at least I still understand it several months later!). Some quick comments are in order however.

The printer size setup characters are defined by equates near the beginning of the program ("NARROW equ 15", "NORMAL equ 18"). The ones shown here work on my Epson, and allow me to have the headings in normal width while the body of the text shows as condensed. If you have a different printer you will need to use its setup characters. If your platen is 14" wide, then you could just set these both to NULL and have everything come out in

standard width.

My program lives at \$F000 because I have free ram there -- you may want to put it somewhere else. Note though that the driver cannot live in the utility area because it has to stay around while the "other" program executes. Note also that PC grabs a lot of ram under MEMEND to use for its buffer.

The routine at MYPOUT is what actually does the work. It replaces FLEX's normal OUTCH routine. Mostly, it stores incoming characters in the text buffer until it becomes full, then calls routine PRNTPG to actually print the buffer. MYPOUT checks incoming characters to see if they are CR or FF and adjusts the buffer pointer accordingly. For CR characters, it counts the line and advances the pointer to the next "line" position. If the line it terminates would have been the bottom one on a page, it sets flags to indicate a column change. If it had been storing on the right-side column then it calls PRNTPG. Incoming FF characters also cause a column change (and PRNTPG if needed).

Most of the other stuff is pretty obvious. Routine PRNTPG does the actual printout of a page, calling routine TOPAGE to print a tearline and header. TOPAGE calls routines PRPAGE (advance page count and print out the number), and PRODATE which prints the date from FLEX's date registers. Routine INITPG initializes the paging buffer and most of its pointers. The buffer is always initialized to hold all spaces (\$20) so that pointer moves within the buffer (i.e. from CR or FF) don't need to worry about the characters they pass over. Location LDATA always points to the start of the left column image, RDATA points to the start of the right column image. BEGLIN points to the start of the current line (this is advanced by LINWID=64 after each CR) and CURPNT points to the location which will receive the next incoming character. Locations LCNTL and LCNTR count lines on the left and right columns respectively (LNCOL=60 determines how many go into a column). CHRCNT counts characters stored in the current line (it is initialized to LINWID=64 after each CR and counts down with each incoming character -- when it reaches zero we ignore characters until a CR or FF). Finally, LRFLAG indicates which column is currently being filled (0=left, 1=right).

You may add this file to your bulletin board system for those who have access to it. For those who don't have modems, I will supply this and a couple other similar and slightly useful programs on an 8" disk for \$10.

Just for background, I have been using 68XX computers at work since sometime around the dawn of recorded history, running Motorola's MDOS. About 2 years ago I built my own home machine using a 6809 processor and have been running FLEX on it for most of that time. I use 64K of MCM6665s controlled by an MC6883 SAM chip. My disk controller is based on a Western Digital WD1793 running programmed I/O which was a mistake because I cannot run double density disks in that mode. Someday I will probably build a DMA controller to fix this. I use a Radio Shack TRS-80 Model I as a terminal -- which job it does rather poorly, and it will be replaced whenever I can afford it. I am a EE and was introduced to computers from the electronics end, but seem to be doing more software than hardware of late. I am also a licensed amateur radio operator (WB8KDU), but haven't been active in that field for quite a while.

Hope you like the program !

Sincerely,

Don Korte

Donald N. Korte
2714 Thomas
Flint MI 48504
(313) 238 6716

'68' Micro Journal

```

; for a new page, and fills the page with
;
F153 C6 20 initpg ldb 0SPACE
F155 10BE 0F00 ldy 0COLS12
F159 BE F344 ldx ldata point to left colu
F15C E7 80 init01 stb 0,x+ store space
F15E 31 3F leay -1,y
F160 26 FA bne init01
F162 10BE 0F00 ldy 0COLS12
F166 BE F346 ldx rdata point to right col
F169 E7 80 init02 stb 0,x+ store spaces
F16B 31 3F leay -1,y
F16D 26 FA bne init02
F16F BE F344 ldx ldata
F172 BF F34C stx beglin
F175 BF F34E stx curpnt pointers ready
F17B 86 3C lda 0LNCOL
F17A B7 F349 sta lncntl line counter--left
F17D B7 F34A sta lncntr line counter--right
F180 B6 40 lda 0LNUID
F1B2 B7 F34B sta chrnt character counter
F1B5 7F F34B clr lrflag say we're on left
F1B8 39 rts

;
; routine prntpg physically prints
; the stored page image. While printing,
; reg x points into left column,
; reg y points into right column,
; reg b counts lines
; loc chrnt counts chars in a column
;
F189 BD 40 prntpg bsr topage print header junqu
F1BB BE F344 ldx ldata left col
F1BE 10BE F346 ldy rdata right col
F192 C6 3C ldb 0LNCOL
F194 B6 40 ppg lda 0LNUID
F196 B7 F34B sta chrnt character counter
F199 A6 80 ppg1 lda ,x+ get char for left
F19B BD CCE4 jsr POUT and print it
F19E 7A F34B dec chrnt end of "line" ?
F1A1 26 F6 bne ppg1 not yet
F1A3 B6 20 lda 0SPACE
F1A5 BD CCE4 jsr POUT so print
F1A8 BD CCE4 jsr POUT some spaces
F1AB BD CCE4 jsr POUT for beauty
F1AE B6 40 lda 0LNUID
F1B0 B7 F34B sta chrnt reinit character c
F1B3 A6 A0 ppg2 lda ,y+ get char for right
F1B5 BD CCE4 jsr POUT and print it
F1B8 7A F34B dec chrnt end of line ?
F1BB 26 F6 bne ppg2 nope
F1BD B6 8D lda 0NULINE
F1BF BD CCE4 jsr POUT so make it happen
F1C2 5A decb done with page?
F1C3 26 CF bne ppg no--go print anoth
F1C5 B6 9C lda 0MFEEED
F1C7 BD CCE4 jsr POUT so move paper up
F1CA 39 rts

;
; routine topage prints tear line, header i
; and advances page counter
;
topage pshs x,y,d
ldx 0POUT restore normal pri
stx OUTCH+1 so PSTRNG works di
ldx 0header
jsr PSTRNG " --- "
bsr prpage print page num
bsr prdate print date
ldx 0title
jsr PSTRNG pprint user's titl
ldx 0head1
jsr PSTRNG end of it
ldx 0aypout now back to interc
stx OUTCH+1 printer driver...
puls x,y,d
rts

;
; routine PRPAGE advances page count,
; then prints page number
;
prpage ldx page
leax 1,x advance count
stx page
ldx 0page point to counter
ldb 01 want leading space
jsr OUTDEC print number
rts

;
; routine prdate prints spaces then the dat
;
prdate ldb 040 forty
lda 0SPACE spaces
prdate jsr OUTCH are
decb printed
bne prdate1 by this !

; print date
lda SYSDAT+1 day
sta num+1 temporary storage
clr num+0 which is 2 bytes 1
ldx 0num point to it
clrb (want spaces suppr
jsr OUTDEC

; print month
lda SYSDAT+0 month 1-12
deca month 0-11
asla 12
asla 14
ldx 0months table of month nan
leax a,x point at proper na
ldb 04 length is 4 chars
prdate1 lda ,x+ print one of them
jsr OUTCH (here)
decb done yet?
bne prdate2 not yet...
lda 0SPACE a space for
jsr OUTCH the sake of beauty

; print year
ldd 01900 base year
addb SYSDAT+2 year

```

```

F23D 09 00          adca 00      double precision!
F23F FD F340        std  num      keep in mem
F242 BE F340        ldx  #num     point to it
F245 5F             clrb          (spaces suppressed)
F246 BD CD39        jsr  OUTDEC
F249 39             rts

#####
; now error routines
#####

F24A A6 01          err2  lda  1,x    get error code
F24C 01 04          capa 0NFER      is it "no such fil
F24E 26 09          bne  err3        nope
F250 30 8D 0014      leax  nopst,pcr point to message
F254 0D CD1E          err25 jsr  PSTRNG print message
F257 20 03          bra  err4
F259 BD CD3F          err3  jsr  RPTERR report harder erro
F25C 0D D403          err4  jsr  FMSCLS close all files
F25F 7E CD03          jmp  WARMS      return to FLEX
F262 30 8D 0021      err8  leax  erstr,pcr point to string
F266 20 EC          bra  err25        and print it...

;
F268 65 72 72 6F      nopst fcc      /error: "PRINT.SYS" not foun
F26C 72 3A 20 22
F270 50 52 49 4E
F274 54 2E 53 59
F278 53 22 20 6E
F27C 6F 74 20 66
F280 6F 75 6E 64
F284 20 21
F286 04              fcb  4
F287 75 73 61 67      erstr fcc      "usage: PC,command or PC/ti
F28B 65 3A 20 50
F28F 43 2C 63 6F
F293 6D 6D 61 6E
F297 64 20 20 6F
F29B 72 20 50 43
F29F 2F 74 69 74
F2A3 6C 65 2C 63
F2A7 6F 6D 6D 61
F2AB 6E 64
F2AD 04              fcb  4
F2AE 12 2D 2D 2D      header fcc      NORMAL,/---/,MULINE
F2B2 0D
F2B3 50 41 47 45      fcc      /PAGE: /,4
F2B7 3A 20 04
F2BA 0F 0D 04          head1 fcb      NARROW,MULINE,4
F2BD 20 4A 41 4E      months fcc      / JAN FEB MAR APR MAY JUN/
F2C1 20 46 45 42
F2C5 20 4D 41 52
F2C9 20 41 50 52
F2CD 20 4D 41 59
F2D1 20 4A 55 4E
F2D5 20 4A 55 4C      FCC      / JUL AUG SEP OCT NOV DEC/
F2D9 20 41 55 47
F2DD 20 53 45 50
F2E1 20 4F 43 54
F2E5 20 4E 4F 56
F2E9 20 44 45 43

;
F2ED              lncnt  rdb  1          line counter (rev

```

```

F2EE              page  rdb  2          page counter
F2F0              title rdb  80
F340              num   rdb  2
F342              oldend rdb  2          stores orig MEMEND
F344              ldata rdb  2          -> start of left c
F346              rdata rdb  2          -> start of right
F348              lrflag rdb  1          0=left 1=right
F349              lncntl rdb  1          line count left si
F34A              lncntr rdb  1          line count right s
F34B              chrct  rdb  1          char count "this"
F34C              beglin rdb  2          -> start current l
F34E              curpnt rdb  2          -> current char
;
; the following code is loaded into
; the system FCB when the PC command is
; executing (load of pc fails if this
; is stored by org in this source)
F350 FF            forfcb fcb  0FF
F351 50 52 49 4E      fcc      /PRINT/
F355 54
F356 00 00 00        fcb  0,0,0
F359 53 59 53        fcc      /SYS/

;
;
end pc

```

0 ERROR(S) DETECTED

SYMBOL TABLE:

COLS12 0F00	DOCMND CD4B	EOL CC02	FCB CB40	FNFEED 0
FMS D406	FMSCLS D403	LNCOL 003C	LNMID 0040	LOAD C
LSTTRM CC11	MENEND CC2B	NEWS12 1E00	NARROW 000F	NFER 0
NORMAL 0012	MULINE 000D	NITCH CD27	OUTCH CD0F	OUTDEC C
PAUSE CC09	PINIT CCC0	POUT CCE4	PR1 CCFC	PSTRNG C
RENTER CD06	RPTERR CD3F	SPACE 0020	SYSDAT CC0E	WARMS C
beglin F34C	cfmfd F133	chrct F34B	clfcr F0D9	curpnt F
err2 F24A	err25 F254	err3 F259	err4 F25C	err8 F
erstr F287	forfcb F350	head1 F2BA	header F2AE	init01 F
init02 F169	initpg F153	ldata F344	llfcr F0FB	lncnt F
lncntl F349	lncntr F34A	lrflag F34B	months F2BD	oyput F
oypret F0D7	nopst F26B	num F340	oldend F342	pl F
pl2 F012	pl21 F02B	pl3 F037	pl4 F04E	pl5 F
page F2EE	pc F000	ppg F194	ppg1 F199	ppg2 F
prd01 F207	prd02 F22A	prdate F203	prntpg F189	prpage F
rdata F346	rfmfd F14B	rlfcr F10E	rlfcre F12B	title F
topage F1CB	vn F002			

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C Programmer's Library
\$19.95 (B. Dalton price)
Author: Dr. Jack Purdum, Tim Leslie & Alan Stegemiller
A Staff Review

```

0010 * PROGRAM 'CELESTE', C.O. EARLE, JANUARY, 10 2.
0020 *
0030 * COMPUTES TOPOCENTRIC DIRECTION OF THE PLANETS
0040 *
0050 * AZIMUTH= ANGLE, IN DEGREES, POSITIVE CLOCKWISE FROM NORTH
0060 * ELEVATION= ANGLE, IN DEGREES, POSITIVE ABOVE THE HORIZON
0070 *
0080 DIM A(8),L(3),M(7),U(3),A(3),M(9),I(1)
0090 LET B=P1/10018TRING-71LIN-1081COSUS 1010
0100 *
0110 HOME INPUT 'LATITUDE(M,NORTH, -SOUTH)',L1
0120 PRINT INPUT 'LONGITUDE(WEST, -EAST)',L2
0130 PRINT INPUT 'TIME ZONE',Z5
0140 PRINT INPUT 'DATE(MONTH, DAY, YEAR)',M5,M5,Y5
0150 PRINT INPUT 'TIME(HOURS, MINUTES, SECONDS)',M1,M1,S1
0160 PRINT INPUT 'DAYLIGHT SAVINGS TIME(1=YES, 0=NO)',F1
0170 *
0180 * COMPUTE JULIAN DATE, OBLIQUITY AND SIDEREAL TIME
0190 GOSUB 3010
0200 *
0210 * COMPUTE GEOCENTRIC POSITION OF THE SUN
0220 LET K=91COSUS 3010
0230 LET X1=X2=Y10 Y2
0240 *
0250 HOME PRINT 'PLANETARY MENU:PRINT
0260 FOR I=1 TO 8PRINT TAB(18);I;A(I);NEXT I
0270 PRINT INPUT 'SELECTION',K
0280 *
0290 * COMPUTE GEOCENTRIC POSITION OF PLANET
0300 GOSUB 3080X=X1+Y2Y=Y1+Y2
0310 LET Y4=Y3+E (E1)-22*01W(E1)Z3=Y308W(E1)+22*08(E1)
0320 *
0330 * COMPUTE TOPOCENTRIC POSITION OF PLANET
0340 LET B=908123002-Y4Y4+Z3+Z3
0350 LET U(1)=X3/R1U(2)=Y4/R1U(3)=Z3/R1COSUS 4010
0360 *
0370 PRINT 07;TAB(5);'PROGRAM CELESTE'PRINT 07;PRINT 07;TAB(5);A(6)
0380 PRINT 07;PRINT 07;TAB(5);'LATITUDE',L1TAB(5);'LONGITUDE',L2
0390 PRINT 07;PRINT 07;TAB(5);'DATE',M5M5Y5TAB(5);'TIME',M1M1S1
0400 PRINT 07;IF E<0 THEN PRINT 07;TAB(5);'BELOW THE HORIZON'
0410 PRINT 07;PRINT 07;TAB(5);'AZIMUTH',0/R1TAB(5);'ELEVATION',-E/R1
0420 *
0430 PRINT INPUT 'ANOTHER BE ECTION(1=YES, 0=NO)',F1IF F=0 END
0440 PRINT INPUT 'ANOTHER LOCATIO 1=YES, 0=NO',F1IF F=1 GOTO 110
0450 PRINT INPUT 'ANOTHER ATE(1=YES, 0=NO)',F1IF F=1 GOTO 140
0460 PRINT INPUT 'ANOTHER TIME(1=YES, 0=NO)',F1IF F=1 GOTO 150
0470 GOTO 250
0480 *
1000 * READ PLANET NAMES AND ORBITAL ELEMENTS DATA SUBROUTINE
1010 FOR I=1 TO 9FOR J=1 TO 11READ M(I);J;NEXT JNEXT I
1020 FOR I=1 TO 9READ A(I);NEXT IRETURN
1030 *
1040 DATA .3070984, .20561421, 2.046E-5, .122223323, 3.2477668E-5
1050 DATA .501047459, .086462615, .022051959, .020605700
1060 DATA 1.70511195, 2.600.70753, .7233316, .00602069, -4.774E-5
1070 DATA .059230027, 1.7555103E-5, .949103109, .000069521
1080 DATA 1.32260435, .015705345, 3.71862610
1090 DATA 1021.32835, 1.5236915, .0933129, 9.2064E-5
1100 DATA .032294409, 21.1700972E-5, 4.90172402, .016670954
1110 DATA .001404037, .013456434, 5.57604053, 324.05355
1120 DATA 5.202561, .04033475, .00016410, .022041754
1130 DATA -9.9415092E-5, 4.76959207, .01042056, 1.73561451
1140 DATA .017637076, 3.93271222, 52.9653676, 9.554747
1150 DATA .05509232, -3.455E-4, .043502671, -6.0397514E-5
1160 DATA 5.98450451, .0109406742, 1.96056385, .0152401302
1170 DATA 3.84264321, 21.3200951, 19.21014
1180 DATA .0463444, -2.450E-5, .013402302, 1.0913156E-5
1190 DATA 1.7116715, .017204044, 1.20241727, .000703395
1200 DATA 1.2679411, 7.47662597, 30.10957, .00009704
1210 DATA 6.33E-6, .0310536253, -1.6656744E-4, 4.01791101
1220 DATA .005603401, 2.20001997, .01910034, .450524400
1230 DATA 3.01200901, 39.517730, .2406430, 0, .29926720
1240 DATA 0, 1.9013255, 0, 1.90161466, 0, 4.0133377
1250 DATA 2.5292055, 1.00000013, .01675104, -4.10E-5, 0, 0
1260 DATA 1.74643601, .02005264, 0, 0, 6.25450350, .620.30194
1270 *
1280 DATA MERCURY,V MARS,JUPITER,SATURN,URANUS, PTUNE,PLUTO
1290 *
2000 * JULIAN DATE, OBLIQUITY AND SIDEREAL TIME SUBROUTINE
2010 LET J1=367*Y5-INT(7*(Y5+INT((M5+9)/12)/4))/INT(27*365/9)+.05+1721613.5
2020 LET T1=M1+(M1+01)/A0+25-F1T2=(J1-2415020)/36525
2030 LET T3=T2+T1/07440081E+.409319755-2.27110494E-4KT3
2040 LET S2=1.73993509+620.33195KT2-SGN(L2)*L2ZNR0
2050 LET S3=S2+.242516163T1*RETURN
2060 *
2080 * HELIOCENTRIC POSITION FROM ORBITAL ELEMENTS SUBROUTINE
2090 LET M(1)=M(6);L1U=IF04 I=2 TO 6
2020 LET M(I)=(M(I)+M(1))/J1KT3U=J2NEXT IIA=M(6);0=N(2)
2030 LET S4=SGN(A1);S5=SGN(Z4A);S6=SGN(Z3A);S7=SGN(Z4A)
2040 LET C=B*(2464+B*(5455/4+B*(13X56-3M54)/12)+B*(103X57-44X051/96)))
2050 LET M(7)=C+M(6);IA=M(1);M(1)=M(2);M(2)=C/(1+M(2)*COS(M(7)))
2060 LET B=M(5);C=COS(M(4)+M(7));S5=SGN(M(4)+M(7));IF M(3)
2070 LET X2=A*(COS(B)*C-S2M(8)*COS(F)*S5);Y2=A*(S2M(8)*C-COS(B)*COS(F)*S5)
2080 LET Z2=A*S5*SGN(F)*RETURN
2090 *
4000 * EQUATORIAL TO TOPOCENTRIC TRANSFORMATION SUBROUTINE
4010 LET A=SGN(L1)*B=COS(L1)*R0;C=SGN(S3);D=COS(S3)
4020 LET A(1)=A*NDIA(1);Z3=MARCIA(1);B=B
4030 LET A(2)=A*(1+C1A(2));H0IA(2);B=B
4040 LET A(3)=B*NDIA(3);Z3=MARCIA(3);A=A
4050 FOR I=1 TO 310=01FOR J=1 TO 310=04(I+J)*U(J);NEXT J;L1(I)=S1NEXT I
4060 LET A=ATAN(L(3)/B0A(1)-L(3)/L(3));L(3)=L(3)
4070 LET A=L(2)/COS(L1);B=L(1)/COS(L1)
4080 IF ABS(A)<1E-8 THEN 0:RETURN
4090 LET B=(Z3-BN(A))/S1IF ABS(B)<1E-8 RETURN
4100 LET D=B-C1A(2)*SGN(B)*ABS(ATAN(A/B))-S0P3;RETURN

```

A very recent addition to the how-to of 'C' books is the C Programmer's Library. Published by QUE Corp. And burdensomely corrected by this reviewer, with three looseleaf pages of corrections to, of all things, the source files included.

This is not intended to be a complete review, as is our policy, but more an 'alert' to those of our readers who might contemplate purchase of this book. While instructive in its 'text' portions, the source files (which to many are the most important parts) are rife with errors, omissions, deletions, typo foulups, etc. So much so that it becomes difficult to use them as study material. After spending the better part of an hour correcting the book, I found myself wondering how many other errors are there, but not attended to. So much so that I have set it aside until I feel most of the goofs have been found and rectified for the reader.

For the educator, intent on referring his/her students to solid C material, this is NOT it! Mainly due to the faults mentioned. The source code (corrected) may compile and run, but the distraction of always wondering what other errors lurk within to snare the unwary student, makes for an unsatisfactory learning situation.

Even the manner of correction is stupidly approached. Instead of reprinting each page in error so that it may be attached by clear tape, or what have you, without a lot of fuss, the corrections are noted, one after the other, for 3 full pages (of the four copies of this title on the bookstore shelf, not all had the corrections loosely inserted, one of our editors purchased a personal copy the night before we purchased our review copy, his does not have any correction sheets inserted). Try getting all that in troops, without leaving something out. You did? Well, maybe.

Another very annoying point is the type font selected by either the authors, publisher or both. It is called 'Digital'. It has one especially bad drawback, the character 'l' (ell) and the numeral '1' (one) are so similar that it is practically impossible to tell them apart.

Example:

```

12byte = sizeof(struct i2_link) + max_i2 * (sizeof(struct i2_line) +
         ikeylen - 1);
13byte = max_i3 * (sizeof(struct i3_line) + ikeylen - 1);

```

```

temp = sizeof(struct global) + ikeylen - 1 + max_i3 * (sizeof(
struct s_line) + ikeylen - 1) + 12byte + 13byte +
(12byte > 13byte ? 12byte : 13byte) + sizeof(
struct recov) + (r_max - 1) * sizeof(long);

```

Is it one two bytes = ?
Or is it 1(ell) two bytes = ?

Page after page of this type of mental jabbing by the font renders it practically useless as tutorial material. Also there is another major fault in this type of font selection.

1. Had they used a plain vanilla dot matrix (or daisy wheel) output, it would have been far more legible, as most computer types spend many hours eyeballing dot matrix fonts (most anyone can read a daisy wheel output without much difficulty), the eye and mind understand them. Not all of us are privy to 'Digital' (or want to be)!

2. Also, had they used the dot matrix font it would probably have been spit out by a computer of some sort. One that most likely also compiles C. This being the case (no pun intended), then it would not have contained all the other errors or omissions. Because if an attempt to compile the source first had been made (a logical assumption) then the printed output would have been correct. Or the system would have 'bombed' the compile session, as the source is presented in this book, thereby alerting someone to the fact of omissions and/or errors existing. The results would probably have been a book that I could wholeheartedly recommend. As it is, unless you like to cut out things and paste them into places where they may or may not fit easily, and enjoy puzzles of this sort (looking for additional errors, etc.), I would recommend holding off on this one.

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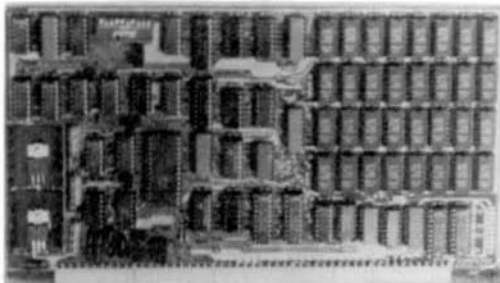
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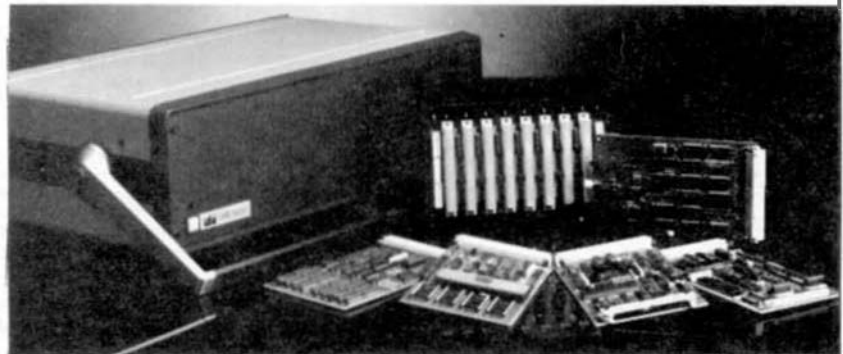
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All-In-One, Write'n Spell, and Spell'n Fix package \$250.00

Software by Technical Systems Consultants, Inc.

	Source (List)	Source (Disk)	Man. Only	Object w/Man.	Add. Man.	UniFLEX™ Man. Only	Object w/Man.
Gen FLEX w/Edt & ASMB	—	—	25	250	—	—	—
FLEX 9.1 (OC-2) w/Edt & ASMB	—	—	25	150	40	100	550
Advanced Programmers Guide	—	—	25	—	—	—	—
Editor	100	250	25	50	—	—	—
Assembler	150	250	25	50	—	—	—
Debugger	175	250	25	75	—	—	—
Extended Basic	—	—	25	100	20	50	200
Basic Precompiler	—	—	25	50	10	25	150
Sort/Merge	—	—	25	75	20	35	150
Utilities	—	Inc.	25	75	10	25	150
Diagnostics	—	—	25	75	—	—	—
Text Processor	150	250	25	75	20	35	150
68000 X-ASMB on 6809	—	—	25	250	20	35	300
Pascal	—	—	50	200	25	50	300
Rot ASMB/Linking Loader	—	—	25	150	20	35	175
6800 X-ASMB on 6809	—	—	—	100	—	—	—
Cobol	—	—	—	—	30	75	750
Fortran 77	—	—	—	—	35	65	450

Software by Microware Systems Corp.

	Run-Time Package	Source	Manual Only	Object w/Man.
OS-9™ Level 1 w/Edt, Asm, Debug	—	400.00	40.00	250.00
OS-9™ Level 2 w/Edt, Asm, Debug	—	400.00	40.00	500.00
OS-9™ Edit, Asm, Debug Pkg	—	—	25.00	125.00
Device Driver for Disk Controller (Specify Model)	—	100.00	—	—
Device Driver for ACIA and PIA	—	50.00	—	—
Clock Driver for 6840 and 8167 clock chips	—	35.00	—	—
Entertainment Pack I, or File Handler Toolbox, or NineCom	—	—	10.00	5.00
Print Spooler (Level 2 only)	—	—	15.00	95.00
Virtual Disk Driver (Level 2 only)	—	—	10.00	5.00
RMA/Relocatable Macro Assembler	—	—	20.00	125.00
RMA/68000 Cross Assembler	—	—	40.00	400.00
BASIC09™ W/Run-Time	—	50.00	25.00	200.00
BASIC09™ To I Guide Book	—	—	18.95	—
"C" Compiler	—	—	25.00	250.00
C Programming Language (Kernighan & Ritchie)	—	—	19.95	—
CIS Cobol Compiler w/Forms 2 Prog. Gen.	—	50.00	N/A	400.00
Pascal Compiler	—	50.00	N/A	250.00
Sage Application Generator	—	300.00	N/A	995.00
Microware yearly support service (IAR products)	—	—	—	150.00
Edition Update w/manuals	—	25.00	—	75.00

Special Software
STAR-DOS Level 1 (Specify ELEKTRA or DC-2) \$75.00 Adaptation guide \$50.00
2K MICROBUG 40.00 4K HUMBUG 75.00 Custom versions \$85.00
Spell'n Fix by Peter Stark 178.58 Write'n Spell by Peter Stark 75.11
All-In-One, Spell'n Fix, and Write'n Spell package \$250.00
SUPER SLEUTH Disassembler System (\$101.00 for OS-9 version) 99.00

	1 head	2 heads	2 heads	1 head	2 heads
	Tandon	Tandon	CDC	MPI	MPI
5-1/4", 40 tracks	225.00	300.00	300.00	250.00	325.00
3-1/4", 80 tracks	300.00	375.00	375.00	325.00	400.00
MPI or CDC Service Manual (Specify 40 or 80 tracks)	—	—	25.00	25.00	550.00

OUTBOARD EPROM PROGRAMMERS BY OPTIMAL TECHNOLOGY
Model EP-2A-79 (Personality modules extra) \$169.00
Optimal Technology, Inc. 30 pin parallel I/O board for EP-2A-79 \$37.00
FLEX™ Software package for EP-2A-79 (Specify 6800 or 6809) \$30.00
OS-9 Software package for EP-2A-79 \$10.00
Model EP-2B-87 (RS-232/20 MA, Motorola Imi-8K buffer, 1200/3600 baud) \$75.00
Model EP-2B-88-4 (Copies 1 to 4 EPROMS) \$550.00
Personality/Copy Modules for 2708, 2716, 27C16, 2732, 27C32, 2732A, 2758, 2758A, 2758B, 2764, 27C64, 2764A, 27128, 27128A, 27258, 27C258, 2508, 2516, 2532, 2564, 25128, 2816, 2816A, R87C32, 8751, 38E70 \$17 to \$39

SPECIALS
U.S. Robotics 1200 baud direct connect auto answer modem \$299.00
SSB BFO Floppy Disk Controller (Version 3) Run FLEX or SSB DOS \$100.00
SWTPC 4K Memory \$15.00 MP-Mb (4K bareboard) 9.95
SWTPC MP-N (Kit) \$54.95 S-32 RAM not included \$124.50
SWTPC MP-D9 2MHz CPU \$295.00 While supplies last
High speed tape reader \$50.00 300 Baud acoustic modem \$129.00
T1810 Printer w/low case and full critical forms control \$1200.00

SPECIAL BOARDS
Microlime II Calendar and Clock Board (Assembled) \$60.00
Oala Mat 16K EPROM bareboard (2708 chips) \$30.00

Smoky Signal Broadcasting
DCB-4A Double Density Controller Board for 5" and 8" with DOS \$49.00
SSB DOS (Specify 6800 or 6809, BFO or DCB-4A, 5" or 8") \$76.00
SSB version of FLEX™ (While supply lasts) \$50.00
LMB-1A Motherboard \$399.00
SCB-68 6809 CPU Board \$399.00
Chief 9524 64K Computer System with OS-DT-00 5" FO \$4325.00
Dynamic Memory Boards M-128 X 995.00 M-256 X 1295.00 M-512 X 1895.00

HELIX
64K 6809 Computer \$2395.00 Other computer systems available
DMA 5" and 8" Floppy Controller \$495.00 6809 CPU Board \$495.00
68008 board for SS-50 \$595.00 CP-M-68K \$350.00

	LIST	OUR PRICE		LIST	OUR PRICE
Cable (Par I/O)	24.95	20.00	6800 CPU board	224.03	100.00
Double disk reg. card	68.22	50.00	#28 controller w/Flex	328.28	270.00
64 X 16 Video Boards	199.71	100.00	Single prt ser, 1 cable	113.38	90.00
16K Mem Bds w/chrtl reg.	145.00	145.00	Dual prt par, 2 cables	138.32	110.00
93L422 DAT chip	17.50	15.00	4K PPD PROM Bds and burner	100.00	100.00

WARNING AAA Chicago Computer Center does not provide a repair or diagnostic service for customer assembled kits. AAA Chicago Computer Center does warranty and maintain service for our assembled boards. The customer should carefully take into consideration the small differential separating out kit and assembled prices when making his choice of purchase.

One other annoying gotcha. Nearly one half of this book is devoted to developing ISAM type source code files. A worthy exercise as well as a worthy addition to a C programmer's library (what this is supposed to be all about, I think). However, not all is rosy in this respect. Another library program offered in this book is called 'isamstr.h', a structure declaration file. From this book, page 206, I quote:

"The struct recov declares a structure that is used to recover data blocks which have become empty through the deletion of one or more keys. The library example in this book allows key deletion but does not list the deletion code. (This code, however, is included on the software disk that is available separately from Que Corporation.)"

The rear most part of the book list this disk, as well as some others, with convenient order cards and a 800 phone number for ordering. However, if I had been typing in all the code and using this book, as a 'library' development tool (which the title leads one to believe), and then finally arriving at page 206 only to find out that I need to invest an addition \$124.95 to complete my newly developed C library function, BANG!!

Right up front, on page one (!) they told me that the source code (all of it) is available if I don't want to type it all in, but they wait until page 206 to slip me the \$124.95 shaft. NEAT!

When, and if, they get all the errors printed correctly in this book, I suspect it just might be one of your favorites. Maybe by then most can figure out how to code the missing \$124.95 portion.

Oh well, after all it is tutorial, isn't it?

DMW - - -

SSB 68000



SMOKE SIGNAL
31338 Via Colinas
Westlake Village, California 91362

news release

NEW 68000 SYSTEM FROM SMOKE SIGNAL
SETS INDUSTRY PRICE STANDARD

Westlake Village, CA. With a list price of \$7500, the new VAR/68K desktop computer from SMOKE SIGNAL may be the lowest price fully configured 68000/UNIX computer on the market today.

A VAR/68K Model VK-5XW5 comes standard with: 256Kb RAM, one 80 track floppy, one 5Mb Winchester, eight serial and one Centronics type parallel ports, an 8 MHz MC68008 processor and Regulus, a UNIX compatible operating system produced by Alcyon. Also included in the price of any VAR/68K computer is an ergonomically-designed terminal with a green phosphor swivel screen and contoured keyboard. The system can easily be expanded to contain up to 1M bytes of RAM and sixteen RS-232 serial ports (and up to four 8 bit parallel ports). Other models in the VAR/68K series contain 1/4" streaming tape drives and Winchesters with up to 80M bytes of storage. Larger disk storage is available with standard SMOKE SIGNAL rack mount Winchesters. SMOKE SIGNAL will also offer OS-9/68000 when it becomes available and a third party will offer CP/M-68K for the VAR/68K series.

SCB-68K CPU BOARD

At the heart of this new computer is the SCB-68K CPU Board developed in-house at SMOKE SIGNAL. This standard (5 1/2" by 9") SS-50 board contains 20 address and 8 data lines and can directly address up to 1 Mbytes of RAM. The SCB-68K can plug into any 6809 based SMOKE SIGNAL computer, converting it to a 68000 based machine without changes to any current revision SMOKE SIGNAL computer boards. The SCB-68K contains a real-time clock with battery back-up, an eight-bit user programmable sense switch which can be read by software and allows custom configuring of hardware and software. The board also can contain up to 64K bytes of EPROM in two 28 pin sockets. The SCB-68K requires less than 2 amps at 5 volts.

A dynamic address translator on the SCB-68K allows for dynamic address translation on 4Kb boundaries. Each 4Kb memory segment can be write, execute and access protected. The dynamic address translator contains provision for four segment tables to co-reside in the high-speed mapping memory. This high-speed memory does not induce any wait states. Hardware circuitry provides for automatic task switching to the supervisor task

table whenever the processor is executing in supervisor mode, minimizing the time required to switch between the user and supervisor modes.

The standard system contains an on-board monitor derived from MACSBUG/VERSABUG. This EPROM also contains an auto-boot loader, a single-line mnemonic assembler and disassembler and a down-loader for S1, S2 and S3 type records. SMOKE SIGNAL also offers an optional Diagnostics EPROM which contains facilities for checking RAM, I/O ports, disk controllers, the real-time clock, the dynamic address translator and the CRC's of on-board EPROM. The SCB-68K can handle external DMA transfers at up to 2M bytes per second and the processor can transfer 1.6M bytes per second in programmed I/O mode. The board contains proprietary circuitry to allow the processor to run at an even throughput during external memory accesses.

SMOKE SIGNAL also makes available an optional Test Pod/Bus-State Analyzer. The test pod plugs into a 50 pin connector on the SCB-68K allowing processor signals to be monitored on the external device. The test pod contains displays for the 20 address lines, 8 data lines, an eight-bit error latch plus various other CPU status conditions. Hardware breakpoints can be in RAM, ROM or I/O and are selected via 20 switches on the test pod. A companion single-step function can be implemented with a push button on the pod. Buttons are also provided for software Abort and hardware Reset.

REGULUS OPERATING SYSTEM

Regulus is a UNIX-like operating system which is source code compatible with UNIX V-7 and S-III and supports shared memory, record-locking, real-time tasks and contains a shell similar to the Berkeley C shell. Regulus comes standard with C, an editor, assembler, linking loader, interactive debugger and word processor and accommodates all UNIX software. Also currently available for Regulus from SMOKE SIGNAL are RM Cobol, SMC (Basic-4 type) BASIC, FORTRAN, DIBOL, Pascal, UltraCalc spread sheet, Lex word processor and Unify, the popular relational data base manager. SMOKE SIGNAL is planning on offering the Sage application generator in the near future. A version of Regulus which is source code compatible with UNIX S-V will be available soon.

Multiple user's are handled more efficiently due to the memory sharing and disk swapping capabilities of Regulus. Multiple users require only a single image of a shared program. Programs and data are automatically swapped from memory to disk allowing several users to run programs when they would otherwise exceed available memory. Disks are buffered and use internal cache in order to speed up I/O operations. There can be more than one buffer for a disk file.

Files can be protected against access by owner, public or groups. The operating system automatically prevents a user from accessing memory not owned by the user. For maximum security, password files can be read but the password itself is encrypted.

Several standard features make the operating system extremely easy to use. An on-line help facility can access disk files to retrieve portions or all of the Regulus users manual. ALIAS is a shell function which allows users to assign a name to replace a standard command name without changing disk files. HISTORY is a shell function which displays the previous 70 commands executed. The shell allows these commands to be re-executed or edited by line number, one-at-a-time or in groups. The shell supports conditionals and variables. Two processes can use named pipes to exchange data. File names can be moved from one directory to another, or within a directory. The same file can be given different names.

Regulus supports the UNIX "Mail" utilities including "NEWS". Messages written to another terminal are displayed immediately unless the user has disabled the receive message function. NEWS can be used to generate a single message which is passed to all users at time of log-on. The super-user can systematically shut-down the system, sending warning messages to all users.

Several Regulus functions allow for system monitoring. Program performance can be checked using the TIME command which returns the execution time for that program only. The disk usage command allows the user to summarize a specific directory's usage. A file system check utility allows files to be recovered; it puts the disk back in order by checking the free list against the used part of the disk plus checks for valid directory entries.

VAR/68K models which contain tape streamers can be used for image back-up and files can also be copied singly or merged into groups and then retrieved singly.

SMOKE SIGNAL will begin shipments of the first 100 VAR/68K computers in June. Delivery will be SMOKE SIGNAL's standard two to four weeks. Prices range from \$7500 for the VK-5XW5 with all of the standard features described above, up to less than \$20,000 for a system with 80M bytes of Winchester disk, 60M bytes of tape streamer back-up, 1M bytes of RAM and 16 ports. Call Don Simonsen at (818) 889-9340 for more information, or write SMOKE SIGNAL at 31336 Via Colinas, Westlake Village, California 91362.

HELIX

Hazelwood Computer Systems

Computer Systems Center of Hazelwood

PRESS RELEASE

O'Fallon, Missouri
April 20, 1984
For Immediate Release

Contact: Mike Smith

Hazelwood Computer Systems is pleased to announce the availability of the MC-20 Mass Storage Controller Board which is capable of interfacing to up to 4 floppy disk drives and a 6801 compatible hard disk controller.

The MC-20 is a 34 pin board which is compatible with the S-30 and S-64 buses. The extra 4 pins allow the MC-20 to address the full 16 Megabyte range of the S-64 bus.

The MC-20 is equipped with an on-board 6809 CPU running at 2MHz with 56K RAM, up to 64K EPROM, a 6840 timer, and a 6850 ACIA (for maintenance mode).

All data as well as disk control/status information is transferred via direct memory access (DMA).

The MC-20 uses a high level protocol to communicate with the host computer which allows logical block numbers to specify the disk address. This removes the tedious and time consuming chore of logical to physical disk address calculation from the host computer. This protocol also allows the specification of multiple block data transfers.

The processing capability of the MC-20 allows other advanced features such as data buffering, hard disk segmentation, automatic motor shutdown and software settable write precompensation and motor shut-off delay.

The MC-20 will control up to 4 floppy disk drives of any size, data rate and step rate, as well as single or double density and single or double sided.

The hard disk segmentation feature allows the drive to be divided into any number of independent logical drives so that any number of operating systems may reside on a single hard disk. It is possible to load the system (boot) from any logical segment with the HELIX(TM) system.

Since the on-board 6809 controls access to both the floppy disk and the hard disk, the DMA arbitration problems found on other systems are totally eliminated thus simplifying software driver design and operation.

The MC-20 uses a high clock rate digital data separator (15MHz) thus eliminating the need for any adjustments.

Cost is \$695.00

Delivery is stock to 30 days.

Hazelwood Computer Systems is pleased to announce the availability of the CP-08 68008 CPU Board.

The CP-08 is a standard S-30 plug-in module which is compatible with existing S-30 and S-64 computers and associated peripheral controllers and interfaces.

The CP-08 is jumper settable for either a, b, or 10 MHz operation. There are additional jumper settings for EPROM type (2716 or 2732), battery power (on/off), and wait state insertion.

Memory management hardware consists of a base/limit register set which is active in user mode. In supervisor mode all addresses are passed through unchanged.

A battery backed-up real time clock and 2K scratchpad ROM as well as a 6840 timer are standard.

The CP-08 achieves high performance and throughput by means of a proprietary synchronization circuit which catches the asynchronous timing of the 68008 to the synchronous timing of the S-30/S-64 bus. (The CP-08 does NOT use the performance robbing VPR method of 6821 synchronization.)

Other notable features include a single cycle DMA facility (no dead cycles) and processor select logic to allow the CP-08 to reside on the bus with the CP-09 6809 board.

The CP-08 is currently available with Microware's OS9/68K Operating System.

Cost is \$593.00

Delivery is stock to 30 days.

Hazelwood Computer Systems is pleased to announce the availability of the OS9/68K(TM) Operating System for the HELIX(TM) Computer System.

Hazelwood Computer Systems is proud to be the first S-30/S-64 bus manufacturer to license this fine operating system from Microware Systems Corporation of Des Moines, Iowa.

The OS9/68K system includes the operating system, a line editor, a screen editor, an assembler, and a debugger.

The OS9 operating system is the standard for 6809 systems and now is available for the 68008 system from Hazelwood Computer Systems.

OS9/68K retains all of the outstanding features of OS9 such as multi-user, multi-tasking capability and in addition allows larger user space and higher performance due to the processing power of the 68008 CPU.

The HELIX with OS9/68K is capable of accessing disk files from 6809 OS9 thus easing the transition to OS9/68K for 6809 users.

A 'C' compiler is available at extra cost.

Using the processor select feature of the HELIX CP-08 and CP-09 boards as well as the hard disk segmentation feature of the MC-20 controller, a HELIX system is capable of running either 6809 OS9 or OS9/68K without the need to remove the cover or change boards or disks.

Cost is \$250.00

Delivery is from stock.

OS9 and OS9/68K are registered trademarks of Microware Systems Corp. HELIX is a trademark of Hazelwood Computer Systems.

907 E. Terra

O'Fallon, Mo. 63366

314-281-1055

Dear Sir:

I saw Don Williams' and Robert Key's editorials in the April issue of 68W, which prompted me to write down my experiences with Educational Microcomputer Systems, Inc. of Irvine, California. You may have seen their small ads in 68W and other magazines.

Their 68K single board reputer is a real product that works as advertised, but I would recommend it only to someone who has had a lot of experience with microprocessors and is willing to do a lot of work. What you see for your \$99.95 is a pretty good printed circuit board and some of the worst documentation I've ever seen. I think the reason they call themselves Educational Microcomputer Systems is because building their system is an education. The assumption behind their documentation is that you know what you are doing.

The 68K board contains a 58000 microprocessor, two 64K ACIAs, an 8095 programmable timer, an unswitched 16-bit bidirectional parallel port, 768 of 2116 static RAM and 8K of 2712 EPROM with a Macintosh compatible monitor connector. The RAM is addressed from \$00000 to \$003FF and \$00400 to \$007FF. The ROM is addressed from \$02000 to \$021FF. Because address pins A10-A23 of the 68000 aren't connected to anything, only the first 512K of the 1M megabyte potential memory map is available. Most of the first 256K of memory is dedicated to device and ROM expansion, leaving only the second 256K for RAM expansion.

Very little software is available from EMS. The Macintosh compatible 68KROM is available for \$135. A partial listing (actually a copy of Motorola's 68KROM documentation) is available for \$15. A cross assembler that runs under CP/M is available for \$149. I haven't examined the cross assembler simply because I don't have a CP/M computer. You can also get FIC-PORT in EPROM for \$110 and a PORT assembler for \$60, also in EPROM. Or you can get a version of FIC-PORT and an assembler in listing form from Mountain View Press for \$25.

I've been using my old SWTPC 6800 as a host computer for the 68K. A smart terminal program lets me upload or download programs from disk in "31-39" paper tape format, which lets me use the 6800 system like a 6800-based teletype. I've been working my way through the Kane, Reelink and Lanthal books, 68000 Language Programming, published by Osborne/McGraw-Hill. What I would really like to see is a 68000 cross assembler that would run under Plan 9 and not cost a fortune.



T & H Engineering, Inc.

P. O. BOX 8099 - MESA, ARIZONA 85204

(602) 992-9079

PRESS RELEASE

**Low Power Memory Board for EXORciser®
Features Extended Addressing and ROM Substitution.**

A 64K byte static CMOS memory board, featuring low power consumption and targeted for the OEM and end user market, is being offered by T & H Engineering, Inc. The board is completely compatible with Motorola EXORciser® and Micromodule® form-factor and electrical interface. It provides extended address decoding, RAM/ROM capability, and two megahertz operation.

The M64KB memory board comes fully populated with 64K (65,536) bytes of high speed, low power CMOS static RAM. Each of the thirty-two 2Kx8 memory devices is installed in a gold plated contact socket; any of the memory devices may be replaced with an industry standard ROM, PROM or EPROM. The board is configured as eight separately enabled 8K blocks for ease of integration into existing systems. Extended address decoding comes standard for use with one or two megabyte addressable systems, although the board is completely compatible with standard 64K byte systems. Fast access time allows the board to be used in one or two megahertz systems. The board requires a single 5 volt supply and typically uses only 2 watts of power. A high quality, fully solder masked board features extensive board labeling, allowing easy in-the-field board reconfiguration. Each board is covered by a one year conditional warranty.

**Universal Prototype Board for EXORciser®
Features Extensive Annotation and Ground/Power Planes.**

A prototype board for use in constructing user-custom circuits is being offered by T & S Engineering, Inc. The board features extensive labeling, ground and power planes, and allows use of .3, .4, .6, and .9 inch pin spaced packages.

The UE universal prototype board is completely compatible with the Motorola EXORcisor[®] and Micromodule[®] form-factor and electrical interface. The board is tin-lead plated, with gold plated card edge fingers; and a fully gold plated board is also available. Heavy-duty ground and power planes are featured for low-noise. Plated-thru holes in the ground and power planes are generously distributed for installation of decoupling capacitors. All edge connector fingers are labeled with their pin number, and all device mounting holes are labeled with wire-list coordinates.

The device mounting holes are plated-thru and accept standard .025 wire-wrap pins. Forty-one vertical holes at .1 inch spacing allow two forty-pin devices to be stacked end-to-end with clearance. The board will accept devices with .3, .4, .6, and .9 inch horizontal pin spacing. Holes are provided for the mounting of card ejectors, and for chassis or stand-off mounting of the board. A continuous double-row of header mounting holes at the top of the board allows the mixed use of any sized headers. Distribution strips for plus and minus 12 volts are provided, as well as an uncommitted analog ground distribution strip.

PIXEL COMPUTER

PIXEL FEATURES

- * 68000/UNIX[™] Based Multi Processor Architecture
- * 10 MHz Clock Speed
- * .5 to 6 MB Zero-Wait-State DRAM
- * Virtual Memory
- * 40 - 280 MB Winchester Disk Storage
- * Up to 16 Serial and 2 Parallel Ports
- * Tape Cartridge Back-up
- * Communications
- * 13 Languages plus Translators
- * Broad Selection of DBMS
- * Excellent Development Tools
- * Office Automation
- * Word Processors and Spreadsheets
- * Many Vertical Applications Packages
- * Western Union and Pixel National Maintenance
- * Board Swap Program
- * National Advertising
- * Dealer Co-op Advertising
- * Sales and Service Training

For more information contact:

Maure Milden
Marketing Communications Manager
(617) 657-8720 X2130

DEXPO Booth # 1608

FOR IMMEDIATE RELEASE

PIXEL COMPUTER OFFERS SUPERMICRO ALTERNATIVE:
UNIX PORTABILITY FOR DEC-COMPATIBLE SOFTWARE

BOSTON, MA, April 3, 1984 -- Pixel Computer will showcase its MC-68000-based, multiprocessor supermicro computers running DEC[™]-compatible, commercial programming languages and software application packages under UNIX[™], at the DEXPO East 84 Exposition opening here today, said Paul Jolicœur, Pixel marketing director.

Pixel will exhibit the powerful TEC Manufacturing System for online tracking and control in a variety of industrial environments, including electronics, sheet metal fabrication, and materials handling. The migration of TEC software to the Pixel 100/AP[™] and Proline 80[™] series computer systems represents the first implementation of a full MRP system for supermicros running under SIBOL[™], a DIBOL[™]-compatible programming language.

Consistent with its rapidly growing third party software program, Pixel is also demonstrating vertical applications software that include programs for office productivity, general accounting, and financial modeling and analysis. Providing full portability to the multi-user 68000/UNIX environment, these "Pixel-compatible" programs offer both the OEM and end-user a low-end, state-of-the-art, price/performance alternative to software packages that previously ran only on expensive mainframes and high-end mini-computer systems.

Featured Pixel third party software and suppliers, in addition to TEC Computer Systems, include: Real World accounting software, Real World Corporation; SIBOL programming language, Software Ireland Limited; Supercomp-Twenty[™] financial modeling worksheet, Access Technology, Inc.; Q-Office OAS tools, Quadratron Systems, Inc.; UNIFY[™] database management system, Unify Corporation; and over thirteen languages, including FORTRAN, COBOL, PASCAL, BASIC, and SIBOL.

Pixel presents these products as an integral part of the company's commitment to the user-friendly interface shared by the 100/AP and Proline 80 series. Integrating high compute performance with interactive software and third party application packages, the 32/16-bit MC68000 microprocessor can be configured with up to 6 Mbytes of no-wait-state main memory for four to sixteen simultaneous users. The systems also provide eight RS-232 serial ports and two parallel printer ports, and optional synchronous communication subsystem. Both systems feature a menu-driven user interface that provides simple, user configurable, single-key access to system utilities and applications. The TTY-compatible ASCII terminal features an amber phosphor non-glare screen with 80-column by 24-line display, 32 programmable function keys and visual attributes for display highlighting such as underline, blink, blank, reduced intensity, and reverse video.

Consistent with the company's commitment to active product maintenance and responsive customer service, Pixel Computer's Supermicro Service Plan guarantees customers on site response within 12 hours by one of Western Union's 425 Field Service sites across the U.S. The Pixel Computer/Western Union agreement covers all parts, labor, and travel required for customer system support.

Since 1982, Pixel Computer has shipped more than 500 supermicro computer systems to locations throughout the world. Headquartered in Wilmington, MA, the company has five full service and sales offices in the United States and seven international distributorships serving Canada, Western Europe, South America and the Far East.

Mr. Larry E. Williams

P. O. Box 61618
Sunnyvale, Ca 94088
7 April 1988

Educational Microcomputer Systems can be rented at P. O. Box 16135, Irvine, CA 92713, phone (714) 551-0133. I would suggest that you write for their current price list.

I think that the Apple Macintosh will probably become the most popular 68000 system, based on price performance alone. For your \$2995 you can get an entire amount of computer. The demonstrations given by the Computer Store stores here in Nashville will knock your socks off. The rumor reported in Byte is that once 256K dynamic RAM chips become available in sufficient quantity, the Macintosh will grow from 128K RAM to 512K. I did read in the paper that Apple will sell 128 Macintosh to educational institutions for \$1345 in sufficiently large orders. Some dealers were reported to be unhappy because that undercuts their whole-number cost. I think that Apple will get into trouble only if they completely alienate their dealers.

Your magazine is carrying a display ad on cover 2 for a new SMTPC system. Why not prevail on the folks in San Antonio to give you a write-up?

Very truly yours,

William F. Hamilton
William F. Hamilton



MOTOROLA
Semiconductor Products Inc.

P.O. BOX 20012 PHOENIX, ARIZONA 85008

AT&T VALIDATES MOTOROLA UNIX OPERATING SYSTEM

Phoenix, March 20, 1984--AT&T, originator of the UNIX® Operating System, has validated Motorola's SYSTEM V/68® Operating System, which is the first UNIX port developed jointly with an outside source.

Validation of the SYSTEM V/68 Operating System, developed in agreement with AT&T by Motorola for the M68000 family of microprocessors, means the operating system is a faithful, functional equivalent to the UNIX System V product developed for microcomputer environments.

Adaptation of the UNIX operating system by industry leaders such as Motorola, AT&T, IBM, and Digital Research is rapidly establishing it as an operating system "standard". Validation of the SYSTEM V/68 Operating System and subsequent validation of other UNIX-derived operating systems strengthens its industry-standard position.

AT&T and Motorola will market the new product under the names UNIX System V, M68000 Version Operating System and SYSTEM V/68 Operating System, respectively. The SYSTEM V/68 Operating System joins the real-time, multi-tasking VERSAdos® Operating System as another software support option on Motorola's EXORcise® Development System and VME/10® Microcomputer System.

Support

Source code updates, maintenance, and support of the SYSTEM V/68 and UNIX System V, M68000 Version Operating systems is provided by Motorola. In addition, Motorola will provide similar support for SYSTEM V/68 object code operating on their development systems.

Price and Availability

SYSTEM V/68 Operating System is now available to manufacturers of M68000-based microcomputer systems from Motorola at both the source and object code levels which are sold individually at a cost of \$2000 plus a media charge. Source code for SYSTEM V/68 is only available to holders of UNIX SYSTEM V, M68000 Version source licenses which may be obtained from AT&T Software Marketing and Sales. Object redistribution licenses for SYSTEM V/68 Operating System are available from Motorola.

For more information contact, Roger Fordham, MD-DW212, Motorola Semiconductor Products Inc., 5005 East McDowell Road, Phoenix, Arizona 85008. (602) 438-3510.

Computer Publishing Center
38 MICRO JOURNAL
3900 Cassandra Smith Rd.
PO Box 849
Hixson, TN 37343

Gentlemen:

I received my April issue and it is defective in that it has duplicate sheets with page numbers 9 through 13 inclusive and is missing sheets with page numbers 17 through 23 inclusive. Obviously, I would like another copy.

While I am writing I might as well give you a few likes and dislikes.

1. I like the honest approach you have especially in reproducing letters exactly, instead of giving them the typical editorial white-wash and recopying.
2. I like the dedication to the 68xx world, but a few months ago a fellow wrote in and gave a very objective comparison (obviously his opinion) of 280 and 6809. You printed it so small it was hardly readable; and cut it off in the middle. I like like that fellow, also use a 280 CP/M system as well as 6809 files and an IBM-PC. The truth is that no one designs a system, either software or hardware, to be bad; hence they each have some good features. By the same token some designs do turn out to be overall better than others. I like files better than either CP/M or DOS 2.0; and I like the instruction set of the 6809 (even the 6800) better than that of the 280 or the 8088; but I won't deny that the availability of software is much greater (and truly better software exists) for the CP/M world. If I tell the truth the IBM software is gross, low in quality but high in quantity; even the most trivial program requires at least 102K bytes, an entire diskette and has a manual of at least 200 pages.
3. I dislike the space you give to the "Color Computer". It is a useless toy; inferior to the Commodore 64. The 6809 world was killed because it seems all progress stopped while all effort was directed to "getting it to go on the CoCo". Nobody with a grain of sense would ever consider the Color Computer for anything serious; even if someone has got a beatcardized version of files to work on it. Thank God the CP/M world was smarter and did not come to a halt while everything was retrofitted to the Times/Sinclair.

Well, that is my soap-box.

Yours truly,
Lafarr Stuart
Lafarr Stuart

Computer Publishing Center
38 MICRO JOURNAL
3900 Cassandra Smith Rd.
P.O. Box 849
Hixson, TN 37343

cc: Pamela Court
Ames, Iowa 50016
April 9, 1984

Dear Sirs,

I very much enjoyed the sample issue of 38 MICRO JOURNAL which you sent me to examine. I learned more about OS-9 pipes and filters in that single issue than from all I could find in the Radio Shack OS-9 manuals and all the other CoCo magazines on the market. Enclosed is my check for \$24.50 for an one year subscription. Keep up the good work.

Sincerely,

Tim Harris
Tim Harris

By: McFarland Corporation
4001 North 1st Avenue
Rolling Hills Estates,
California 90274
(213) 541-4828
Toll: 800-444-2233
Telex: 744253

MC'S RELEASE

RYAN-MCFARLAND'S RM/COBOL® AND ADVANCED RM/COS® OPERATING SYSTEM
CHOSEN FOR MCR'S NEW I-TOWER™ SMALL-BUSINESS SUPERMICROCOMPUTER SYSTEM.

Ryan-McFarland Corporation (Rolling Hills Estates, CA) is supplying their RM/COBOL® compiler and advanced multi-user RM/COS® operating system to MCR Corporation for use on the latter company's newly announced I-Tower™ supermicrocomputer system. MCR's 68000-based I-Tower is a business-oriented companion to the company's Year-old UNIX® based Tower™ 1632 system. MCR will sell and support RM/COS directly through their field sales organization.

This support of RM/COBOL (a GSA-certified implementation of the ANSI X3.23 74 COBOL standard) gives I-Tower users potential access to more than 750 business applications and development tools written for other systems, including MCR's own INDS-based products. RM/COBOL runs under more than 45 operating systems on 20 different CPUs, and has been installed on more than 200,000 machines worldwide. MCR has also

been working with application developers to provide RMC/COS and RM/COS supported packages for key vertical markets, like banking, manufacturing control, school systems, and local government.

RM/COS (Ryan-McFarland's Commercial Operating System) is multi-user and multi-tasking and is optimized for the execution of RM/COBOL programs. Because of I/O optimizations--Read and Write operations, for example, occur at the record level rather than the byte or block level--most RM/COBOL programs execute three to five times faster under RM/COS than under any other OS. Record locking, multi-user handling and contention, multiple-level access security, robust I/O (input/output) capabilities, print queue management and service, a complete JDL (job description language), terminal customization and related operations are greatly simplified by features built into the OS level with RM/COS. For all its power, RM/COS can run several users in only 256K bytes of resident memory.

Ryan-McFarland has been a leading developer of commercial systems software packages since 1970. Their software is sold either as custom-made products or standard proprietary packages to customers, including computer OEMs and sophisticated end users.

For additional information, contact Ryan-McFarland Corporation, 609 Deep Valley Drive, Rolling Hills Estates, CA 90274; (213) 541-4828.



MOTOROLA INC.
MOS Microprocessor Division
3501 ED BLUESTEIN BLVD
AUSTIN, TEXAS 78721

For further information contact:

Editorial Contact: Susan Dunn

312 928-6804

Reader Contact: Jim Lovegrove

312 928-6866

MC68000 User's

Manual Available

From Motorola and

Prentice-Hall

Austin, Texas, March 5, 1984... Motorola Microprocessor Division and Prentice-Hall Inc., publishers, announce the availability of the Fourth Edition MC68000 Microprocessor Programmer's Manual. This latest and most up-to-date design tool offers the software engineer a complete and definitive source of information on the architecture and operation of the entire M68000 microprocessor family. It includes the basic MC68000 processor, the 8-bit external bus MC68008, and the virtual memory MC68010. In addition all software in this manual will be compatible with all versions of the MC68000 family.

Each section in the Programmer's Reference Manual provides answers to the M68000's architecture, its data organization and addressing capabilities. A detailed summary of the M68000 instruction set is also included.

Each individual instruction is explained in bit pattern format and includes examples which show how each specific instruction operates and its execution time.

The manual is available for \$16.95 from Prentice-Hall Inc, Englewood Cliffs, NJ, or from Motorola Literature Distribution Center, 616 W. 24th St., Tempe, Arizona, 85282.

Benchmark Report For the M68000 Family and the IAPX86 Family is Now Available

Austin, Texas, April 2, 1984.... Motorola Microprocessor Products Division announces availability of the "M68000 vs. IAPX86 Benchmark Performance." Motorola's Applications Labs have prepared the report which compares the performance differences between the segmented, dedicated register design of the IAPX family and the non-segmented, general purpose architecture of the M68000 Family.

The benchmark procedures used to compare the devices are well known and widely accepted as standard measurements of processor performance. Included are the Carnegie-Mellon/EDN benchmark set of seven procedures, the digital filter benchmark as specified by Nagle and Nelson and the Berkeley benchmark set of Patterson, Hansen, et al.

In addition to the simple comparison of execution times, the report provides a detailed analysis of each procedure, specific reasons why one device performs better than another, and a comparison of source code size and complexity for each benchmark. As a supplement to the report, the actual benchmark source code listing for the M68000 family is provided.

The report is available now and copies are available from the Literature Distribution Center, 616 West 24th St., Tempe, AZ 85287. The order number is BR150.

News Release



FOR FURTHER INFORMATION:

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Language Processors, Inc.
(617) 890-1155

Edgar E. Gellhner
Gellhner/McGowan, Inc.
(617) 875-3821

FOR IMMEDIATE RELEASE

Compilers based on Component Architecture™

LANGUAGE PROCESSORS, INC. INTRODUCES FAMILY
OF HIGH PERFORMANCE, TRUE COMPILERS
FOR MOTOROLA MC68000-BASED UNIX SYSTEMS

MALTHAM, MASS., March 20 -- A family of high level language compilers whose Component Architecture™ assures high performance, lets users mix sub-programs written in different languages into a single program, and whose largely identical subsystems assure reliability and quick availability was introduced today by Language Processors, Inc. The compilers, for COBOL, RPG-III, Pascal, C, PL/I, and BASIC, are aimed at computer manufacturers whose systems use the Motorola MC68000 microprocessor and the UNIX operating system.

The products are the first family of integrated compilers available from an independent software company.

They are priced at \$50,000 each to hardware OEMs, plus royalties based on the size of the system on which they are used. Single copies with run-time license distribution rights are available to qualified software developers. LPI-COBOL, LPI-RPG II, LPI-Pascal, and LPI-C are available immediately. LPI-PL/I will be available this summer, and LPI-BASIC will be available in late 1984. All are available 90 days after receipt of order.

The family uses LPI's Component Architecture, a modular software architecture that combines five standard subsystems -- a front end, optimizer, code generator, run time library, and high level debugger -- into a high performance compiler. They are largely unchanged from compiler to compiler. The compilers also use the same Indirect Sequential Access Method to handle data files, so programs written in one language can address and use files written in another language.

PHILON, INC.

50 Cooper Square
New York, New York 10003
(212) 420-0317

FOR INFORMATION CONTACT:
Bob Gildenberg (212) 420-0317
Mike Morris (212) 887-0818

IMMEDIATE RELEASE

NEW HI-SPEED, PORTABLE COMPILERS FROM PHILON
FOR THE MC68000/UNIX SERIES;
HEAVY CUSTOMER SERVICE SUPPORT STRESSED

NEW YORK, March 28 -- Philon, Inc. announced today the introduction of a family of computer language compilers -- PHILON FAST/Compilers -- for the MC68000/UNIX operating environments. Initial products, all of which will be available by mid-summer, are PHILON FAST/BASIC-C, PHILON FAST/BASIC-R, PHILON FAST/COBOL, and PHILON FAST/C.

"Our compilers are named 'PHILON FAST' for a reason," says Michael Parrella, president of Philon. "Benchmark results indicate superior speed of execution -- and that's what's really important today. Hardware manufacturers and software application developers recognize that in this highly competitive marketplace, fast benchmarks for their products are essential. PHILON FAST/Compilers can give them the high performance they require to help increase sales and profits."

PHILON FAST/Compilers are true compilers, not interpreters. Some of the most advanced optimization techniques have been utilized in PHILON FAST/Compilers. They were specifically designed with 16/32-bit architecture in mind and not merely moved from the 8-bit environment.

Philon's compilers are the result of 75 man-years of effort and represent a leap forward in commercial application development. They offer significant advantages in speed, portability, and productivity:

- Speed - Philon languages offer very fast execution of compiled code. Users can reduce execution time by 50% or more by simply recompiling an existing program with PHILON FAST/Compilers.
- Portability - Through Philon's development of a common intermediate code (PHI-Code), Philon's programs can now be moved easily from one hardware/operating system to another.
- Productivity - Philon languages contain a powerful set of run-time libraries and file handling routines. Also provided are numerous programming aids to minimize application development time, including an interactive debugger (the Phi-Analysr) that slashes program development time.

Philon is backing these products with a strong commitment to customer service. The company recognizes the growing demand for a support-oriented systems software organization that responds to the needs of all customers.

Says Bob Gildenberg, vice president of marketing: "Our PHILON FAST/HELP Customer Support System is fully computerized and maintains all the detailed information that must be easily accessed to assist our customers properly. Customers get prompt answers to their questions from our team of skilled customer service people."

Formed in 1980, Philon has concentrated its efforts on developing the programming software to meet the computer industry's need for a new generation of improved compilers. The company is currently under contract from a Fortune 100 corporation to develop compilers for the United States Government.

Dear Don,

This is in answer to your plea in the April issue for more info on the 68000 system out there. Well, I have a surprise for you. There is a reliable, debugged, multiuser 68000 system with codes of applications software. The name of this system is the Alpha Micro, and its operating system is called AMOS/L.

This is a relatively unknown system because: A.) the company seems to have a dislike for advertising in any microcomputer magazine; and B.) Alpha Micro computers and software are not "popularly" priced.

First, let's examine what the system is like. Alpha Micro began as a purveyor of 3-100 bus computers around 1977. Its first computers were based on the WD-16 chipset, a pseudo PDP-11 chip set that had a semi-subset of the instructions available on the 68000. Converting all the old WD-16 software to the 68000 was a snap, and Alpha computers have been running an extremely large library of applications packages almost from the first day the 68000 based AM-100/L was released--in July of 1982. The top of the line AM-100/L is a 3-100 based. Alpha's economy model is called the AM-1000 and does not have a standard bus, although one of the many hardware vendors who support the Alpha Micro system has created a bus for it. Both computers have the potential for supporting the full 16MB address space the 68000 can handle.

But the hardware is not the best part. AMOS/L, the Alpha Micro operating system is multi-user and multitasking. It is a truly business applications oriented OS that has the power of UNIX without its incomprehensibility. It is a mature, reliable operating system with an advanced BASIC language, excellent Macro Assembler, file and record locking ability, and enough utilities to satisfy the heart of the most dedicated hacker. One of the handiest features of the system is its "virtual terminal" interface that allows applications programs to do fancy screen handling without having to worry about what kind of terminal the application is dealing with. This feature OUGHT to be a part of any multi-user system, but is sadly lacking in most, including most implementations of UNIX. Also available with AMOS/L are the Pascal, COBOL, and FORTRAN languages.

Since word processing is one of the most useful applications of any computer system, AMOS/L scores high with users in its wide range of Word Processing capabilities. Bundled with the OS is WUE, a fairly sophisticated full screen editor. Also bundled with the system is TITVMT, a very sophisticated text processor capable of handling extremely complex documentation, letters, etc. Well merge is not built into the system, but writing programs to do mail merge is simple, and many software houses have packages that can do very sophisticated mail merging. If a full word processor is what is needed, two highly sophisticated word processors, AlphaWRITE and SuperWUE, are available. These are both complete word processing programs with what you see is what you get capability. They are the match of any dedicated word processor I have ever used. Many users rate SuperWUE above the WANG word processors.

As a programmer, I rate AlphaBASIC better than most COBOL implementations. It has comprehensive multi-level record definition and I/O capability. It also has a very effective ON ERROR system that catches any BASIC errors and allows the program to trap the error and take corrective action without crashing the program or mystifying the user. Another handy feature is AlphaBASIC's ability to call subprograms written in assembly language. One especially useful subroutine that I use often is called INP, and it does everything from control key trapping to input verification.

Furthermore, Alpha computers are reliable. One system that I've worked with recently has 22 terminals attached, with about 15 being used at any given time. The system runs 24 hours a day 7 days a week and the business using it is totally dependent on the computer. Even 15 minutes of downtime can be disastrous, but this system keeps going day after day after day.

I suppose the main reason most readers of your magazine haven't heard of the Alpha is because it is not a hobbyist's machine. It is a very rare individual who can afford a complete Alpha system. For businesses it is perfect, but few individual people are willing to pay for multiuser capability. Even the smallest of Alpha's computers can handle two users and a spooled printer with no add-ons or extra software. This is probably overkill for most hobbyists, but is just what the doctor ordered for a small business, especially one that is likely to grow. Any software written for the smallest of Alpha's computers will run totally unmodified on the largest 60 user system.

I could go on raving about extremely capable data base systems like Alpha Base that do automatic program generation, screen generation, data base security, etc. etc., but this has gone on long enough. At least I have perhaps made some people curious enough to investigate.

Sincerely,

Earl Allen
4100 Winfield Ave.
Fort Worth, TX 76109

P.S. Please renew my subscription to "68 Micro". My check is enclosed.

68' MICRO JOURNAL
3688 Cassandra Smith Rd
Hixson - Tenn 37343

Mr. Williams:

Please publish the following orders and FLEX fix. The program should be useful to readers who like to find out how a disk full of files that need to be written or deleted is created. The program will write, delete or catalog a disk. The entire disk, also remove protection of the entire

disk. I don't trust those stick on write protect tabs. They do fail off with time. While writing this program I found that the FMS code 00 Put Information Record did not work. A call to TSC confirmed that it never did work. All versions of FLEX 8800 & 8809 (this is true of SUTP FLEX) also have this bug. The fix is to watch two bytes. The fix is: At or near 1057D find the following code BE 04 09 E4 08 22 4C 08 22 write down the address of the first byte (BE) of this code. At or near 00005 find the following code 2D xx xx 33 14 36 01 the xx xx is different in different versions. Now place the address you wrote down before in the xx xx part of code you just found. For SUTP FLEX 2.0: it up to 2.9:1 the 10573 code is at 10505, and the 00005 code is at 0000E. If you have any questions I would you call TSC. 6800 FLEX users will have to call TSC for the fix. Maybe Don Williams could call and have TSC send the info for inclusion in 6800 micro. This would save TSC a lot of calls.

Thanks for a GREAT Magazine.

Joseph Aulicino
2014-59th Street
Bklyn, N.Y. 11204
212-837-2687

```

OPT   PAC
NAM   PROTDISK
*****
# Disk Protection for Entire Directory #
*****
# By Joseph Aulicino #
*****
# This program will Write, Delete or #
# Catalog protect the entire disk. #
# It will also remove protection of #
# entire Disk... #
*****

# Syntax: PROTDisk drive B):<prot. code (W,D,C,X) #
# File Equates #

C104  BUFFNT EQU 0CC14
C103  WARMS EQU 0CD03
C101E PSTRNG EQU 0CD1E
C1027 NXTCH EQU 0CD27
C1042 GETHEX EQU 0CD42
D406  FMS EQU 0D406
C103F RPTERR EQU 0CD3F

C100
C100 20 07 START ORG 0C100
C102 01 2E 01 BRA GETDRV
001,02E,001 Version

C105 0000 TEMP FDB 0
C107 00 DRU FCB 0
C108 00 PRDT FCB 0

C109 00 C102 GETDRV JSR GETHEX ;set drive number
C10C 1025 LDCB DRIVER error?
C110 0F C105 STX TEMP
C113 F6 C104 LDB TEMP+1
C116 102D 0001 LBLT DRIVER ;is drive number
C11A C1 03 CNP8 03 ;between 0 and 3
C11C 102E 0078 LBLT DRIVER ;save drive number
C120 F7 C107 STB DRU
C123 BE CC14 LDX BUFFNT
C126 BD C1027 CHPA CHPA
C129 01 07 NEG SETW
C12B 27 12 BNE SETW
C12D 01 44 BNE SETD
C12F 27 10 BNE SETD
C131 01 43 CHPA 0'C
C133 27 1E NEG SETC
C135 01 50 CHPA 0'A
C137 27 24 NEG SETX
C139 01 00 BNE Opendir
C13B 27 23 NEG SETC
C13D 20 C7 BRA GETSPEC
C13F F6 C108 SETW LDB PRDT
C142 CA 00 ORG 0000
C144 F7 C108 STB PRDT

PROTDISK

C147 20 0D SETD LDB GETSPEC
C148 FA C100 ORG PRDT
C14C CA 40 ORG 0040
C14E F7 C100 STB PRDT
C151 20 03 BRA GETSPEC
C153 FA C100 LDB PRDT
C156 CA 10 ORG 0010
C158 F7 C100 STB PRDT
C159 20 C9 BRA GETSPEC
C150 F7 C100 SETX CLR PRDT

C140 0E C200 Opendir LDX 0FCB ;point to FCB
C143 06 C107 LDA DRU ;put drive
C146 A7 03 STA J,X ;in FCB
C148 06 0A LDA 06 ;open directory code
C14A A7 04 STA 0,X ;put code in FCB
C14C BD D406 JSR FMS ;call FMS
C14F 26 2F BNE CRFR ;check for error
C171 06 07 READDIR LDA 07 ;set info code
C173 A7 04 STA 0,X ;put in FCB
C175 BD D404 JSR FMS ;call FMS
C178 26 15 BNE ERR ;check for error
C17A 7D C20C TGT FCB+4 ;test for deleted
C17D 2D F2 BHI REaddir ;directory entry (if it was)
C17F F6 C100 LDB PRDT ;set prot code
C182 E7 0C STB 13,X ;put in FCB
C184 06 08 WRITDIR LDA 08 ;put info record
C186 A7 04 STA 0,X ;put in FCB
C188 BD D406 JSR FMS ;call FMS
C18B 26 10 BNE ERR ;check for error
C18D 20 E2 BRA REaddir ;reset & ill EOF

0
ERR LDA 1,X ;set FCB error code
C18F A6 01 CHPA 0B ;check for EOF
C191 01 00 BNE MSG1 ;if EOF branch
C193 27 10 JSR RPTERR ;if not report error
C195 BD C103F JMP WAHNB ;return to FLEX

```

```

C19B 0E C103 DRIVER LDX 0NE01 ;point to string
C1 E 20 4D BRA 0UTIT ;print string

C1A0 0E C1C4 ERR4 LDX 0NE02 ;point to string
C1A3 20 08 BRA 0UTIT ;print string

C1A5 0E C103 MSG1 LDX 0NE03 ;point to string
C1A8 20 03 BRA 0UTIT ;print string

C1AA 0E C1EF ERR4 LDX 0NE04 ;point to string
C1AB BD C10E JSR PSTRNG ;print string
C1B0 7E C103 JMP WAHNB ;return to FLEX

C1B3 44 72 49 76 MESS1 FCC 'Drive Number Error'
C1B7 65 20 4E 75
C1B9 6D 62 65 72

```

PROTDISK

```

C10F 20 45 72 72
C1C3 6F 72
C1C5 04
C1C6 44 69 72 65 MESS2 FCC 4 /Directory won't open/
C1CA 63 74 6F 72
C1CE 79 20 77 6F
C1D2 6E 27 74 20
C1D6 6F 70 65 6E
C1DA 04
C1DB 46 69 6E 69 MESS3 FCC 4 'Finished processing'
C1DF 73 68 65 44
C1E3 20 70 72 6F
C1E7 63 65 73 73
C1EB 69 6E 67
C1EE 04
C1EF 43 61 6E 27 MESS4 FCC 4 /Can't write to Directory/
C1F3 74 20 77 72
C1F7 6F 74 65 20
C1FB 74 6F 20 44
C1FF 69 72 65 43
C203 74 6F 72 79
C207 04
FCB 4

C20B FCB AND 320 File Control Block
END START

```

0 ERROR(S) DETECTED

PROTDISK

6-31-83 6:09 PM BTSC PAGE 3

SYMBOL TABLE:

```

BUFFNT C104 DRU C107 DRIVER C19B ERR C10F ERR4 C1A0
ERRC C1AA FCB C200 FMS D406 GETDRV C109 GETHEX C102
GETSPE C126 MESS1 C1B3 MESS2 C1C4 MESS3 C1DB MESS4 C1EF
MSG1 C1A5 NXTCH C1027 Opendir C160 OUTIT C1AB PROT C100
PSTRNG C10E READDIR C171 RPTERR C103F SETC C153 SETD C140
SETW C10F SETX C150 START C100 TEMP C105 WARMS C103
WRTDIR C184

```

HACKING THE MAC

By Michael Wolf

I am a hardware hacker. I built my first computer in 1976 from an assortment of parts I collected as evaluation samples from Motorola. I have been a Motorola chauvinist ever since. Consequently I have never before been too interested in the products of Apple Computer Inc. The first ready built computer I purchased was a Radio Shack 4K Color Computer. It was the first unit in town, and I bought it mainly on the strength of it's 6809 CPU. I took it home and didn't even plug it in before I had opened it up to see what made it tick. I upped it to 16K that afternoon and within a couple of weeks I had reverse engineered it sufficiently to figure out how to up it to 64K. This I did as soon as my bank account could afford the 64K RAMS (they were \$40 each in those days).

I am telling you this to let you know what my attitude is towards computers. A computer has to be technically interesting and should have a Motorola type processor before I pay much attention to it. I look at computers from two perspectives as a device for entertainment (can I hack it) or as a tool to do a job (can I use it to help me hack).

I was attracted to the Macintosh by an article in *BYTE* which indicated to me that it met my requirements for interest in that it had a 68000 micro and had some technically interesting features. Furthermore it might meet a need I had as a tool to do some report writing and general computing.

I went to my friendly dealer to see it and fell in love. Even if you don't anticipate purchasing one you ought to go see a demonstration. I feel that this is the advance unit that will show the way for the next generation of personal computers just as the original Apple led the way for the current generation.

The only question I had was could a hacker be happy with a computer that was obviously intended for people who didn't know anything about computers and didn't care. In short an "appliance" computer.

In keeping with this concept when I took it home I broke a long standing tradition and actually plugged the computer in before opening the case. I was so intrigued by operating it that it was several days before I opened it up.

Hardwarewise the Mac comes in three units. The main unit contains the display, computer and disc drive. The other units are the keyboard and mouse. The main unit is 11x10x13.5 inches and weighs about 16 pounds. The display is a 9 inch black and white display that is one of the sharpest I've seen. Some people have thought a 12 inch CRT would be better but I've not found it to be too small. The disc drive is a modified 3.5 inch Sony with 400K bytes capacity. It seems to be quite reliable and apparently faster than a 5 1/4 inch drive.

The detached keyboard is 13x6x2.5 inches and the mouse is 2.4x4.3x1.5 inches. The keyboard is small because it has only a standard keyboard with no numeric pad, cursor control keys, or special function keys. At first I felt this would be a serious limitation. But the mouse takes care of the need for the cursor and special function keys and a numeric pad is available as a option for those who do a lot of numeric entries. What is left makes a nice lap sized keyboard which is easy to use and has a nice feel although it could have a lighter touch. I haven't decided if I would prefer the numeric pad be attached or not. I may make a bracket to attach them together to see how I like it.

The mouse does as much of the control of the Mac as the keyboard. In fact some applications do not even need the keyboard. Moving the cursor to select functions and clicking the single button on the mouse is all that is required to perform such things as opening files, selecting files, organizing directories, and copying files and discs. The mouse moves freely about on any reasonably clean flat surface and requires an area of about a square foot.

The interface to the operating system is through an "electronic desktop". That is a series of pictures or "icons" that represent the resources available. Icons represent application software, data files, and discs. By manipulating these icons with the mouse you can copy files, delete files, copy discs, etc. A neat feature is a selection of desk accessories such as a calculator and notepad which can be called up at any time without disturbing the current task. The icons and accessories operate out of "windows" on the screen.

While only one window is active at one time several can be present on the screen at one time. For example the windows for two discs could be on the screen at the same time. Files are copied from one disc to the other by moving the icons representing those files from one window to the other.

I was impressed by the Macintosh's human interface, but I was quite concerned that it would inhibit experienced users and become frustrating to use after some experience was gained. After several weeks and a considerable amount of work on it I am happy to report that those concerns were largely unfounded. There are shortcuts available for most functions so you don't have to resort to the mouse to do everything. However, mousing is not very time consuming and I found it simpler in most cases to use the mouse rather than memorize the various keystrokes to perform the shortcut functions. The mouse was a little awkward to use at first but as I used it more it became quite natural. The main problem is that you need quite a bit of free desk space to operate it which, to some extent, negates the advantages of the machine's small footprint. I would guess that a large aftermarket will spring up in trackballs or joysticks to replace the mouse for those people who find the mouse not to their liking.

Let's you think that I am so enamored of the Mac that have lost my objectivity I will give you some negative things that I have observed. The Mac comes with 128K of RAM and 64K of ROM. The amount of RAM is marginal. There is no clear cut way to add more except to replace the chips (4165s) with 256K RAMs. First problem is those chips cost currently \$60-\$90 each and you need 16 of them. Second, the RAM chips are soldered into the main circuit board. Even I have misgivings about unsoldering 16 chips from a 4 layer PCB and replacing them with \$1400 worth of parts.

The single disc drive suffers from the problems any single drive system has. Copying is a hassle and it has marginally enough space for applications and data. Even though the one

drive has as much capacity as the 3 drives in my CoCo system it's not adequate. Those 68000 applications programs are BIG!

These problems are relieved somewhat by the ROM which has a lot of the utility software resident. The friendliness of the operating system also makes it less of a problem. This system is useful with only one drive, but two would be much better, and a 5 or 10MB winchester would be great.

In spite of the attention that has been paid to the software it is not completely bullet or idiot proof. It is possible to get into a corner from which the only exit is power off or reset. It is interesting to note that the reset switch is accessible only by installing a plastic clip that is provided. The manual cautions against installing this clip "unless you are developing applications software". Most owners won't even know what that means. Needless to say I installed the clip as soon as I found it. So far I've only crashed one disc by pushing it at the wrong time.

One thing that takes getting used to is the lack of a disc eject button. You must request the ejection of the disc via the mouse or keyboard. It is then automatically ejected after closing files etc. This is nice when copying files or when the computer wants another disc but awkward if you want to remove the disc after powering down. It contributes to the idiot proofing by preventing the removal of a disc while files are open. I guess I feel that it's taking some of my control of the system away, but I'll get used to it.

A major problem concerns the printer interface and driver software. The printer attaches to one of two serial interfaces. These interfaces are the only means of expanding the I/O capabilities of the Mac. Not only does this limit you to only serial printers, the only printer driver software in the system is for an Apple Imagewriter. While this is a nice enough printer and not too expensive, I already owned two Epson printers. It hurt to have to buy another printer. Since the Mac does most of it's printing using the graphics capability of the printer it won't be a simple task to interface another printer.

Now a few words about software. The Mac comes with MacWrite, a word processor, and MacPaint, a graphics generator. It does not come with any form of BASIC. A BASIC is available from Microsoft for \$200.

MacWrite is a superb word processor. The combination of mouse control and the multiple windows make it a joy to use. The only real complaints are that it only works in the insert mode, which makes it awkward to replace text, and it only works out of RAM, which limits the size of the document you can edit at one time to about 8 pages of single spaced text. It is easy to break documents into small chunks so that isn't serious. Otherwise, it is quite easy to use. It allows the changing of type fonts and sizes within a document at will. It allows justification, line spacing options, and margins. Automatic paragraph indentation, regular and decimal tabs, and page headers and footers are all provided. One of the nicest features is that any changes in typestyle or format are reflected on the screen. What you see is what you get on the printer, exactly. I wish that there were provision for using a letter quality printer but that would spoil some of the fun with typestyle/size changes.

MacPaint is a graphics generator program that has to be seen to be believed. With it you can generate pictures which can be printed out separately or incorporated in a MacWrite document. Everything except entry of text is done with the mouse. This program alone will sell a lot of Macs. I won't even try to describe it. Go see a demonstration. For entertainment value I rate it better than most computer games.

The currently available BASIC is from Microsoft and is similar to other Microsoft BASICs. This will make translation of programs from other computers relatively easy. It has a few machine specific commands such as "MOUSE" and graphic commands. There are also some syntax variations related to file handling with the Mac's file structure. The nicest feature is that it uses the Mac windows. This is especially valuable during program editing when you can have two listing windows working on separate parts of the program at once. Editing is easy as the line editor works similar to Macwrite. Lines are selected with the mouse by scrolling the listing window to display the line, selecting it with the mouse, and editing it in the command window. The command window is a single line window which is used to enter commands and programs. You can also have the listing window and the output window present at the same time which makes editing graphics easy as you can alter the program and watch the output change without a lot of messing around.

with "EDIT" commands. The ease of entering and debugging programs is worth the expense of the program although I do think it should be included with the computer.

Well enough about the box as delivered from Apple. How about the hacker. Can a hacker be happy with the Mac? If you want to poke around inside the box and modify things you will be disappointed. There isn't much to do inside. No expansion slots or extra RAM sockets. About the only thing useful to be done is the RAM expansion mentioned previously. Another difficulty is the lack of documentation. No schematics, no software entry points. The supplied manual does not even include the pinouts of the serial ports. The manual does give an address to write to to get technical documentation. I have not yet received a reply so I can't comment on what is available. The sum of this is that you might not even want to bother to open the case.

Where a hacker can really have fun is with the external ports. I already mentioned a trackball to replace the mouse. How about a printer adapter that would translate the Imagewriter commands into Epson or whatever? I would sure like to see what one of the Hewlett Packard Thinkjet printers could do on a Mac. Could one adapt a 5 1/4" drive to work off the external drive connector to facilitate downloading of software from other machines? How about the previously mentioned Winchester disc?

I expect to see some software use one of the ports as a network port to interconnect several Macs to a common printer and hard disc. Perhaps someone will come up with a I/O adapter which will communicate through the serial port and allow connection of a variety of I/O devices. There are certainly plenty of things to hack without opening the case.

I would normally take the lack of documentation as a challenge to reverse engineer the computer but, at \$2500 a throw, it makes for pretty expensive mistakes. I guess I'll wait to see if Apple sends me any useful documentation before ripping into it.

In conclusion I think the Mac is the start of the next generation of personal computers. Considering that it has been on the market only a few months the amount of software and hardware support is amazing. In many ways it reminds me of the IBM PC in that it had instant support as soon as it hit the market. This indicates to me that it will be a real winner while the software has a few bugs they are minor and a lot fewer than in any other computer I have used. The price is in the ballpark for what you get but has the potential to be cut in half and still make a fair profit for Apple. Discounts are available, particularly through universitys and to dealer's employees. If you do some careful looking you could get one for under \$2K. At that price it's a real bargain.

Can a hacker be happy with the Mac? Only if they will be content with working outside the box. The development of software will be a lot of fun with the icons and windows to play with. A lot of software development tools will be available soon including at least 2 BASICs, PASCAL, and an assembler. If the ROM routines are properly documented, and I have read that they are, software using the resources (windows, icons, mouse, etc.) will be a snap. I think the Mac will keep me entertained for some time although I will have to change my tactics somewhat. Yes, I believe the Mac can be useful and interesting for both the computer illiterate and the real hacker. Again I urge you to go see one, you'll be amazed.

Michael Wolf

3125 Arizona Ave
Las Alamos, NM 87544
April 24, 1984

6 April 1984

Dear Mr. Nay:—

You ask "Where is the 68000?" and supply some remarkably good answers. Here is one more:

HSC Inc., of Herkimer, N. Y. is offering a 68000 co-processor board for 286-based computers, with up to 768K RAM (see ads in *Microsystems*). The operating system at this writing is CP/M 68K, but that includes the Digital Research "C" Compiler which is supposed to be a full implementation fully compatible with Unix "C" so there is a lot of software ready for transporting.

I have seen two totally incompatible price-lists so I don't want to be too specific on that point but it looks like something between \$1000 and \$2000.

The point of interest for us is that they are promising to have OS-9 for the 68000 board by May. Now I'll gladly eat their whole factory without ketchup if they manage to do so, but that may mean the salvation of the 68000 is at hand?

2125 Hoogdal Road
Sedro-Woolley, WA
98284 U.S.A.

Yours very sincerely,

R. W. Odlin

R. W. Odlin

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68000 COMPUTER SOFTWARE:

EDITORS, ASSEMBLERS, COMPIERS

PRESS RELEASE

GINIX DO !

GINIX, Inc. has licensed "DO" from LLOYD I/O for distribution with their OS-9 systems.

DO is an OS-9 shell procedure control language. Its appearance is similar to BASIC. DO reads the procedure file from disk using a total of about 8.5K of user memory.

Parameter passing is the main feature of DO. Statements that DO does not recognize are passed to SHELL for execution. DO has 26 number variables and nine string variables. Labels are used to control the flow of execution. The ON ERROR GOTO traps errors and allows the user's procedure to take the correct action.

Don Williams, Publisher
'68' Micro Journal
P O Box 849
Hixson, Tennessee 37343

Dear Don:

For those of us who have lower case terminals, there is an error in TSC's PROT command that can be annoying, even though it is harmless. It took me a while to figure it out, since it only affects commands issued in lower case. And it isn't really annoying enough to warrant notice most of the time. The only effect is to prevent entering multiple protection statuses with a single command.

The problem is, of course, failure to force lower case commands to upper case -- but it only affects the protection codes themselves. The culprit is a branch (BCC) located at \$C13E. In the version I have (Version 1), the protection code parsing loop branches to \$C139 if the carry is clear.

The fix is easy. Change the BCC \$C139 to BCC \$C137 so the new character just fetched by NEXTCH is AND-ed with \$3F, forcing upper case. The information needed to make the change:

Version 1: At \$C13E, find: \$24D9
Change to: \$24D7

I like to keep my documentation neat, so I changed \$C102 from \$01 to \$02 also, making the updated PROT.CMD version 2.

Now you won't have to ensure that the terminal is in upper case when you want to change a protection status to, say, WD.

Yours very truly,

Wilbur N. Killebrew, Jr.
Wilbur N. Killebrew, Jr.

Dear Don,
I want to report to you that John E. McGinness Jr. of 2331 Shannon Drive, Houston Texas 77027, passed away March 31, 1984.

I am sure that this is not the kind of news one likes to hear about but I know that you, as well as alot of your readers, who knew John, would want to know.

John was a very kind person who, through our sometimes bimonthly amateur radio net, assisted many of us through many problems. I was especially thankful since I am very isolated here in Panama.

I have been trying to find a way to let John's friends know, and I think with your assistance we can accomplish it.

Yours Very Truly,

Eric W. Davenport

Eric W. Davenport
PSC Box 3664
APO Miami, Florida 34082

Mr. Don Williams
68 MICRO JOURNAL
5900 Cassandra Smith
PO Box 849
Riceon, TX 77343

Dear Don:

I am afraid Bob May may have his head in the sand ("Where is the 68000?", April, 1984). I know of at least forty manufacturers who are producing 68000 based computers. With rare exception, they run Unix (Trademark of Bell Labs) or a Unix clone like Idris or Regulus. Usually, they are single board or use Multi-bus or VMEbus. My distributor handles two machines, the top of the line Plexus and the best value for money Altos. Both run Unix System III. I think it is interesting that Plexus first used the 28000 and later found the 68000 the better choice, while Altos started with the 8086 (which is extremely successful for them) but found they needed the 68000 to move upscale. There are some very exciting things going on here, but they are passing you by. Yes, the chip is important, but as always, the real criteria is the software.

I first went with the 68000 because, in 1977, I could get a disk based system at a reasonable cost from Midwest Scientific Instruments. Later, I upgraded this system to run the Software Dynamics Operating System and SOBASIC. This was darn nice software, and we used it until early 1983. During this period, we looked at the 68009, OS-9 and Unixflex. At one point we came very close to going with Glia and OS-9. We did not because, for about the same amount of money, we could get the 68000 with a full Unix OS.

Do not under estimate Unix. People say it is not user friendly. That is bull. It is not novice friendly, but you are only a novice for a few months. Once you move out of the novice class, user friendly systems rapidly begin to get to your way. Unix is very expert friendly. Furthermore, although I have not kept up with OS-9 and Unixflex, my initial reaction was that they were a good deal less friendly than Unix. Why? Because the user interface is identical, a command interpreter called the shell, but while the Unix shell is a fairly high level interpretive language, the OS-9 and Unixflex shells are (sorry?) little more advanced than the SDOS command interpreter.

Unix comes with about 200 utilities, most of which can be used within a shell program. One of the most useful, at least for me, has been a program called sed, which to my knowledge only exists on Unix. sed is an interpretive programming language with a C-like syntax which can be used to process text files or data. I use it like a BASIC interpreter for one-shot, quick and dirty programs. I also use it in many shell scripts where fairly extensive processing of data is required. The nice thing is, of course, I can do what I want, as fast as I want with available programs like sed. Later, if I find people are using a particular script often enough for efficiency to be a criteria, I can rewrite part or all of the script in C. Other useful programs are the grep family, groff, m4, yacc, sort, and sed. I won't describe them here. They are well covered in the literature. I will say that this letter was produced by nroff, using the macro package which comes with the system. The point is Unix provides the tools to do what the user wants to do, and to do it very quickly, very efficiently or in some combination of the two as desired!

Let's talk about C. Bob blames C for Lisa's problems. Not so. C is as efficient or slow as you want to make it. What is nice is, under Unix, you can profile a program to see where it is spending its time. If speed is a problem, you can then concentrate on those areas where efficiency pays the greatest return. If this means writing some routines in assembler, you can do that. The linker does not care how an object file was created.

The Unix wt program, which I imagine is similar to OS-9's coast program, takes 5518 bytes, considerably less than

count. On the other hand, the assembler uses 33,406 bytes, the linker 16,338 bytes, and the compiler, a huge 105,986 bytes. The Berkeley editor (vi, as, ed) also takes more than 100K. Incidentally, the three versions use the same code. C allows a program to determine by which name it was called and to act accordingly.

One must also consider the software available from other sources. This seems to be on a far higher level than the typical micro software. One example, which we have selected as the basis for one of our software products is a relational database management system called Inforaix. Now Inforaix is not the very best DBMS we have seen, but it lists for less than \$1000 (dbase II lists for \$750). That money buys you something far more powerful than your typical micro DBMS. Schemas may be set up with the editor and changed with no difficulty. Records may be up to 2K long with keys on any number of fields. There is a default data entry language which is very nice. A C interface is provided. The report generator is provided in object form which allows you to look in custom routines. There are two query languages. One is a SQL type of language while the other is closer to QBE (Query By Example). For the latter there is a method to set up a default form, or you can build custom forms using an editor. Overall, it is difficult to find anything comparable for that amount of money.

All is not perfect. Unix needs either very fast disks or lots of RAM (preferably both) to be at its best. It is a time-sharing system and will swap programs to disk if it needs RAM. This makes spread sheet programs run slower than one might prefer. The best setup would use SMD disks. My Altos uses an IBM Quantum Winchester drive with a 35ms average access time. The speed of this disk, compared to the typical 80-100ms, 5.25in. drive, was a critical factor in choosing the Altos. It has 0.5Mb of RAM, but the OS uses 170K, leaving only 330K for the users. By the time this is printed, it will have 1.0Mb and a second hard disk. Spreading the load across two disks also improves system speed.

It is also very important for the vendor to "tune" the system. This was Fortune's problem and it is still a bit of a problem with the Altos 68000 machines. If the system is not tuned properly, it can take inordinate amounts of time to do things. As one example, a well tuned system that takes, say, 10sec. to compile a program, may take 35sec. to do two simultaneous compilations of the program. A poorly tuned system may take 60sec. This is not unique to Unix. I have used an H-P230 that takes 2.3 times as long to do a job if two people are using it and, of course, Bob already mentioned Lisa. Still, tuning is a poorly understood art which must be applied to get the best from the system.

Overall, I fall into the Unix as the wave of the future camp, but I mean full Unix with all the utilities. Other systems like OS-9 and Unixflex are only interesting to the extent they support a full Unix-like environment. Unix without the utilities just would not be Unix, and your readers should keep that in mind when considering Unix clones. They may have some of the features, but it is all those utilities that really make the system sing.

Sincerely,

Robert B. Peirce

Robert B. Peirce
Investment Software Co.
123 W. Edgewood Dr.
McMurray, PA 15317

GIMIX INC. 1337 WEST 37TH PLACE • CHICAGO, ILLINOIS 60608 • (312) 927-5510

GIMIX is proud to announce the availability of the

OS-9 GMI III Support ROM

On power-up or reset, the Support ROM provides OS-9 GMI III users with a command menu. The menu includes three separate security tests that can be run anywhere in the 1 Mbyte address space to verify the integrity of the system RAM, and options to bootstrap OS-9 GMI III from either a floppy disk or directly from a hard disk.

Two versions of the Support ROM are available:

OS-9/OS-9: This version is for OS-9-only systems. It includes two separate versions of the Support ROM software: the standard Support ROM as described above, and a special version that bypasses the menu and boots directly from a hard disk on power-up or reset.

OS-9/GMIBUG: This version is for software switching systems that use both OS-9 and GMIBUG/FLEX. It includes the menu driven Support ROM software as described above, and a special version of the GMIBUG system monitor required to run GMIBUG/FLEX on GMI III systems.

Both versions are supplied on a single 8Kx8 PROM for use on the GMI 6809 CPU III. Jumper on the CPU board determine which half of the PROM will be used on power-up or reset. Switching between OS-9 and GMIBUG/FLEX can be accomplished through jumper selection or from the keyboard. Availability is from stock.

OS-9/OS-9 Support ROM \$125.00
Includes update to the Support ROM version of OS-9 GMI III

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D-FLEX is a special version of the Technical Systems Consultants FLEX operating system, designed to run as a user process under OS-9 level II (both GMI II and GMI III versions). It allows one or more users on a level II system to run FLEX and most of the software designed to run under other versions of FLEX. D-FLEX supports all of the standard FLEX features, with the exception of print-spooling and disk formatting.

D-FLEX will read and write standard GMI FLEX format floppy disks and has the ability to create and use OS-9 files that are formatted and used like FLEX disks. These "pseudo-disks" are treated exactly like an ordinary FLEX disk.

Included with D-FLEX is a utility that transfers both text and binary files from OS-9 to FLEX or from FLEX to OS-9 formats, and several other utilities unique to D-FLEX. All of the applicable standard utilities normally included with standard GIMIX FLEX are also supplied.

Most commonly available software designed to run with other versions of FLEX can be run under D-FLEX, although some software may require minor modification. The patches necessary to run Technical Systems Consultants Extended BASIC (XBASIC) as well as information on patching other software for use with D-FLEX are included. NOTE: Interrupt driven software and software that directly accesses hardware other than memory (memory management hardware, memory-mapped video, EPROM programmers, etc.) will NOT work with D-FLEX.

D-FLEX will run on both OS-9 GMI II and GMI III systems and requires 64K of free RAM for each D-FLEX user. Although primarily intended for hard disk systems, D-FLEX can also be used on floppy-only systems. Note: While D-FLEX can read and write FLEX format floppy disks, a stand-alone FLEX system is required in order to format disks.

GIMIX D-FLEX \$250.00
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GIMIX will again be exhibiting at NCC on July 9 - 12, in Las Vegas. Our booth numbers are B4016/18. We look forward to seeing 6809 users and showing them our latest state-of-the-art products.

After convention hours, we will have our informative, fun, creative 6809 Mini convention, in the GIMIX Hospitality Suite at the Las Vegas Hilton.

Dear Sir:

I constantly read in computer magazines that "X number of sales have been lost because of copying." Has anyone ever considered how many sales have been made because of copying. A lot of programs are just too expensive for many hobbyists to afford unless they go in with a few friends- its either copy or do without. And the problem is worse on the S-50 bus than for people with IBM'S or Apples; they have a large number of public domain and "el cheapo" programs to work with. We start out paying four or five hundred for one language and an operating system and then another good thousand for a few basic programs. That would be fine for a business where the programs are used every day- but \$250 for a program to use two or three hours a month?

I'm not advocating stealing- In fact I' spent a lot of time arguing for honesty with people who are copying music (where the problem is even worse). But both the software people and the music publishing houses are going to have to make some provision for the small operator who needs a good product at reasonable cost. I don't know what the solution is but I am quite certain a lot of copies aren't lost sales, they're programs that never would have been used otherwise. Thanks for the OS-9 articles in 68 Micro. The system is starting to make sense.

Robert J Otto
Box 60
Crystal ND 58222

HELP

Sirs;

I am interested in putting together a high-resolution color-graphics system based on a 64K or 128K home type computer for scientific purposes. Any ideas or information about your products you could send me would be appreciated. I would like to interface a video camera with the computer also. What will I need to do this?

Thanks for any help.
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Norman Mark

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- Disk-5 *DISKFIX 1, *DISKFIX 2, **LETTER, **LOVESIGN, **BLACKJAK, **BOWLING.
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- Disk-7 Linking Loader, Rload, Harkness
- Disk-8 Crtest, Lanpher (May 82)
- Disk-9 Datecopy, Diskfix9 (Aug 82)
- Disk-10 Home Accounting (July 82)
- Disk-11 Dissembler (June 84)
- Disk-12 Modem68 (May 84)
- Disk-13 *Intmf68, Testmf68, *Cleanup, *Dskalign, *Leobug, Help
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NOTE:

This is a reader service ONLY! No Warranty is offered or implied. The Disk Files are as received by '68' Micro Journal, and are for reader convenience ONLY (some MAY include fixes or patches). Also 6800 and 6809 programs are mixed, as each is fairly simple (mostly) to convert to the other.

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COMPILER EVALUATION SERVICES By: Ron Anderson

The S.E. MEDIA Division of Computer
Publishing Inc.
Is offering the following **SUBSCRIBER SERVICE**:

COMPILER COMPARISON AND EVALUATION REPORT

Due to the constant and rapid updating and enhancement of numerous compilers, and the different utility, appeal, speed, level of communication, memory usage, etc., of different compilers, the following services are now being offered with periodic updates.

This service, with updates, will allow you who are wary or confused by the various claims of compiler vendors, an opportunity to review comparisons, comments, benchmarks, etc., concerning the many different compilers on the market, for the 6809 microcomputer. Thus the savings could far offset the small cost of this service.

Many have purchased compilers and then discovered that the particular compiler purchased either is not the most efficient for their purposes or does not contain features necessary for their application. Thus the added expense of purchasing additional compiler(s) or not being able to fully utilize the advantages of high level language compilers becomes too expensive.

The following COMPILERS are reviewed initially, more will be reviewed, compared and benchmarked as they become available to the author:

PASCAL "C" GSPL WHIMISCAL PL/9

Initial Subscription - \$39.95
(Includes 1 year updates)
Updates for 1 year - \$14.50

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Introl's highly acclaimed 6809 C compilers and cross-compilers are now more powerful than ever!

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Resident compilers are available under **Uniflex, Flex and OS9.**

Cross-compilers are available for **PDP-11/UNIX** and **IBM PC/PC DOS** hosts.

Trademarks:

Introl-C, Introl Corporation
Flex and Uniflex, Technical Systems Consultants
OS9, Microware Systems
PDP-11, Digital Equipment Corp.
UNIX, Bell Laboratories
IBM PC, International Business Machines

For further information, please call or write.

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CORPORATION

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HELIX...OS9/68K...NOW!

HAZELWOOD COMPUTER SYSTEMS is pleased to announce the availability of several new products: the CP-08 68008 CPU board, the MC-20 Disk Controller, and OS9/68K(TM) for the HELIX(TM) computer system.

The CP-08 68008 CPU board is a standard S-50 plug-in capable of 4, 8, or 10MHz operation. Features include a 6840 Timer, OKI battery back-up real-time clock, Base and Limit Register, and the capability to co-reside on the same bus with the Hazelwood CP-09 6809 board. The CP-08 achieves high performance by use of a proprietary synchronization circuit to match the asynchronous timing of the 68008 to the S-50 bus.

The MC-20 Disk Controller Board is capable of controlling up to 4 floppy disk drives (either 5 1/4 or 8 inch in any combination) as well as any SASI compatible hard disk controller such as the XEBEC 1410. The MC-20 has its own 2MHz 6809 CPU on board with 56K RAM, up to 6K EPROM, a 6840 Timer, and a 6850 ACIA for maintenance mode. All data as well as control/status transfers are via direct memory access (DMA).

The OS9/68K operating system is the 68000 version of the popular operating system by Microware Systems Corporation. Included with the operating system is a line editor, a screen editor, an assembler, and a debugger. A 'C' compiler is available at extra cost. Current 6809 OS9 users will appreciate the features of this system running on the HELIX including higher speed and larger work area as well as the ability to access 6809 OS9 disk files.

As a one-time, introductory offer, Hazelwood Computer Systems is offering the following package discounts to enable more users to move up to the computing power of OS9/68K. This sale ends June 30, 1984.

Package 1

CP-08 68008 CPU Board
MC-20 Disk Controller (695.00)
OS9/68K Operating Sys (250.00)

Total Retail Value \$1540.00
Sale Price \$995.00

Package 2

CP-08 68008 CPU Board (595.00)
MC-20 Disk Controller (695.00)
DM-256 256K Memory (995.00)
OS9/68K Operating Sys (250.00)
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Package 3

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OS9/68K Operating Sys (250.00)

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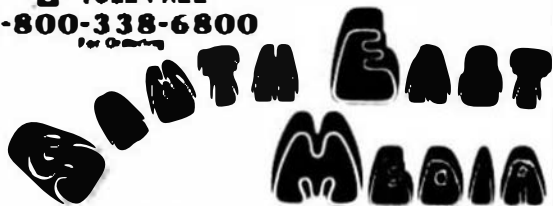
Order from:

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O'Fallon, MO 63366
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Or the authorized HELIX Dealer in your area.

OS9 and OS9/68K are registered trademarks of Microware Systems Corp.
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CoCo OS-9™ FLEX™
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ASSEMBLERS

Southeast Media

ASTRUK09

A "Structured Assembler for the 6809" which requires the TSC Macro Assembler. Allows direct use of structured statements such as IF, ELSE, DO, REPEAT, etc., and provides indented level formatting of the listing so that the structure is apparent. Re. '68' Micro Journal, Sept. '83 (program was called "STASMO9"; has been renamed due to conflicts).

A User reports

"... I'm very pleased and am now writing almost exclusively in (ASTRUK09). I've selected it over --- for all future systems development... As (one) of my early evaluations, I rewrote a rather elaborate routine originally done in assembly. Out of the 1000 bytes of code generated, the (ASTRUK09) version used only 20 more bytes than the original. --- could not andie this program since it uses triple-precision fixed point arithmetic... I have a large body of code already written t at is incompatible with --- constructs. No problem with (ASTRUK09) and the structure sure helps in understanding the logic!"

F, CCF - \$99.95

TSC

Macro Assembler

The FLEX STANDARD Assembler. F,CCF \$50.00
Relocating Assembler w/Linking Loader F,CCF \$150.00
Use with many of the C and Pascal Compilers.

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Relocating, Recursive-Macro Assembler and Linking Loader.
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FRALLI

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Windrush Micro Systems

RACE

by Graham Trott.

F,CCF - \$98.00

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An "easy to use", powerful Disassembler for Disk Resident 6809 and 6800 Binary Files. Allows the development of a "Control File" of various Program "Boundaries" during successive disassemblies; can use a Label File which automatically replaces a Hex location with a Label Name; includes an XREF Utility; etc. Label Files provided for Mini-FLEX, FLEX2, FLEX9, Color Computer (for use with Color FLEX Systems), etc. OS-9 Version includes special OS-9 options.

CCF, Obj. Only \$100.00
CCO, " " \$150.00
F, " " \$100.00
O, " " \$150.00
U, " " \$200.00

COMPILERS & DECOMPILERS

6809 "Structured" Assembly Lang. Compilers

Microbus Micro Systems

FL/9

By Graham Trott. A "Structured" Assembly Language Editor/Compiler/Debugger, all in ONE PACKAGE: provides a totally INTERACTIVE Program Development Cycle. The Compiler supports large Symbol Names, Variable Types, Pointers, Control Structures, Stack, A-, B-, and D-Register manipulation, etc. The Source-Oriented Trace/Debugger provides Single Stepping, Breakpointing, etc. An excellent Software Development Tool for utilizing the power of the 6809 in developing small to medium sized packages.

F, CCF - \$198.00

Whitnical Developments

MEDICAL

Need the Ease of Design and Maintainability of "Structured programming" AND the Speed and Control of Assembly Language? Then MEDICAL was designed for you! This Single Pass, Recursive Descent Compiler provides the tool for developing simple Utilities to MAJOR Systems in Assembly Language. Supports 3 "Lex" Levels which allow one level of Procedure nesting, or more within "Modules". It is easy to develop programs written for other machines since you are working at the Assembly Language level. Features unified, user-defined I/O; produces relocable, recursive, re-entrant Code; Structured style and statements with Procedures and Modules; supports Byte and Double-Byte primitives with 3 types of Integers (up to 32 bit), Char and Boolean, and unlimited sized Arrays (vectors only); Interrupt handling; unlimited length Variable Names; Variable Initialization (defaults to \$00); Include "Source File" directive; Conditional compiling; direct Code insertion; control of the Stack Pointer; etc. To quote Ron Anderson in his review of MEDICAL in the Sept. '83 issue of '68' Micro Journal that, except for the lack of floats, "... I have to give this one VERY high rating. ...". It is a FAST Compiler which produces FAST Code (his "Primes" Benchmark ran at 9 secs. on a 2 Mhz System).

F and CCF - \$195.00

C Compilers

Microbus Micro Systems

C Compiler

By James McCosh. Full featured C Compiler for the FLEX Operating System. Includes a Reloc. Amb., but needs the TSC Relocating Assembler/Linking Loader (which includes a Library Manager) for those "full blown" System Packages.

F and CCF - \$295.00

Introl

C Compiler

A full-featured C, streamlined for the 6809. Generates very efficient object code. Output "benchmarks" close to 1800x 68000 in 8 Bit Operations; 1.5 times faster than a 4 Mhz Z80 when using a 2Mhz 6809 System (Re. p 43, '68' Micro Journal, May '83). Floats, etc.

F, CCF, and O - \$375.00
U - \$425.00
One Year Maint. - \$100.00



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*OS9 is a trademark of Microware

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CCO = Color Computer Disk
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DIET-TRAC Forecaster

DIET-TRAC Forecaster is an X BASIC program that plans a diet in terms of either calories and percentage of carbohydrates, proteins and fats (C P G) or grams of Carbohydrate, Protein and Fat food exchanges of each of the six basic food groups (vegetable, bread, meat, skim milk, fruit and fat) for a specific individual.

Sex, Age, Height, Present Weight, Frame Size, Activity Level and Basal Metabolic Rate for no mal individual are taken into account. Ideal weight and sustaining calories for any weight of the above individual are calculated. When a weight goal is given (either gain or loss), and a calorie plan is agreed upon between the computer and the individual, the number of days to reach the weight goal is projected. The starting and ending rate of weight loss is calculated, and a daily calendar with each day's weight for a 30-day period is printed.

F - \$59.95
 U - \$89.95

Southeast Media

XDATA

A COMMUNICATION Package

for the UniFLEX Operating System

Allows UniFLEX Based Systems to Transmit and Receive files to and from other Computer Systems via Modem. Use with CP/M, Main Frames, other UniFLEX Systems, etc.

- Verifies Transmission Integrity using checksum or CRC
- Automatically Re-Transmits bad blocks
- Transmits data in 128 byte blocks

U - \$299.99

Southeast Media

JUST

Text Formatter

JUST, a Text Formatter developed by Ron Anderson, provides numerous features which make it a valuable addition to any FLEX Users Software Library. JUST is designed for formatting Text Output for Dot Matrix Printers and provides many unique features:

- Output the "Formatted" Text to the Display for format analysis and change.
- Output the "Formatted" Text to a Text File for use with the supplied FPRINT.COM for producing multiple copies of the Text on the Printer INCLUDING IMBEDDED PRINTER COMMANDS (this utility is very useful at other times also, and worth the price of the program by itself).
- "User Configurable" for adapting to other Printers (comes set up for Epson MX-80 with Graftrax); provides for up to ten (10) imbedded "Printer Control Commands", such as Italics on and off, boldface on and off, etc.
- Automatic compensation for a "Double Width" printed line.
- Includes the normal line width, margin, indent, paragraph, space, vertical skip lines, page length, page numbering, centering, fill, justification, etc.
- Use with ANY Editor.
- Supplied with "Structured Source" (Windrush PL/9); easy to see the flow of the program.

F and CCF - \$49.95

Lucidata

PASCAL UTILITIES

Requires LUCIDATA Pascal ver 3.

XREF -- produce a Cross Reference Listing of any text; oriented to Pascal Source.

F and CCF - \$25.00

INCLUDE -- allows the inclusion of other Files in a Source Text; has unlimited nesting capabilities. Also allows Binary File inclusions.

F and CCF - \$25.00

PROFILER -- produces an Indented, Numbered, "Structogram" of a Pascal Source Text File. Allows viewing the overall structure of large programs, and provides clues as to the integrity of the program. Supplied as Source Code; requires compilation.

F and CCF - \$25.00

Lucidata

COPYCAT

Pascal NOT required

Allows reading TSC Mini-FLEX, SS8 DOS68, and Digital Research CP/M Disks while operating under FLEX 1.0, FLEX 2.0, or FLEX 9.0 with 6800 or 6809 Systems. COPYCAT will not perform Miracles, but, between the program and the manual, you stand a good chance of accomplishing a transfer. Includes Utilities to List Directories, Copy Files, and convert Text Files when required. Also includes a Utility for Investigating Physical Compatibility problems. Programs supplied in Modular Source Code (Assembly Language) to make it easier to solve unusual problems.

F and CCF 5" - \$50.00
 F 8" - \$65.00

Computer Systems Consultants

FLEX DISK UTILITIES

Eight (8) different FLEX Utilities that should be a part of every FLEX Users Toolbox; Assembly Language (Source Code):

Copy a File with CRC Errors, so it can possibly be salvaged; Test Disk for errors; Compare two Disks; a fast Disk Backup Program; Edit Disk Sectors; Linearize Free-Chain on the Disk; print Disk Identification; and Sort and Replace the Disk Directory (in sorted order).

F and CCF - \$50.00

WORD PROCESSORS

Alford and Associates

SCREDITOR III

EXTREMELY Powerful Screen-Oriented Editor/Word Processor. Almost 50 different commands; EXCELLENT Documentation (over 300 pages), including a full Tutorial Section to help you learn how to use the system. Features Cursor-based editing, dynamic Screen Formatting (what you see is what you get), Multi-Column display and editing, "decimal align" columns (AND add them up automatically, if wanted), define multiple keystroke macros, even add page number headers and footers, imbed printer control codes in text, full justification series of commands, full "help" support, store common command series on disk for future use, etc. Easy "Set-Up" (for example, you just hit the key you want to use for a specific function, such as "cursor up", and the System reads an stores that key - no digging into tech manuals for codes, etc.); use supplied "set-ups", or remap the keyboard to what you are used too. Except for proportional printing, this package will DO IT ALL!

6800 or 6809 FLEX or SS8 OOS, OS-9 - \$175.00

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STYLOGRAPH

A full-screen oriented WORD PROCESSOR -- (now runs on the Data-Comp and FHL Color FLEX Systems; uses the 51 x 24 Display Screens). Full screen display and editing (i.e., what you see is what you get); supports the Daisy Wheel proportional printers.

SPECIAL CCF - \$195.00

F and O - \$295.00

U - \$395.00

SPELL

Fast Computer Dictionary.

F, CCF, OS/9 - \$125.00

U - \$175.00

MAIL MERGE

Greatly extends the power and flexibility of STYLOGRAPH.

F, CCF, O - \$145.00

U - \$195.00



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Great Plains Computer Co.

MAIL MERGE

Greatly extends the power and flexibility of **STELLA**. Allows Multiple Text files to be printed out as one large document. Provides for merging information into the Text File during printing (such as different names and addresses), etc.

F, CCF, O - \$145.00
U - \$195.00

Southeast Media

SPELLB "Computer Dictionary"

OVER 120,000 words!

No more "Let your fingers do the walking through the Dictionary" while you are entering Text with your favorite Editor or Word Processor. **SPELLB** is more than just "another Spelling Checker"; it allows you to look up a word from within your Editor or Word Processor so that you **KNOW** it is right **WHEN YOU TYPE IT IN** with the **SPN.CMD** Utility (which operates in the **FLEX** Utility Space). Yes, it **ALSO** allows you to check and update the Text after you are finished; along with allowing you to **ADD WORDS** to the Dictionary, "Flag" questionable words in the Text for evaluation later, "View a word in context" before changing or ignoring, etc. **SPELLB** first checks a "Common Word Dictionary", then the normal Dictionary, then a "Personal Word List", and finally, any "Special Word List" you may have specified. **SPELLB** also allows the use of **Small Disk Storage** systems.

F and CCF - \$129.95

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SPELL

Post Computer Dictionary -- allows directly changing the Text File, adding words to the dictionary, etc. 75,000 words in less than 400 seconds.

F, CCF, OS/9 - \$125.00
U - \$175.00

IBM BASIC MANAGEMENT SYSTEMS

IBM Applied Business Systems

XIMS

Possibly one of the most powerful Database Management Systems available, this machine language program is small enough to operate on a **single sided 5" disk**, yet provides the speed of M.L. and power limited only by the user's imagination. This **IMS** supports Relational, Sequential, Hierarchical, and Random Access File Structures, and has Virtual Memory capabilities for those Giant Data Bases. **XIMS Level I** provides a functional "entry level" System which provides for defining a Data Base, entering and changing the Data, and producing Reports. **XIMS Level II** adds the **POWERFUL "GENERATE"** facility which uses an English Language Command Structure in manipulating the Data to create new File Structures, Sort, Select, Calculate, etc. **XIMS Level III** adds several special "Utilities" which provide additional ease of working with the various structures, changing System Parameters, etc.

XIMS Level I - F & CCF - \$129.95
XIMS Level II - F & CCF - \$199.95
XIMS Level III - F & CCF - \$269.95
XIMS System Manual only - \$24.95

Great Plains Computer Co.

INFOLOG DBMS

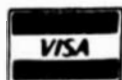
An **XBASIC**, Menu Driven, **DBMS** with "Built-In" Audit Tracking, Extremely Powerful Report & Format Capabilities, etc. This **INFOLOG DBMS** will become the "Work Horse" of your Software Stable.

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U - \$375.00

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Ten BASIC Programs to:

A **BASIC Resequencer** with **EXTRAS** over "RENUM"; works with ALL Versions of **FLEX BASIC** AND the Precompiler, checks for missing label definitions, processes Disk to Disk instead of in Memory.

Compare, Merge, or Generate Updates between two BASIC Programs, check **BASIC Sequence Numbers**, compare two unsequenced files, and 5 Programs for establishing a Master Directory of several Disks, and sorting, selecting, updating, and printing paginated listings of these files.

A **BASIC Cross-Reference Program**, written in Assembly Language, which provides an X-Ref Listing of the Variables and Reserved Words in **TSC BASIC**, **XBASIC**, and **PRECOMPILER BASIC** Programs. ALL Utilities include **SOURCE** (either BASIC or Source Code). An **EXCELLENT** Value!

F and CCF - \$25.00
UnifLEX - \$50.00

Computer Systems Consultants

FULL SCREEN INVENTORY/HRP

The Full Screen Inventory System provides a means of maintaining small inventories. Using a linked, keyed random file structure based upon the item field, it keeps the file in alphabetical order for easier inquiry. With the **FIND** command, the user may locate and/or print all records matching on partial or complete item, description, vendor, or attributes. Items in backorder or below minimum stock levels may be located and/or printed thru the same process. Printed output may be produced in item or vendor order. A materials requirement planning (MRP) capability for manufacturing environments is included to allow the maintenance and analysis of Hierarchical assemblies of items in the inventory file. It requires **TSC's Extended BASIC**.

F and CCF - \$100.00, U - \$150.00

ALSO AVAILABLE

The Virginia Company

Bizpack

BIZPACK is used for storing accounting, numeric, and financial data which can then be used for planning, budgeting, forecasting, analyzing, etc. While "Electronic Spreadsheets" are extremely useful in many situations, **BIZPACK** excels in businesses where there are numerous expense columns, revenue sources, significant business indicators, large numbers, erratic week-to-week and month-to-month fluctuations, etc. **BIZPACK** helps determine statistical relationships, establish trend lines, "smooths" data via moving averages, analyze seasonal data, adjusts for inflation, lags data in Statistics or Column functions, plots data, etc. **BIZPACK** is oriented toward time series analysis of businesses. The Program displays information on the screen in Columns of Information with each Row conforming to a defined Period of Time (weeks, months, years, etc.), and is very easy to use (data is easy to enter, change, and modify; commands can be renamed to suit the users requirements; unlimited ability to create specialized commands using common BASIC Statements; etc.). Requires **TSC's Extended BASIC**.

F and CCF - \$135.00
with Source - \$250.00

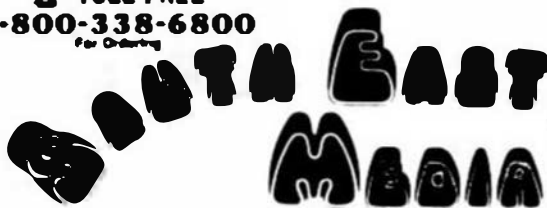
SPECIAL

Purchase **XBASIC** and **BIZPACK** together for \$221.50
-- a Savings of \$13.50 --

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U = UnifLEX
CDD = Color Computer Disk
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— a Savings of \$13.50 —

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TABULA RASA SPREADSHEET

TABULA RASA is similar to DESKTOP/PLAN and provides for the generation and maintenance of tabular computation schemes often used for analysis of business, sales, and economic scenarios. Its menu-driven user interface provides these capabilities even to those users with no programming experience. Its extensive report-generation capabilities allow the user to generate professional results with minimum effort. It requires TSC's Extended BASIC.

F and CCF - \$100.00, U - \$125.00

Computer Systems Center

DYNACALC

THE Electronic Spread Sheet for 6809 Computer Systems. An extremely POWERFUL Business Tool, this Program will find an unlimited number of "non-business" applications, also (for example, a Full Junior College Electronics Curriculum was set up using DYNACALC). Advanced features like "Table Lookup" make Income Tax work easy; Column or Row Sorting for numerous applications; etc. Completely "Memory Resident", Machine Language, this Program is FAST. Provides STANDARD FLEX Text File output for use with BASIC, Word Processors, Pascal, "C", etc. Also available for Data-Comp and FHL FLEX systems using the 50 x 24 Displays.

F and SPECIAL CCF - \$200.00
U - \$395.00

ODDS & ENDS

Computer Systems Consultants

FULL SCREEN FORMS DISPLAY

This package supports any Serial Terminal with cursor control of Memory-Mapped Video Displays. The package substantially extends the screen input/output capabilities of TSC's Extended BASIC programs by providing a simple, table-driven method of describing and using full screen displays. These table entries are easy to set up and maintain, and are normally stored on disk and read as required. A simple, interactive means of generating the forms and the data field definitions is provided.

F and CCF - \$50.00, U - \$75.00

Computer Systems Consultants

FULL SCREEN MAILING LIST

The Full Screen Mailing List System provides a means of maintaining simple mailing lists. Using a random fill structure based on the first character of the name field, it maintains the file in alphabetical order for easier inquiry. With the FIND command, the user may locate all records matching on partial or complete name, city, state, zip, or attributes. Printed listings and output to labels may also be produced on the same selective basis. It requires TSC's Extended BASIC.

F and CCF - \$100.00, U - \$110.00

COLOR COMPUTER SOFTWARE

Stearns Electronics

FORTH

Intrigued by FORTH?? Here is a FORTH package tailored to the Color Computer! This package is supplied on Tape, with instructions for transferring it to disk if you wish. Written primarily in machine language, it's speed is unparalleled. A full Semigraphic-8 Editor is provided, along with "goodies" like Graphics and Sound Commands. Printer Commands, Auto-Repeat and Control Keys, etc. If you are interested in learning FORTH, a Trace Feature is provided which is invaluable. If you are a FORTH Pro, this package provides CPU carry Flag accessibility, Fast Task Multiplexing, Clean Interrupt Handling, etc. (Or; you won't "out grow" the basic capabilities of this implementation). Combine this package with Leo Brodie's EXCELLENT Book "Starting FORTH", and you will be a FORTH Expert before you know it (and have a lot of fun doing it!).

Color Computer TAPE - \$58.95

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F = FLEX, CCF = Color Computer FLEX
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For the past several months, we at the **Southeast Media Division of Computer Publishing, Inc. (CPI)**, the parent company of '68' **MICRO JOURNAL** and **COLOR MICRO JOURNAL**, have debated expanding our software distribution business. Many other magazines have been doing so for years (in fact, **MOST** were in the Software Distribution Business **BEFORE** they began to publish a Magazine). Presently there are many fine examples of software that has been developed by **YOU**, our readers, that will never see the "light of day" due to the **Cost of Advertising** and **TIME** and **Cost** involved in the production, distribution, and **CUSTOMER SUPPORT** of that software unless **SOMEONE**, with enough exposure and the willingness to continually advertise, runs with the ball.

Software is the "backbone" for the **REAL** utilization of any Computer System, and ours are no exception! This has been no simple decision. While we realize that there could be some conflict with some of our advertisers, we **ALSO** hear a **LOUD** and **CONTINUOUS** cry for **HELP** from our Readers. From day one, the **foremost** concern of '68' **MICRO JOURNAL** has been it's **READERS!** Therefore, our **Southeast Media Division** will accept, for appraisal for possible Distribution, **6809** software; Games, Utilities, Software Development, Business Application Programs, etc.



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In some instances the program may be small or short and not justify itself as a "single" sale product. In this event it will be combined with other like programs, and offered as a package. In that event, the royalties will be split between the various authors.

If you have software that you feel will qualify under this program, please contact one of the people below. Remember, if your software has any problems or "funnies" -- **GET IT STRAIGHT BEFORE YOU CONTACT US!** Also get your source code in proper shape and well commented; there is too much 99% code already drifting around.

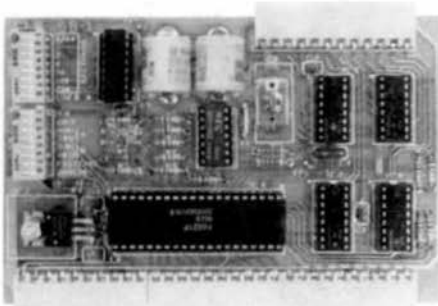
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CLK68-1

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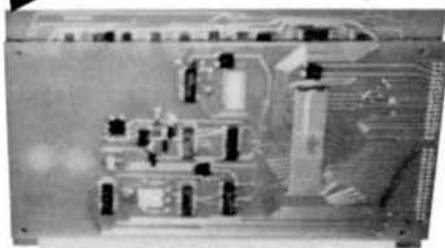
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	A	B	C	D	E	F
INTERFACE	PAR	PAR	SER	S30	SER	SER
INTELLIGENT	NO	NO	YES	NO	YES	YES
PROGRAMS						
2704*		•				•
2508		•				•
2708*		•				•
2758	•	•	•	•	•	•
2516	•	•	•	•	•	•
2716	•	•	•	•	•	•
2716*	•	•	•	•	•	•
2532	•	•	•	•	•	•
2732	•	•	•	•	•	•
2732A	•	•	•	•	•	•
2564	•	•	•	•	•	•
2784	•	•	•	•	•	•
2528	•	•	•	•	•	•
27128	•	•	•	•	•	•
2616						•
68764		•			•	
8748					•	
8749					•	
TOTAL	3	12	6	11	11	11
PRICE	\$45*	\$169	\$289	\$375	\$489	\$575

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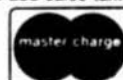
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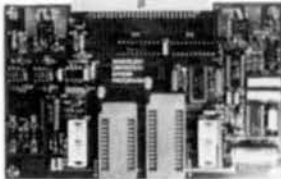
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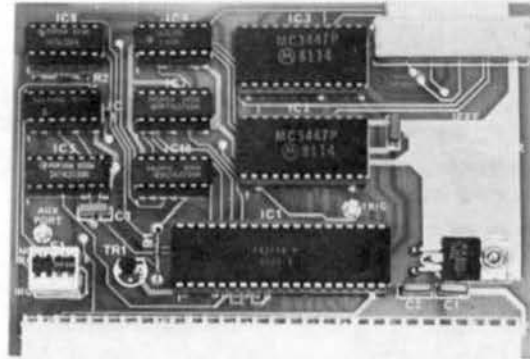
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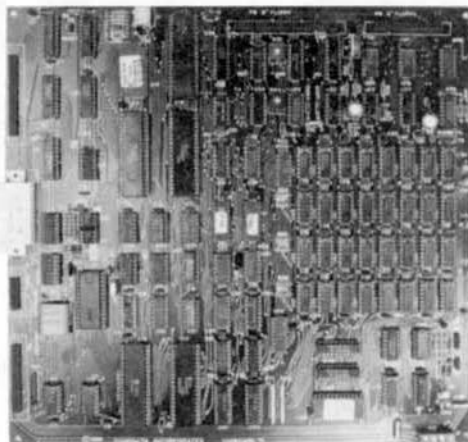
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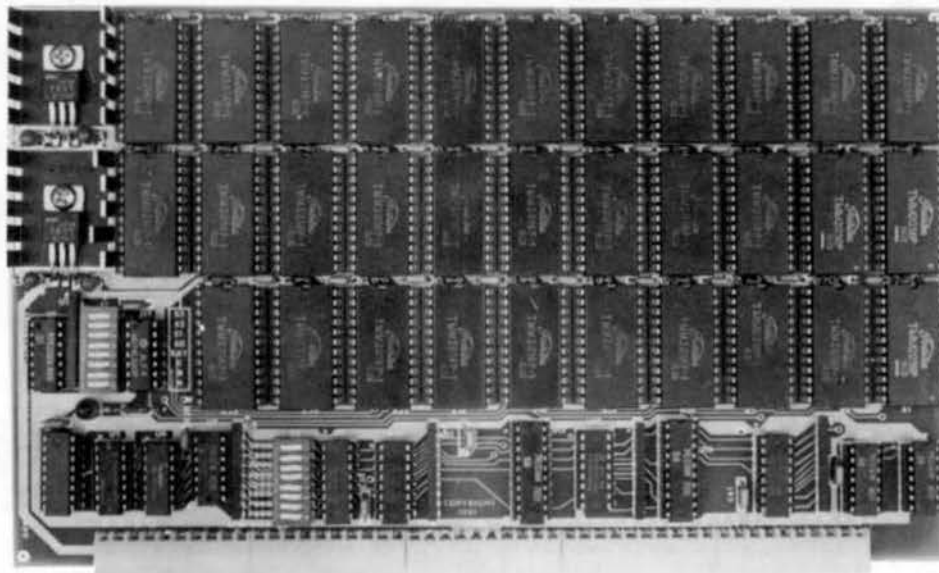
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Color Micro Journal

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\$1.95 per issue Vol. 1, Issue 2 October, 1983

THIS 'N THAT

The **BIG NEWS** this month is that **OS-9** has finally arrived for the Color Computer. The **ASTOUNDING** part of the Radio Shack OS-9 Package, besides the price, is the **documentation**. You 'Old Time Radio Shack Followers' will not believe what you see. Jon Shirley has been telling us that the main reason for the "lack" of documentation with a lot of their products was the restrictions placed on releasing that information by **Microsoft**. I

One of the "Operating Systems of the Future" is **now available** for the "little old Color Computer"; **OS-9**. Freely translated, **OS-9** means "Operating System for the 6809" (**OS-9** is now being written for the **68000**, also). Since it is fairly obvious that **UNIX** and "UNIX-Type" Operating Systems will be running on just about every computer to come out in the next few years, a whole new language is beginning to appear on the horizon.

Color Computer OS-9; the Package

We had been running a preliminary release of **OS-9** on the Color Computer for a few weeks, and received the "Official Radio Shack" version for Review a couple of days ago. To put it mildly, this package is **IMPRESSIVE**. For \$69.95 (Radio Shack Catalog Number **26-3330**), you receive a 9 1/2" x 7 5/8" x 2" package containing 4

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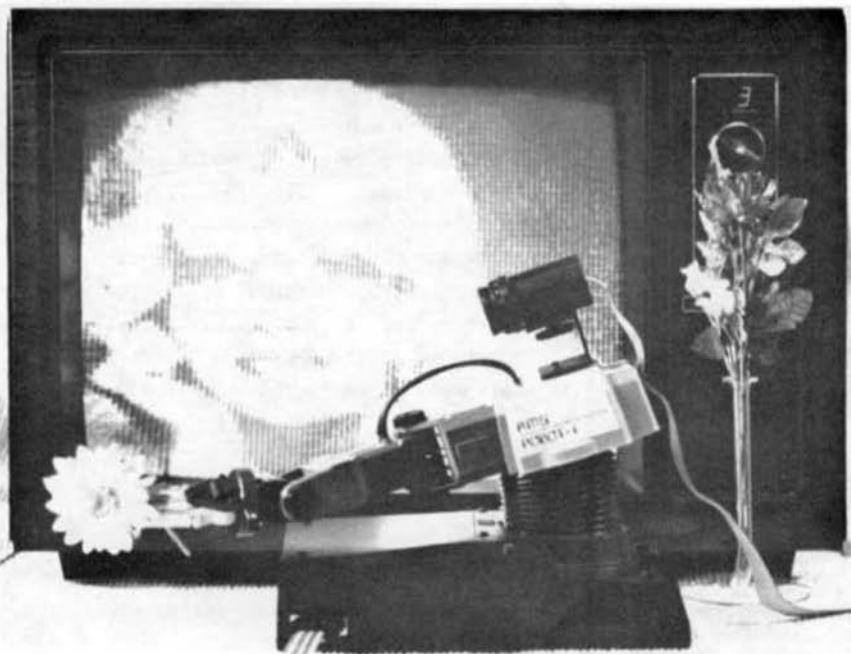
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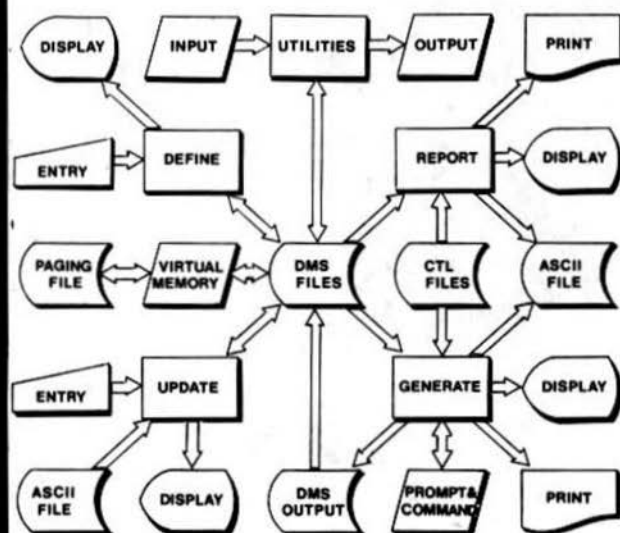
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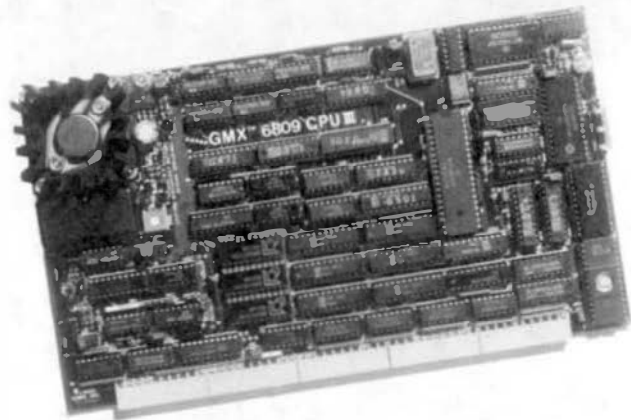
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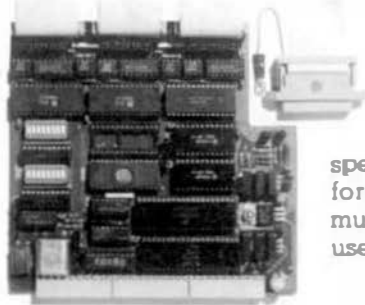
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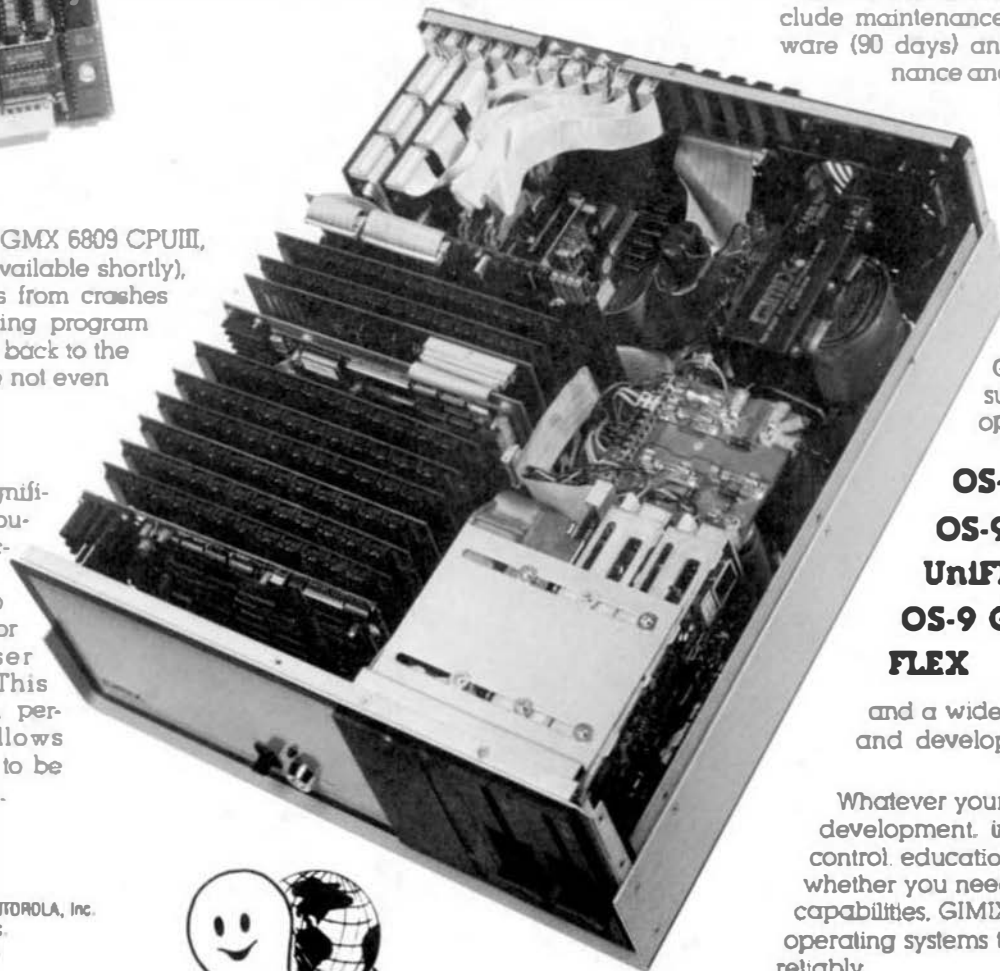
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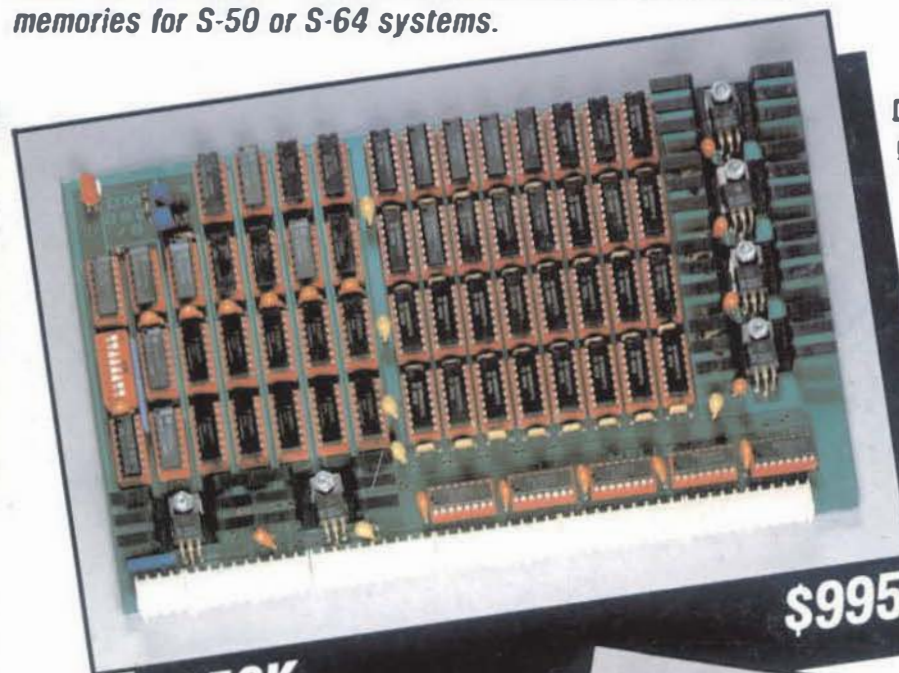
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